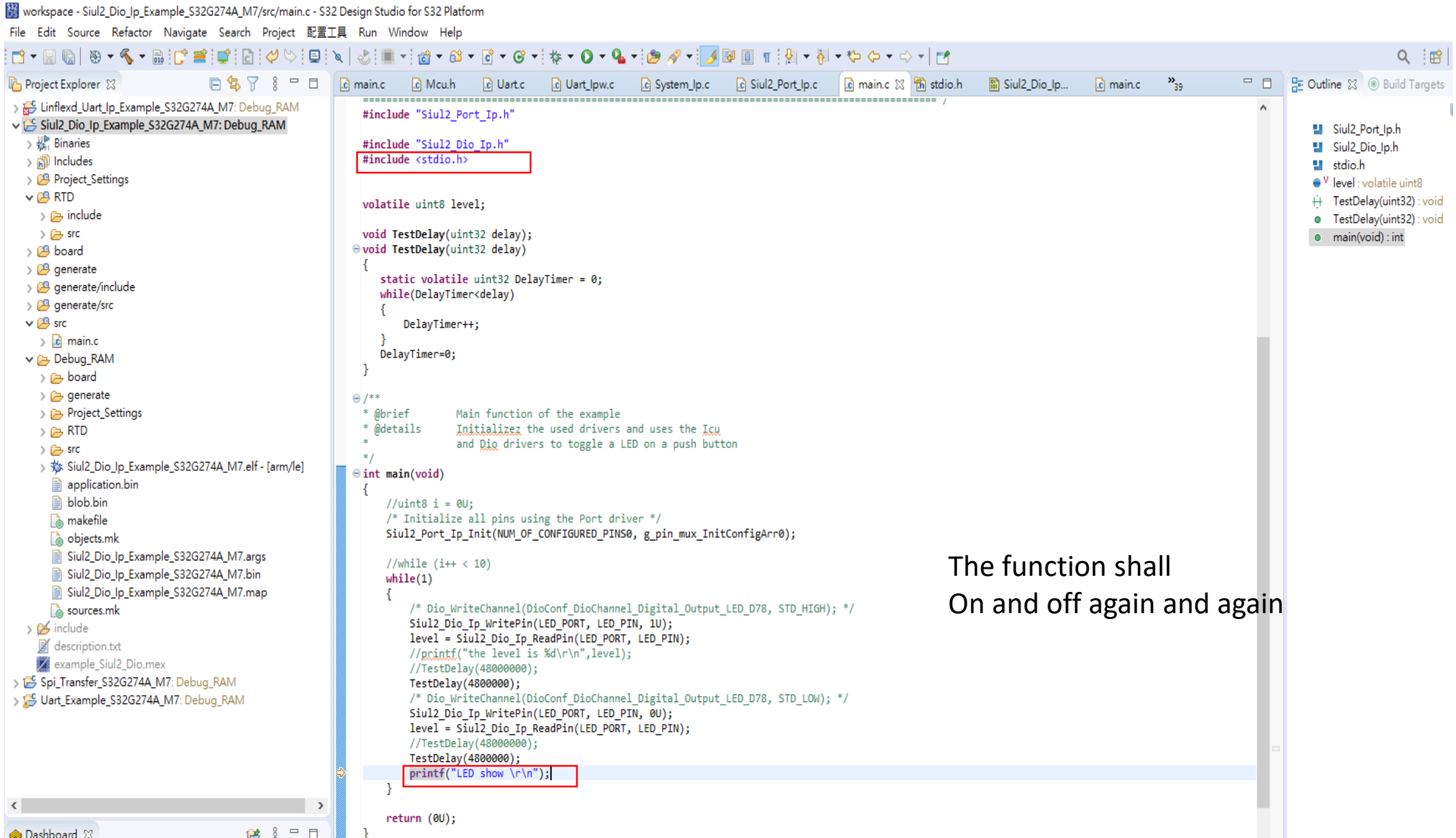
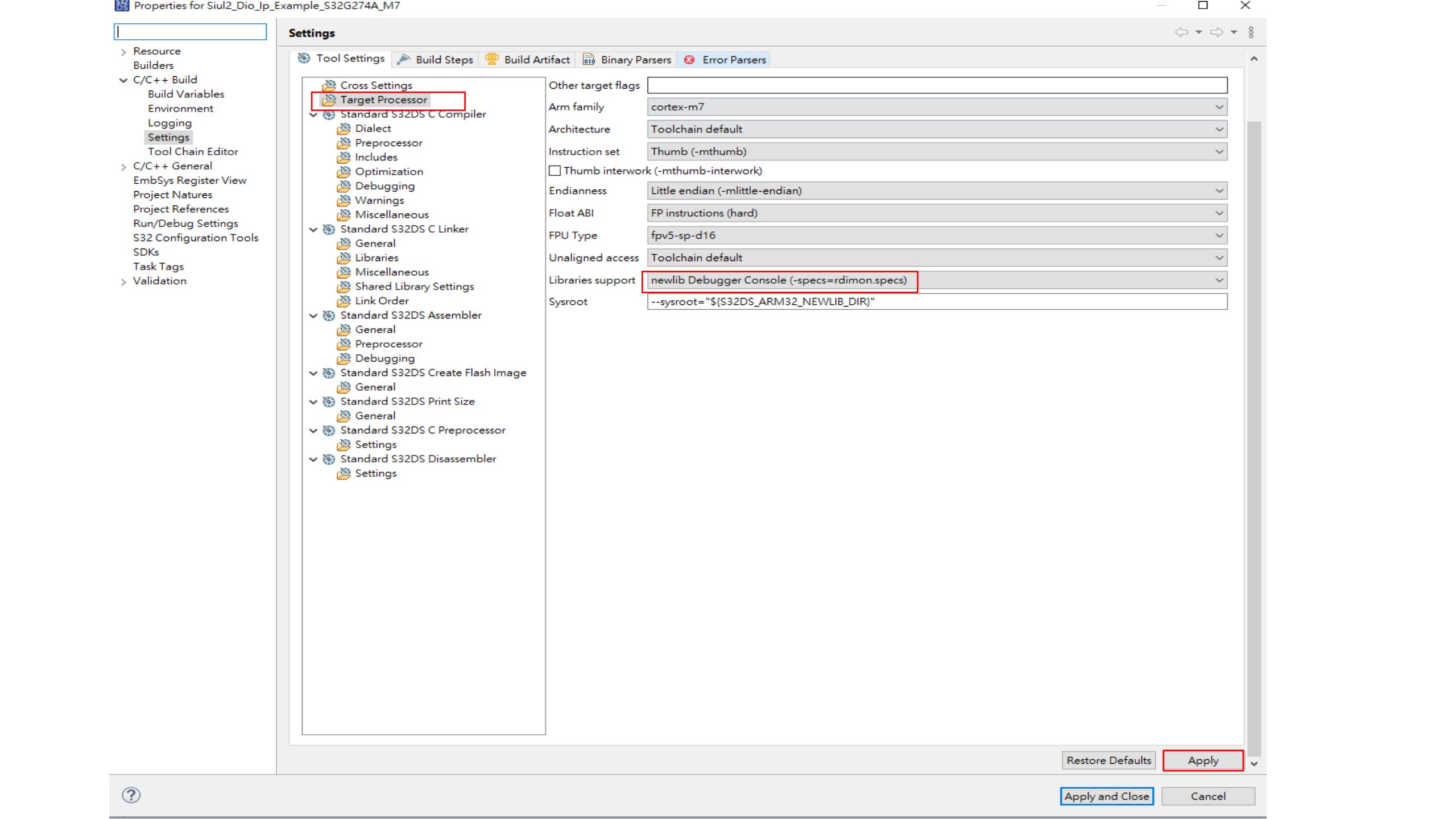
Sample :
Siul2_Dio_lp_Example_S32G274A_M7
printf() to console can’t work

- sample setting
- Test result

sample setting



The function shall
On and off again and again


Properties for Siul2_Dio_Ip_Example_S32G274A_M7
Resource
Builders
C/C++ Build
Build Variables
Environment
Logging
Settings
Tool Chain Editor
C/C++ General
EmbSys Register View
Project Natures
Project References
Run/Debug Settings
S32 Configuration Tools
SDKs
Task Tags
Validation
Settings
Tool Settings
Build Steps
Build Artifact
Binary Parsers
Error Parsers
Cross Settings
Target Processor
Standard S32DS C Compiler
Dialect
Preprocessor
Includes
Optimization
Debugging
Warnings
Miscellaneous
Standard S32DS C Linker
General
Libraries
Miscellaneous
Shared Library Settings
Link Order
Standard S32DS Assembler
General
Preprocessor
Debugging
Standard S32DS Create Flash Image
General
Standard S32DS Print Size
General
Standard S32DS C Preprocessor
Settings
Standard S32DS Disassembler
Settings
Other target flags
Arm family
cortex-m7
Architecture
Toolchain default
Instruction set
Thumb (-mthumb)
Thumb interwork (-mthumb-interwork)
Endianness
Little endian (-mlittle-endian)
Float ABI
FP instructions (hard)
FPU Type
fpv5-sp-d16
Unaligned access
Toolchain default
Libraries support
newlib Debugger Console (-specs=rdimon.specs)
Sysroot
--sysroot="${S32DS_ARM32_NEWLIB_DIR}"
Restore Defaults
Apply
Apply and Close
Cancel

Debug Configurations

Create, manage, and run configurations

type filter text

- C/C++ Application
  - Siul2_Dio_Ip_Example_S32G274A_M7.elf
- C/C++ Remote Application
- Eclipse Application
- GDB Hardware Debugging
- GDB PEMicro Interface Debugging
  - Siul2_Dio_Ip_Example_S32G274A_M7 Debug_RAM
- GDB SEGGER J-Link Debugging
- Launch Group
- Launch Group (Deprecated)
- Launch Group for S32 Debugger
- LAX Simulator
- S32 Debugger
  - Linflexd_Uart_Ip_Example_S32G274A_M7_Debug_RAM_S32Debug
  - Siul2_Dio_Ip_Example_S32G274A_M7_Debug_RAM_S32Debug
  - Spi_Transfer_S32G274A_M7_Debug_RAM_S32Debug
  - Uart_Example_S32G274A_M7_Debug_RAM_S32Debug
- S32 Debugger Flash Programmer
- VLAB Simulator Debugging

Filter matched 19 of 29 items

Name: Siul2_Dio_Ip_Example_S32G274A_M7 Debug_RAM

Main | PEmicro Debugger | Startup | Source | Common | SVD Support | OS Awareness

Semihosting Settings

- [x] Enable semihosting Console routed to: [x] Telnet [ ] GDB client
- [x] Enable Telnet console Telnet Port: 51794

Load Symbols and Executable

- [x] Load symbols
  - (•) Use project binary: Siul2_Dio_Ip_Example_S32G274A_M7.elf
  - ( ) Use file: Workspace... File System...
  - Symbols offset (hex):
- [x] Load executable
  - (•) Use project binary: Siul2_Dio_Ip_Example_S32G274A_M7.elf
  - ( ) Use file: Workspace... File System...
  - Executable offset (hex):

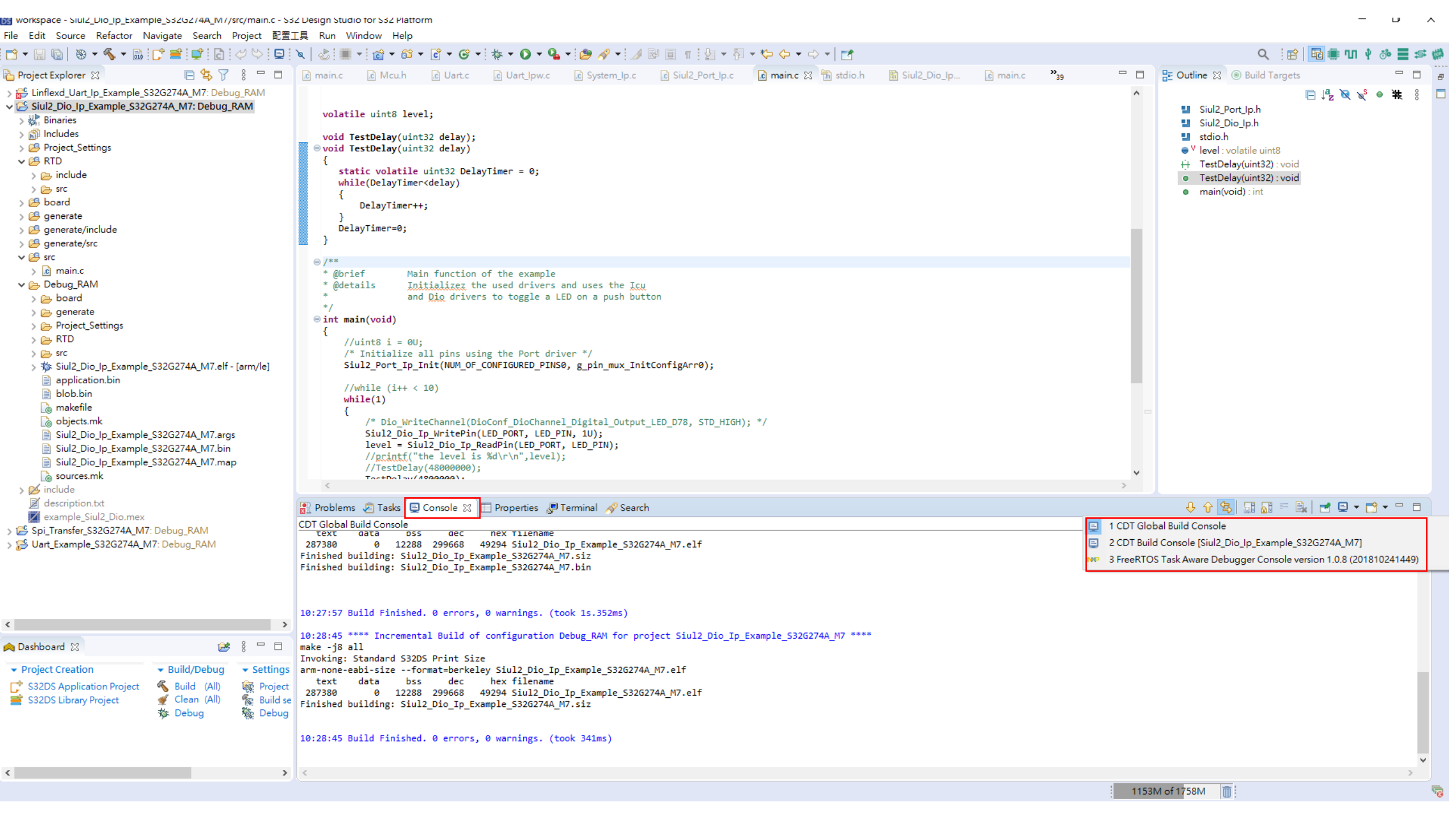
Runtime Options

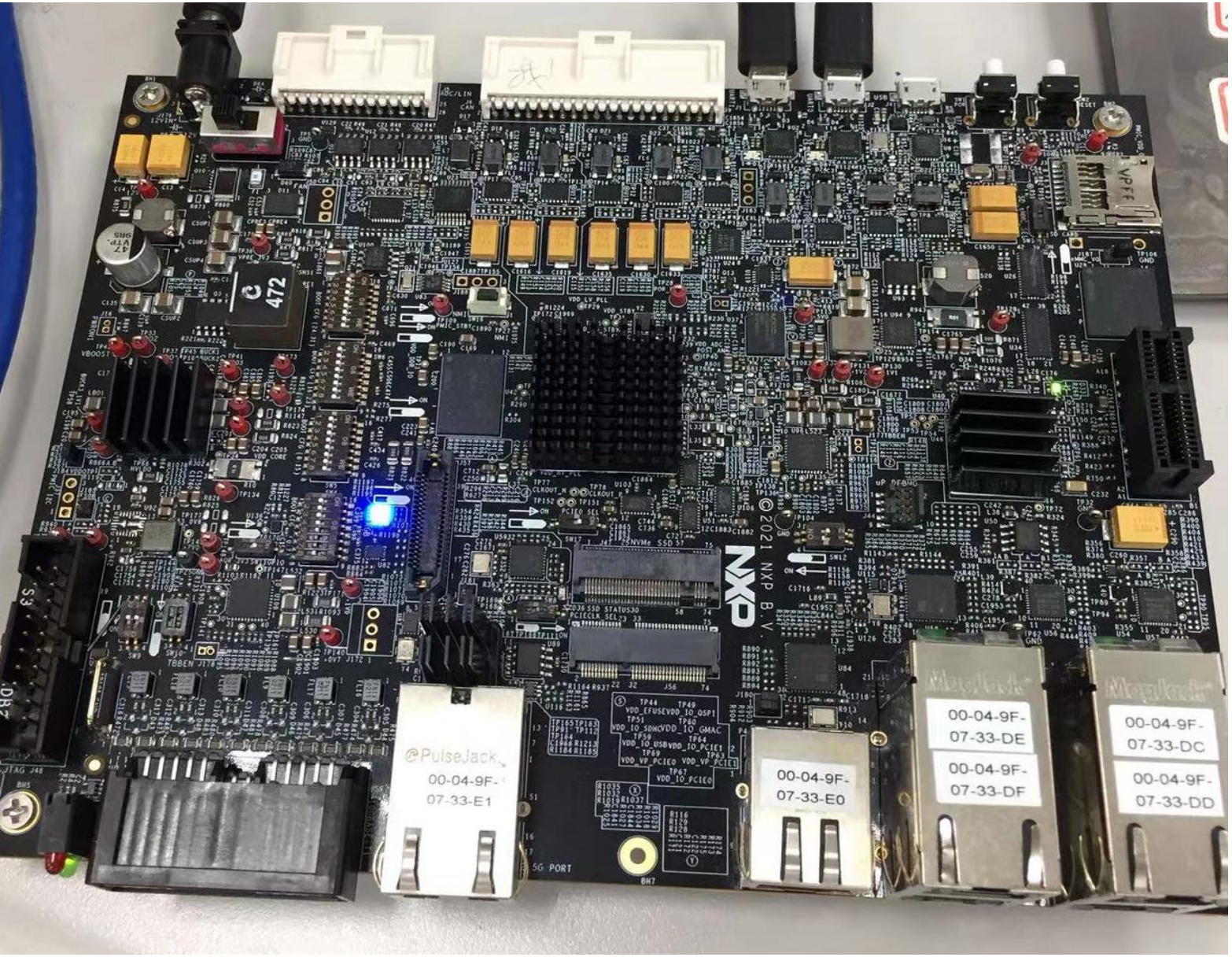
- [ ] Attach to Running Target [x] Run on reset
- [ ] Set PC (absolute hex address or symbol): [x] Set breakpoint at: main

GDB run commands:

Revert Apply

Debug Close


workspace - Siul2_Dio_Ip_Example_S32G274A_M7/src/main.c - S32 Design Studio for S32 Platform
File Edit Source Refactor Navigate Search Project 配置工具 Run Window Help
Project Explorer
Linflexd_Uart_Ip_Example_S32G274A_M7: Debug_RAM
Siul2_Dio_Ip_Example_S32G274A_M7: Debug_RAM
Binaries
Includes
Project_Settings
RTD
include
src
board
generate
generate/include
generate/src
src
main.c
Debug_RAM
board
generate
Project_Settings
RTD
src
Siul2_Dio_Ip_Example_S32G274A_M7.elf - [arm/le]
application.bin
blob.bin
makefile
objects.mk
Siul2_Dio_Ip_Example_S32G274A_M7.args
Siul2_Dio_Ip_Example_S32G274A_M7.bin
Siul2_Dio_Ip_Example_S32G274A_M7.map
sources.mk
include
description.txt
example_Siul2_Dio.mex
Spi_Transfer_S32G274A_M7: Debug_RAM
Uart_Example_S32G274A_M7: Debug_RAM
Dashboard
Project Creation
S32DS Application Project
S32DS Library Project
Build/Debug
Build (All)
Clean (All)
Debug
Settings
Project
Build se
Debug
main.c
Mcu.h
Uart.c
Uart_lpw.c
System_lp.c
Siul2_Port_lp.c
main.c
stdio.h
Siul2_Dio_Ip...
main.c
volatile uint8 level;
void TestDelay(uint32 delay);
void TestDelay(uint32 delay)
{
static volatile uint32 DelayTimer = 0;
while(DelayTimer<delay)
{
DelayTimer++;
}
DelayTimer=0;
}
/**
* @brief Main function of the example
* @details Initializez the used drivers and uses the Icu
* and Dio drivers to toggle a LED on a push button
*/
int main(void)
{
//uint8 i = 0U;
/* Initialize all pins using the Port driver */
Siul2_Port_Ip_Init(NUM_OF_CONFIGURED_PINS0, g_pin_mux_InitConfigArr0);
//while (i++ < 10)
while(1)
{
/* Dio_WriteChannel(DioConf_DioChannel_Digital_Output_LED_D78, STD_HIGH); */
Siul2_Dio_Ip_WritePin(LED_PORT, LED_PIN, 1U);
level = Siul2_Dio_Ip_ReadPin(LED_PORT, LED_PIN);
//printf("the level is %d\r\n",level);
//TestDelay(48000000);
Outline
Build Targets
Siul2_Port_Ip.h
Siul2_Dio_Ip.h
stdio.h
level : volatile uint8
TestDelay(uint32) : void
TestDelay(uint32) : void
main(void) : int
Problems
Tasks
Console
Properties
Terminal
Search
CDT Global Build Console
text data bss dec hex filename
287380 0 12288 299668 49294 Siul2_Dio_Ip_Example_S32G274A_M7.elf
Finished building: Siul2_Dio_Ip_Example_S32G274A_M7.siz
Finished building: Siul2_Dio_Ip_Example_S32G274A_M7.bin
10:27:57 Build Finished. 0 errors, 0 warnings. (took 1s.352ms)
10:28:45 **** Incremental Build of configuration Debug_RAM for project Siul2_Dio_Ip_Example_S32G274A_M7 ****
make -j8 all
Invoking: Standard S32DS Print Size
arm-none-eabi-size --format=berkeley Siul2_Dio_Ip_Example_S32G274A_M7.elf
text data bss dec hex filename
287380 0 12288 299668 49294 Siul2_Dio_Ip_Example_S32G274A_M7.elf
Finished building: Siul2_Dio_Ip_Example_S32G274A_M7.siz
10:28:45 Build Finished. 0 errors, 0 warnings. (took 341ms)
1 CDT Global Build Console
2 CDT Build Console [Siul2_Dio_Ip_Example_S32G274A_M7]
3 FreeRTOS Task Aware Debugger Console version 1.0.8 (201810241449)
1153M of 1758M



1. Download bin file to norflash
2. The function isn't ok. It shall on and off but it is only on.

workspace - Siul2_Dio_Ip_Example_S32G274A_M7/src/main.c - S32 Design Studio for S32 Platform

File Edit Source Refactor Navigate Search Project 配置工具 Run Window Help

Project Explorer

- Linflexd_Uart_Ip_Example_S32G274A_M7: Debug_RAM
- Siul2_Dio_Ip_Example_S32G274A_M7: Debug_RAM
  - Binaries
  - Includes
  - Project_Settings
  - RTD
    - include
    - src
  - board
  - generate
  - generate/include
  - generate/src
  - src
    - main.c
  - Debug_RAM
    - board
    - generate
    - Project_Settings
    - RTD
    - src
    - Siul2_Dio_Ip_Example_S32G274A_M7.elf - [arm/le]
    - application.bin
    - blob.bin
    - makefile
    - objects.mk
    - Siul2_Dio_Ip_Example_S32G274A_M7.args
    - Siul2_Dio_Ip_Example_S32G274A_M7.bin
    - Siul2_Dio_Ip_Example_S32G274A_M7.map
    - sources.mk
  - include
  - description.txt
  - example_Siul2_Dio.mex
- Spi_Transfer_S32G274A_M7: Debug_RAM
- Uart_Example_S32G274A_M7: Debug_RAM

Dashboard

Project Creation
- S32DS Application Project
- S32DS Library Project

Build/Debug
- Build (All)
- Clean (All)
- Debug

Settings
- Project
- Build se
- Debug

main.c | Mcu.h | Uart.c | Uart_lpw.c | System_lp.c | Siul2_Port_lp.c | main.c | stdio.h | Siul2_Dio_Ip... | main.c | »39

```
    while(1)
    {
        /* Dio_WriteChannel(DioConf_DioChannel_Digital_Output_LED_D78, STD_HIGH); */
        Siul2_Dio_Ip_WritePin(LED_PORT, LED_PIN, 1U);
        level = Siul2_Dio_Ip_ReadPin(LED_PORT, LED_PIN);
        //printf("the level is %d\r\n",level);
        //TestDelay(48000000);
        TestDelay(4800000);
        /* Dio_WriteChannel(DioConf_DioChannel_Digital_Output_LED_D78, STD_LOW); */
        Siul2_Dio_Ip_WritePin(LED_PORT, LED_PIN, 0U);
        level = Siul2_Dio_Ip_ReadPin(LED_PORT, LED_PIN);
        //TestDelay(48000000);
        TestDelay(4800000);
        printf("LED show \r\n");
    }

    return (0U);
}


#ifdef __cplusplus
}
```

Problems Tasks Console Properties Terminal Search

CDT Global Build Console

```
Finished building: ../Project_Settings/Startup_Code/startup_cm7.s


Building target: Siul2_Dio_Ip_Example_S32G274A_M7.elf
Invoking: Standard S32DS C Linker
arm-none-eabi-gcc -o "Siul2_Dio_Ip_Example_S32G274A_M7.elf" "@Siul2_Dio_Ip_Example_S32G274A_M7.args"
Finished building target: Siul2_Dio_Ip_Example_S32G274A_M7.elf

Invoking: Standard S32DS Create Flash Image
Invoking: Standard S32DS Print Size
arm-none-eabi-objcopy -O binary Siul2_Dio_Ip_Example_S32G274A_M7.elf  "Siul2_Dio_Ip_Example_S32G274A_M7.bin"
arm-none-eabi-size --format=berkeley Siul2_Dio_Ip_Example_S32G274A_M7.elf
   text    data     bss     dec     hex filename
 287380       0   12288  299668   49294 Siul2_Dio_Ip_Example_S32G274A_M7.elf
Finished building: Siul2_Dio_Ip_Example_S32G274A_M7.siz
Finished building: Siul2_Dio_Ip_Example_S32G274A_M7.bin



10:27:57 Build Finished. 0 errors, 0 warnings. (took 1s.352ms)

10:28:45 **** Incremental Build of configuration Debug_RAM for project Siul2_Dio_Ip_Example_S32G274A_M7 ****
make -j8 all
Invoking: Standard S32DS Print Size
arm-none-eabi-size --format=berkeley Siul2_Dio_Ip_Example_S32G274A_M7.elf
   text    data     bss     dec     hex filename
 287380       0   12288  299668   49294 Siul2_Dio_Ip_Example_S32G274A_M7.elf
Finished building: Siul2_Dio_Ip_Example_S32G274A_M7.siz


10:28:45 Build Finished. 0 errors, 0 warnings. (took 341ms)
```

No message of “LED show”