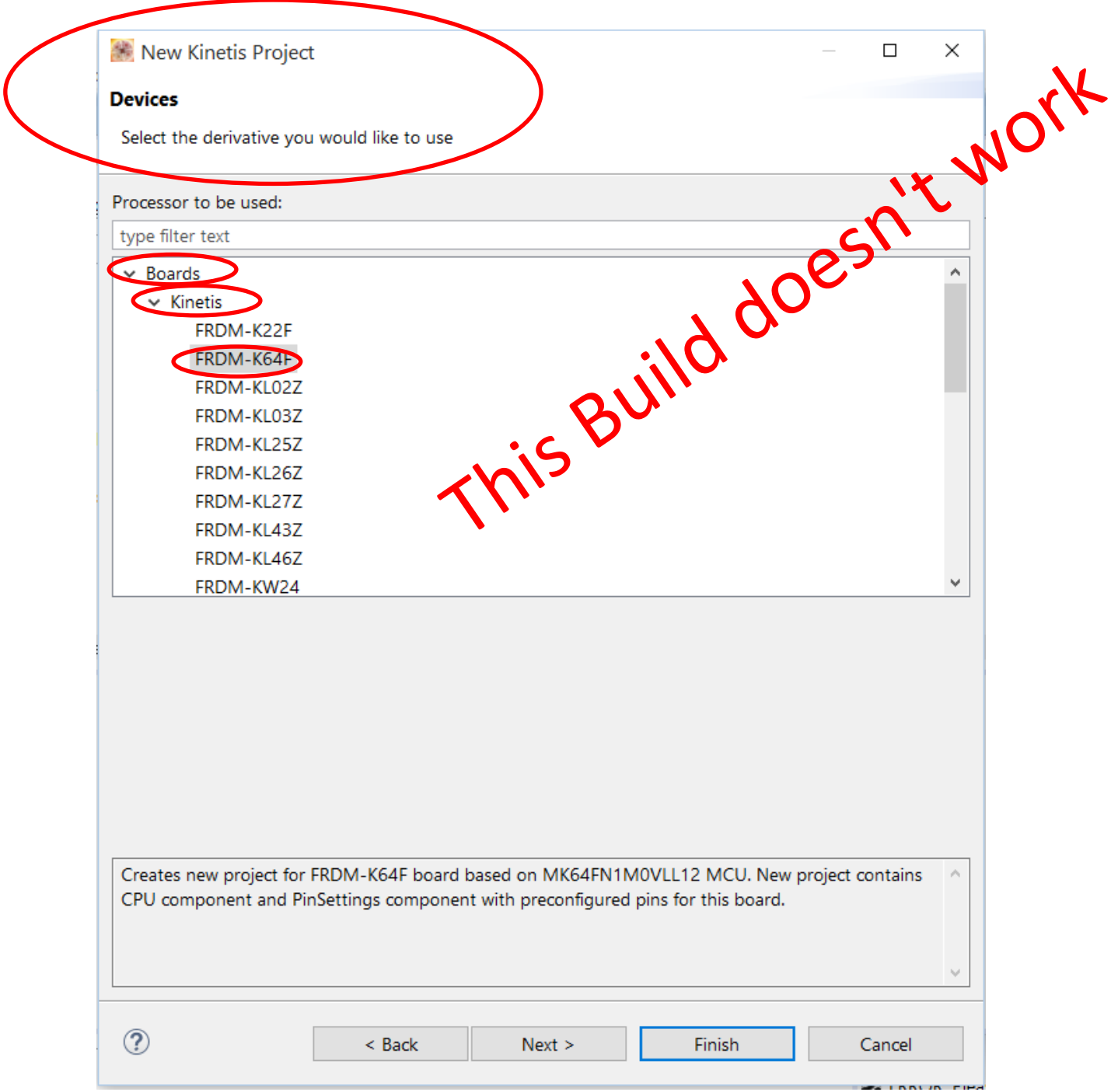
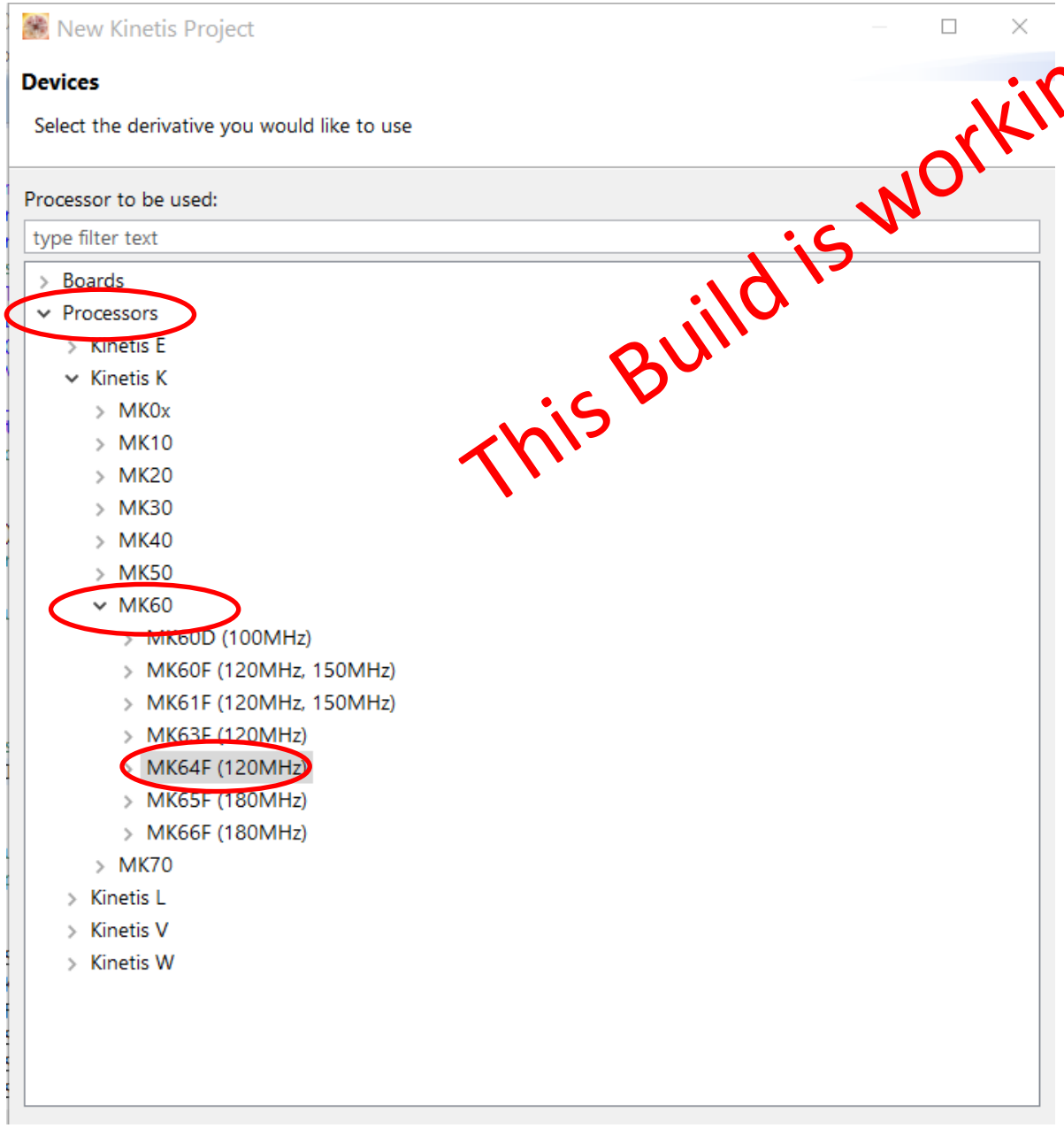
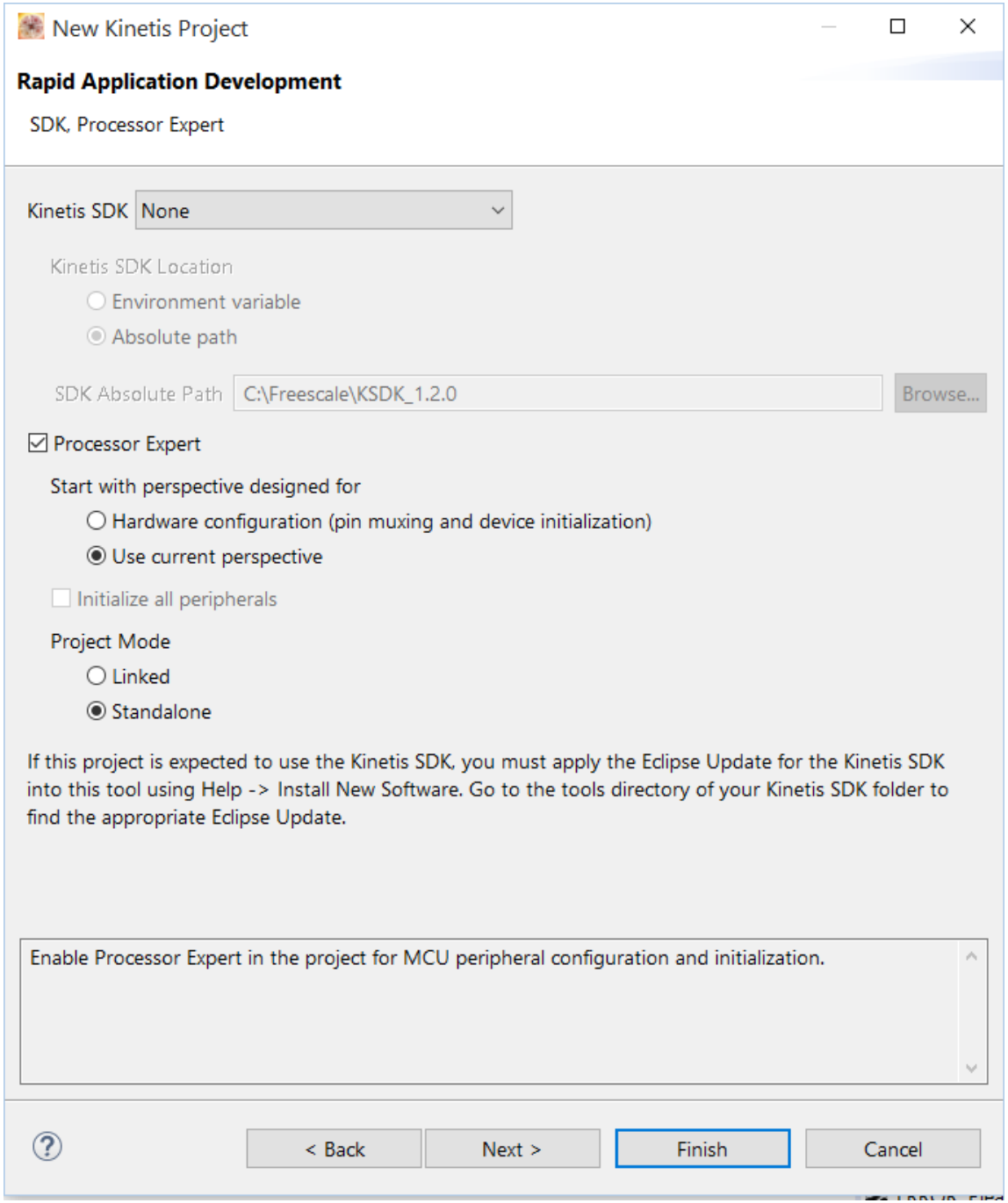
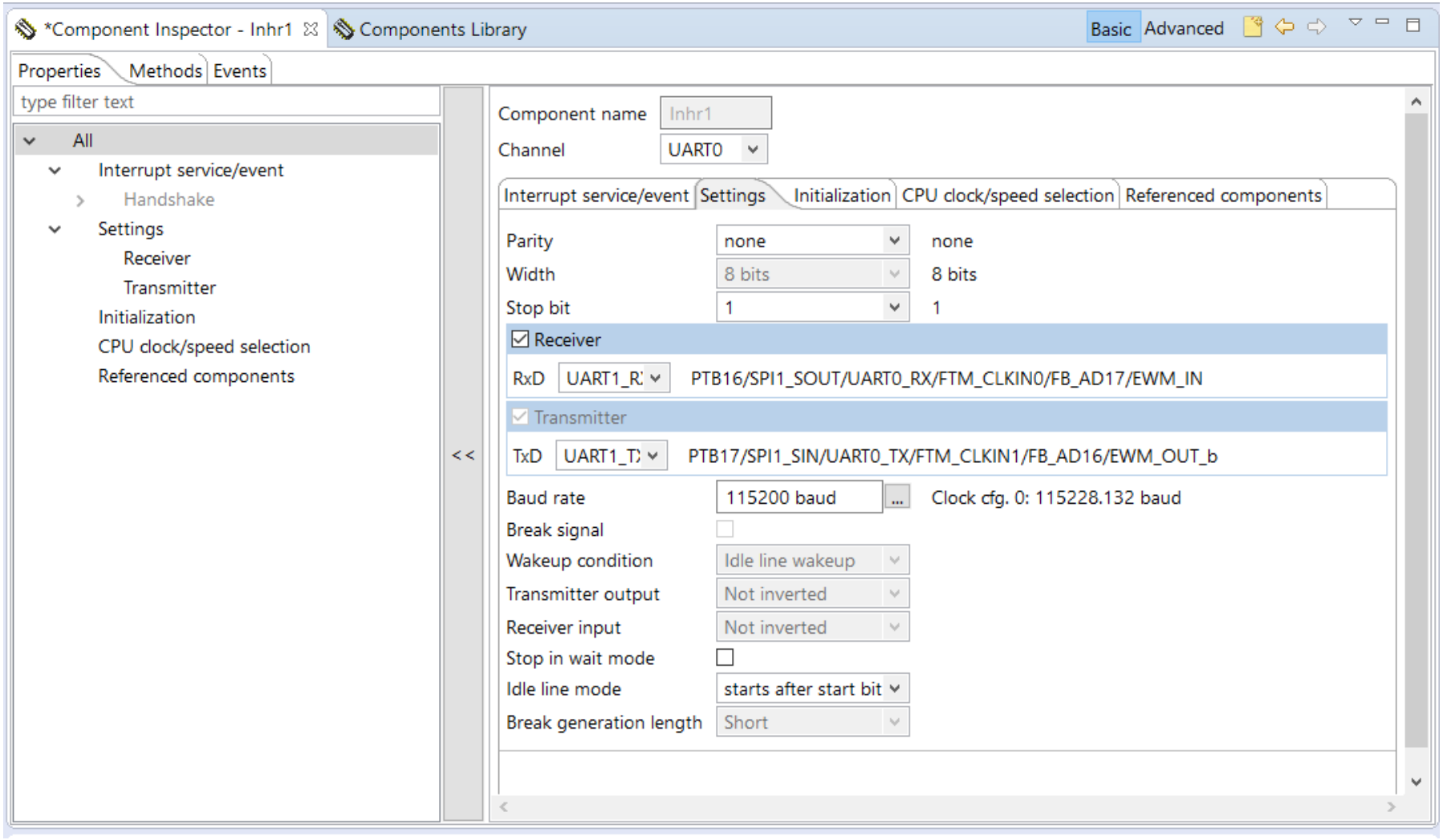
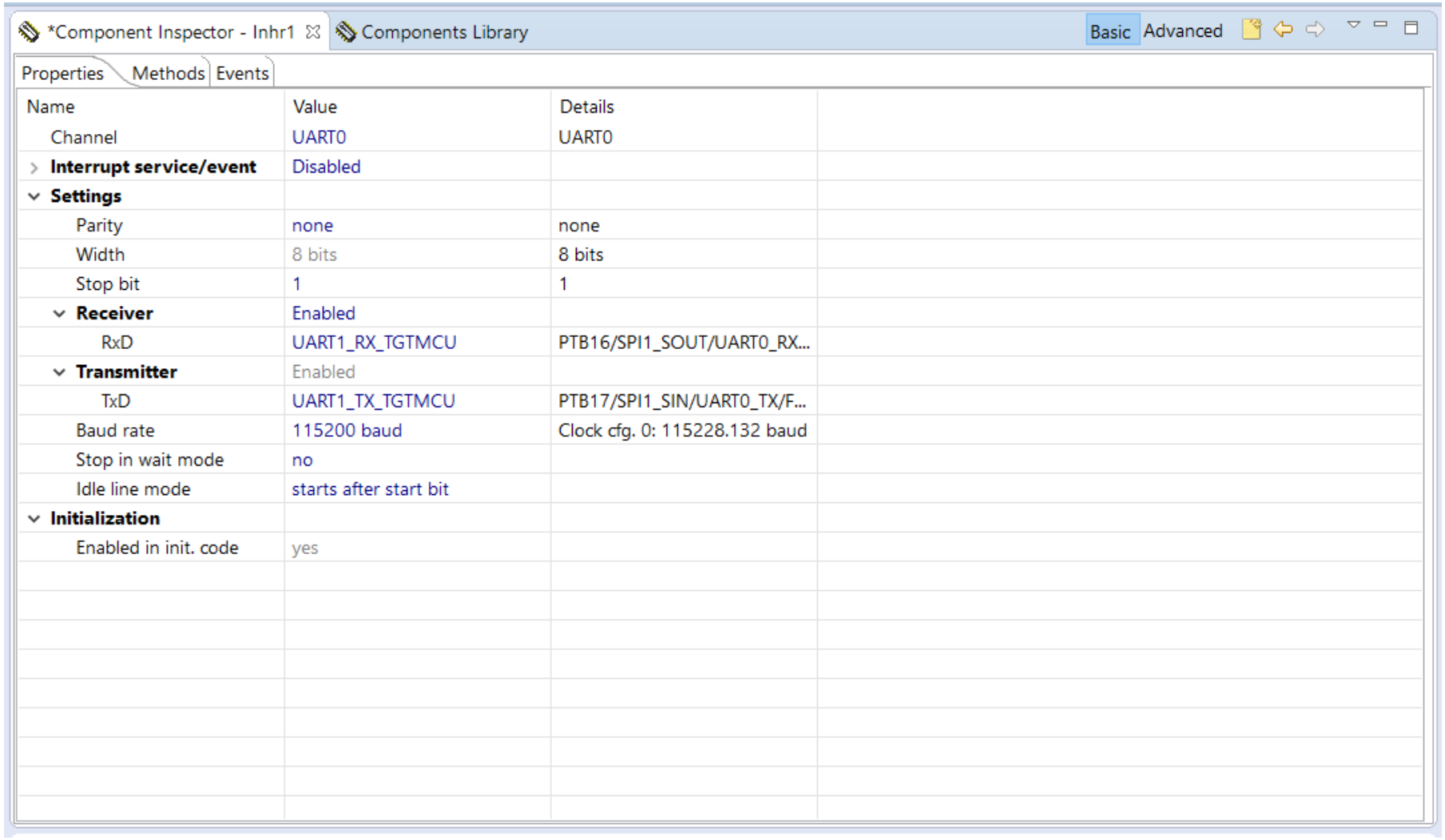
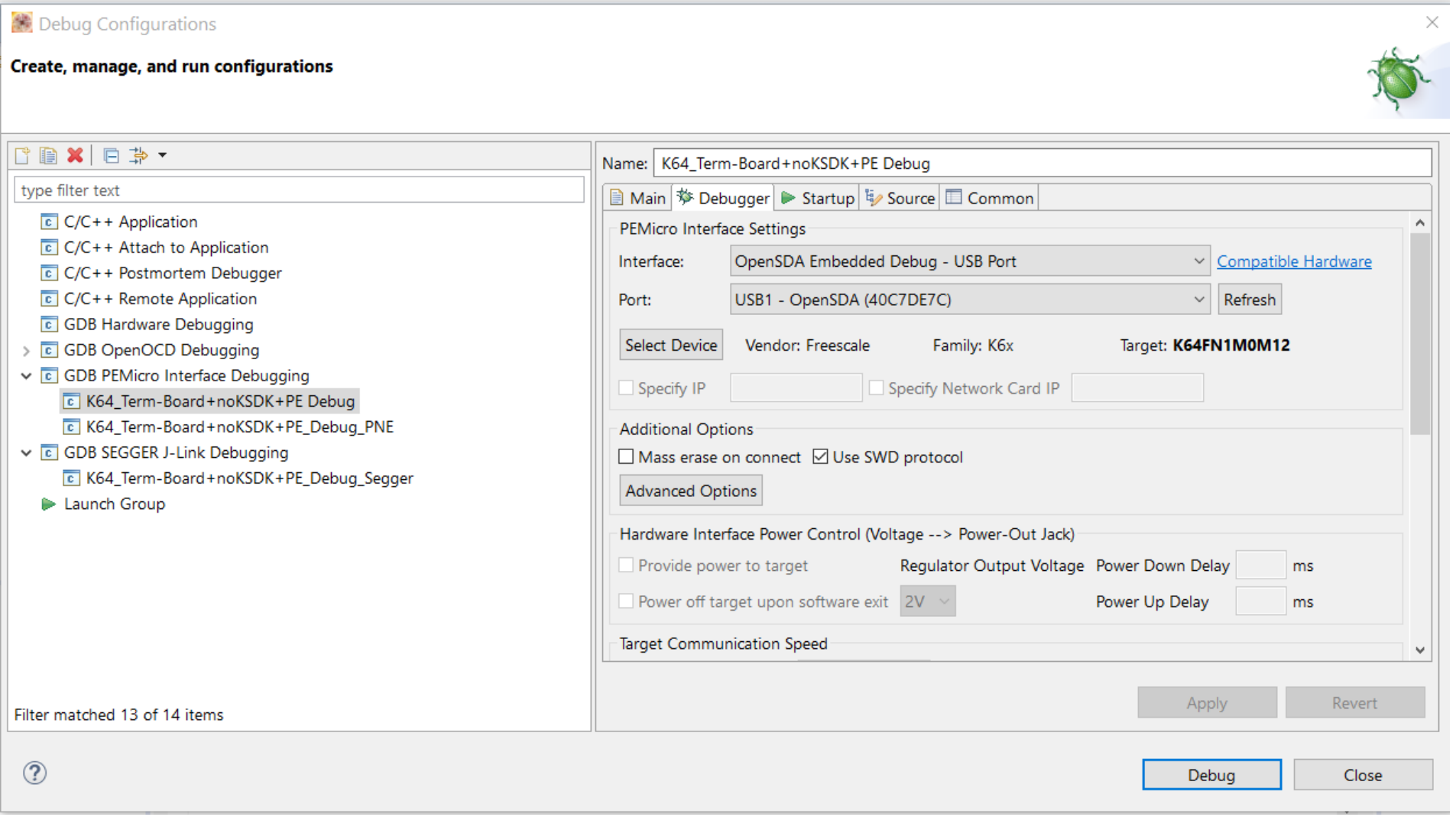

New Kinetis Project
Devices
Select the derivative you would like to use
Processor to be used:
type filter text
Boards
Kinetis
FRDM-K22F
FRDM-K64F
FRDM-KL02Z
FRDM-KL03Z
FRDM-KL25Z
FRDM-KL26Z
FRDM-KL27Z
FRDM-KL43Z
FRDM-KL46Z
FRDM-KW24
This Build doesn't work
Creates new project for FRDM-K64F board based on MK64FN1M0VLL12 MCU. New project contains CPU component and PinSettings component with preconfigured pins for this board.
< Back
Next >
Finish
Cancel


New Kinetis Project
Devices
Select the derivative you would like to use
Processor to be used:
type filter text
Boards
Processors
Kinetis E
Kinetis K
MK0x
MK10
MK20
MK30
MK40
MK50
MK60
MK60D (100MHz)
MK60F (120MHz, 150MHz)
MK61F (120MHz, 150MHz)
MK63F (120MHz)
MK64F (120MHz)
MK65F (180MHz)
MK66F (180MHz)
MK70
Kinetis L
Kinetis V
Kinetis W
This Build is working


New Kinetis Project
Rapid Application Development
SDK, Processor Expert
Kinetis SDK
None
Kinetis SDK Location
Environment variable
Absolute path
SDK Absolute Path
C:\Freescale\KSDK_1.2.0
Browse...
Processor Expert
Start with perspective designed for
Hardware configuration (pin muxing and device initialization)
Use current perspective
Initialize all peripherals
Project Mode
Linked
Standalone
If this project is expected to use the Kinetis SDK, you must apply the Eclipse Update for the Kinetis SDK into this tool using Help -> Install New Software. Go to the tools directory of your Kinetis SDK folder to find the appropriate Eclipse Update.
Enable Processor Expert in the project for MCU peripheral configuration and initialization.
< Back
Next >
Finish
Cancel


*Component Inspector - Inhr1
Components Library
Basic
Advanced
Properties
Methods
Events
type filter text
All
Interrupt service/event
Handshake
Settings
Receiver
Transmitter
Initialization
CPU clock/speed selection
Referenced components
<<
Component name
Inhr1
Channel
UART0
Interrupt service/event
Settings
Initialization
CPU clock/speed selection
Referenced components
Parity
none
none
Width
8 bits
8 bits
Stop bit
1
1
Receiver
RxD
UART1_R
PTB16/SPI1_SOUT/UART0_RX/FTM_CLKIN0/FB_AD17/EWM_IN
Transmitter
TxD
UART1_T
PTB17/SPI1_SIN/UART0_TX/FTM_CLKIN1/FB_AD16/EWM_OUT_b
Baud rate
115200 baud
...
Clock cfg. 0: 115228.132 baud
Break signal
Wakeup condition
Idle line wakeup
Transmitter output
Not inverted
Receiver input
Not inverted
Stop in wait mode
Idle line mode
starts after start bit
Break generation length
Short


*Component Inspector - Inhr1
Components Library
Basic
Advanced
Properties
Methods
Events
Name
Value
Details
Channel
UART0
UART0
Interrupt service/event
Disabled
Settings
Parity
none
none
Width
8 bits
8 bits
Stop bit
1
1
Receiver
Enabled
RxD
UART1_RX_TGTMCU
PTB16/SPI1_SOUT/UART0_RX...
Transmitter
Enabled
TxD
UART1_TX_TGTMCU
PTB17/SPI1_SIN/UART0_TX/F...
Baud rate
115200 baud
Clock cfg. 0: 115228.132 baud
Stop in wait mode
no
Idle line mode
starts after start bit
Initialization
Enabled in init. code
yes


Debug Configurations
Create, manage, and run configurations
type filter text
C/C++ Application
C/C++ Attach to Application
C/C++ Postmortem Debugger
C/C++ Remote Application
GDB Hardware Debugging
GDB OpenOCD Debugging
GDB PEMicro Interface Debugging
K64_Term-Board+noKSDK+PE Debug
K64_Term-Board+noKSDK+PE_Debug_PNE
GDB SEGGER J-Link Debugging
K64_Term-Board+noKSDK+PE_Debug_Segger
Launch Group
Filter matched 13 of 14 items
Name: K64_Term-Board+noKSDK+PE Debug
Main
Debugger
Startup
Source
Common
PEMicro Interface Settings
Interface:
OpenSDA Embedded Debug - USB Port
Compatible Hardware
Port:
USB1 - OpenSDA (40C7DE7C)
Refresh
Select Device
Vendor: Freescale
Family: K6x
Target: K64FN1M0M12
Specify IP
Specify Network Card IP
Additional Options
Mass erase on connect
Use SWD protocol
Advanced Options
Hardware Interface Power Control (Voltage --> Power-Out Jack)
Provide power to target
Regulator Output Voltage
Power Down Delay
ms
Power off target upon software exit
2V
Power Up Delay
ms
Target Communication Speed
Apply
Revert
Debug
Close

There is no response to my key press