

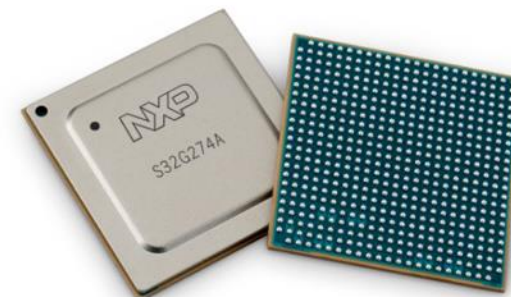
Get to Know the S32G Vehicle Network Processor

March 4, 2020

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Automotive Processors Business Line



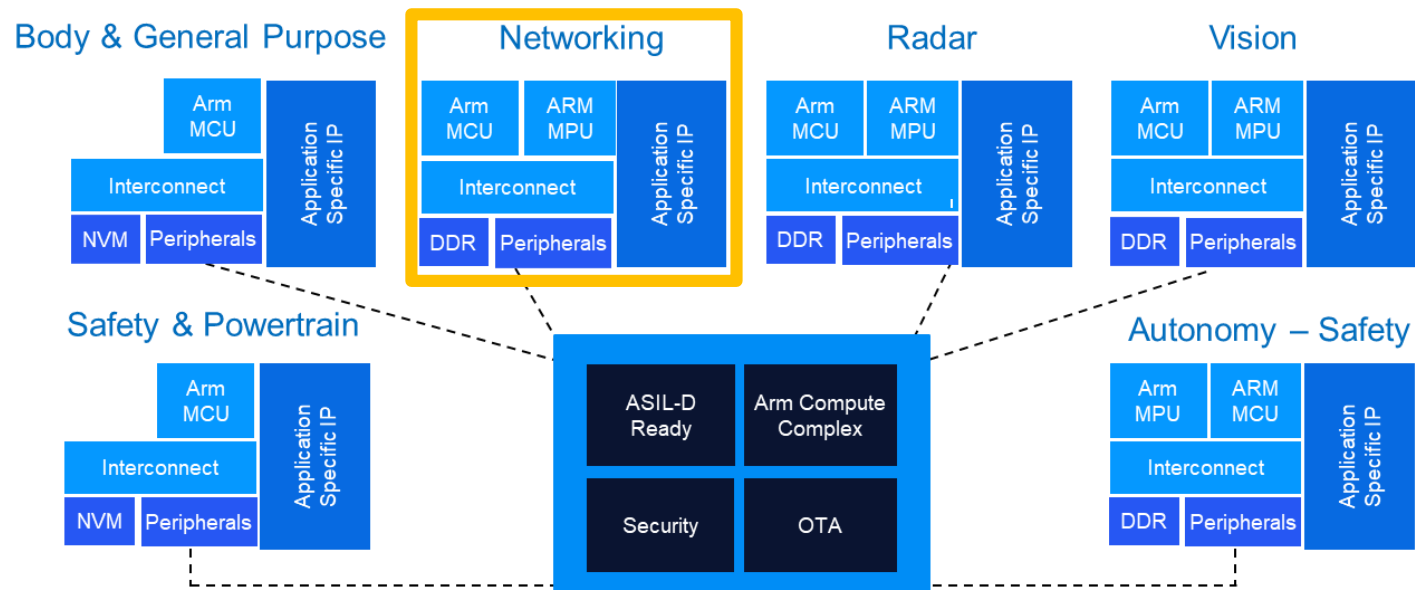
SECURE CONNECTIONS
FOR A SMARTER WORLD



PUBLIC

NXP Introduced the S32 Family of Processors

| | | | | | |
|--|--|--|---|---|--|
|  <h2>Performance</h2> <p>Optimized for functional domains</p> <p>Real-time and high-performance</p> <p>Scalable</p> | <h2>arm Processors</h2> <p>Arm® cores across portfolio</p> <p>Arm Cortex® -A, -R, and -M cores</p> |  <h2>Safe</h2> <p>ASIL D Functional Safety</p> <p>Process certified to ISO 26262:2018</p> |  <h2>Secure</h2> <p>Powerful hardware security engine (HSE)</p> <p>Firmware-upgradable public key encryption</p> |  <h2>OTA Updates</h2> <p>OTA updates while driving</p> <p>Fault recovery rollbacks</p> |  <h2>SW Reuse</h2> <p>Maximizes software re-use within and across application domains</p> <p>Cost savings for customers</p> |
|--|--|--|---|---|--|



The World's First Fully Scalable Safe Auto Compute Platform

Introducing the S32G

Vehicle Network Processor optimized for gateway, domain controller and safety controller applications

S32G introduces network acceleration to automotive with new levels of functional safety and security

Enables new service-oriented gateways to rapidly deploy new services and support over-the-air (OTA) upgradeable vehicles

High level of compute with legacy automotive and Ethernet network interfaces ideal for domain controllers

High-level of ASIL D processing for AD/ADAS safety controllers

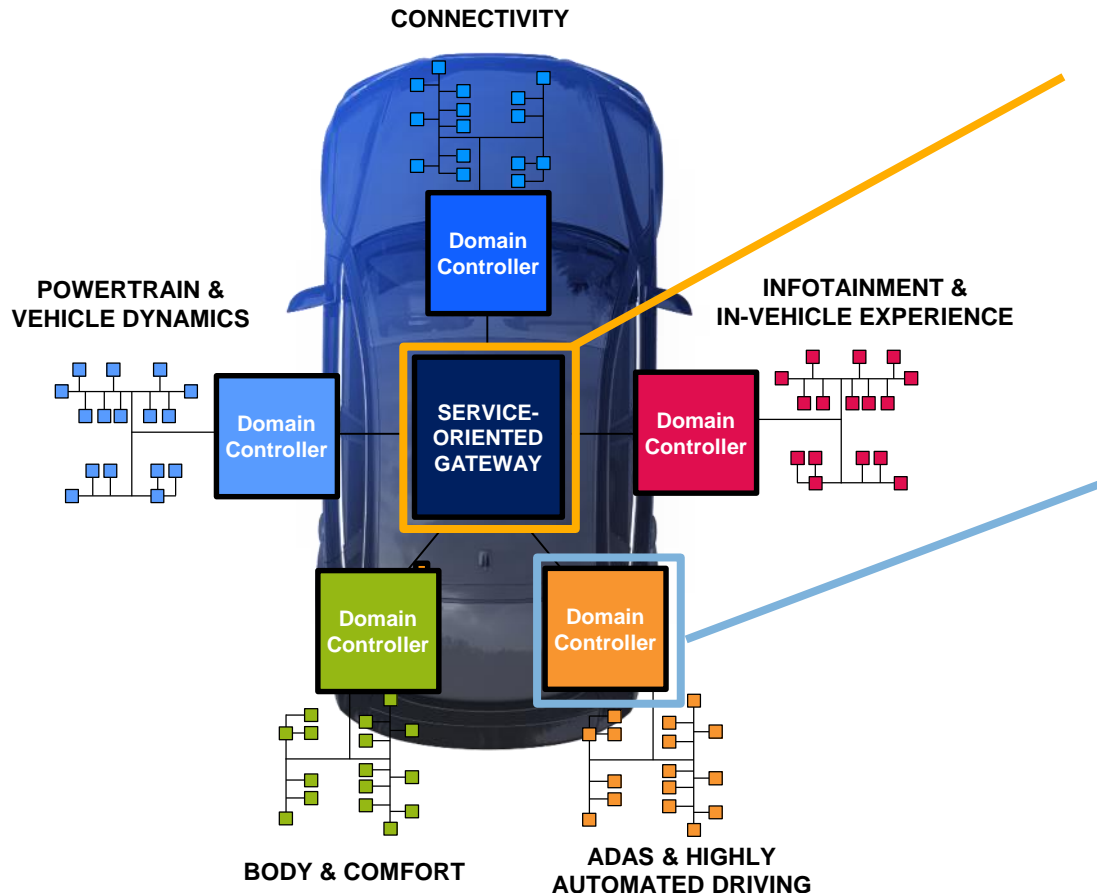


NXP

S32G

The Versatile Uses of the S32G Vehicle Network Processor

DOMAIN VEHICLE ARCHITECTURES

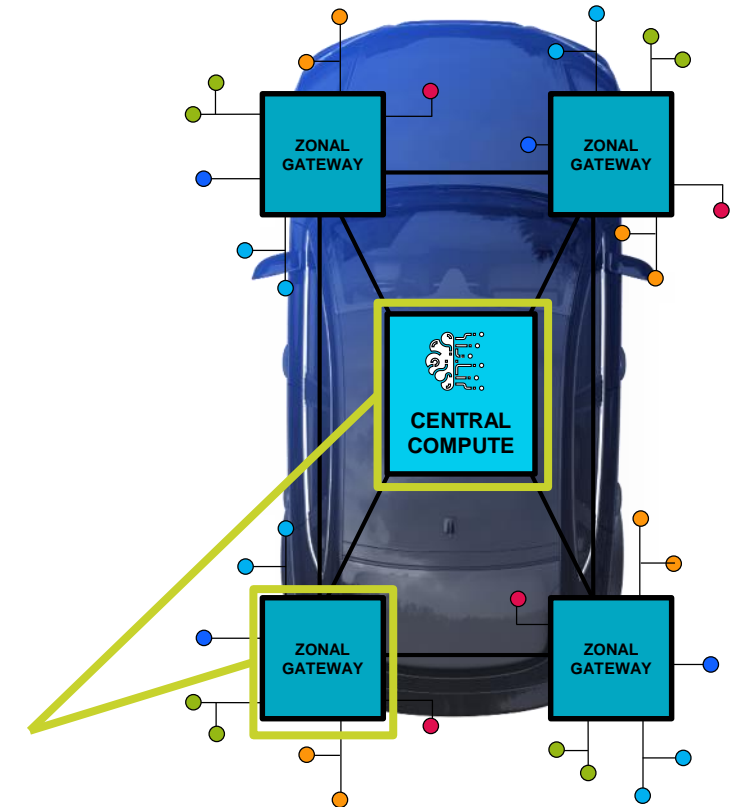


Service-oriented Gateway

Domain Controller / ADAS Safety Controller

Zonal Compute / Gateways

ZONAL VEHICLE ARCHITECTURES



S32G is a New Type of Automotive Processor: Vehicle Network Processor



Processing

- Lockstep Microcontrollers
- Cluster Lockstep Microprocessors
- Automotive Networks Acceleration
- Ethernet Packet Acceleration



Networking

- 20 x CAN/CAN FD Interfaces
- LIN and FlexRay™ Interfaces
- 4 x Gigabit Ethernet Interfaces
- PCI Express Gen 3 Interfaces

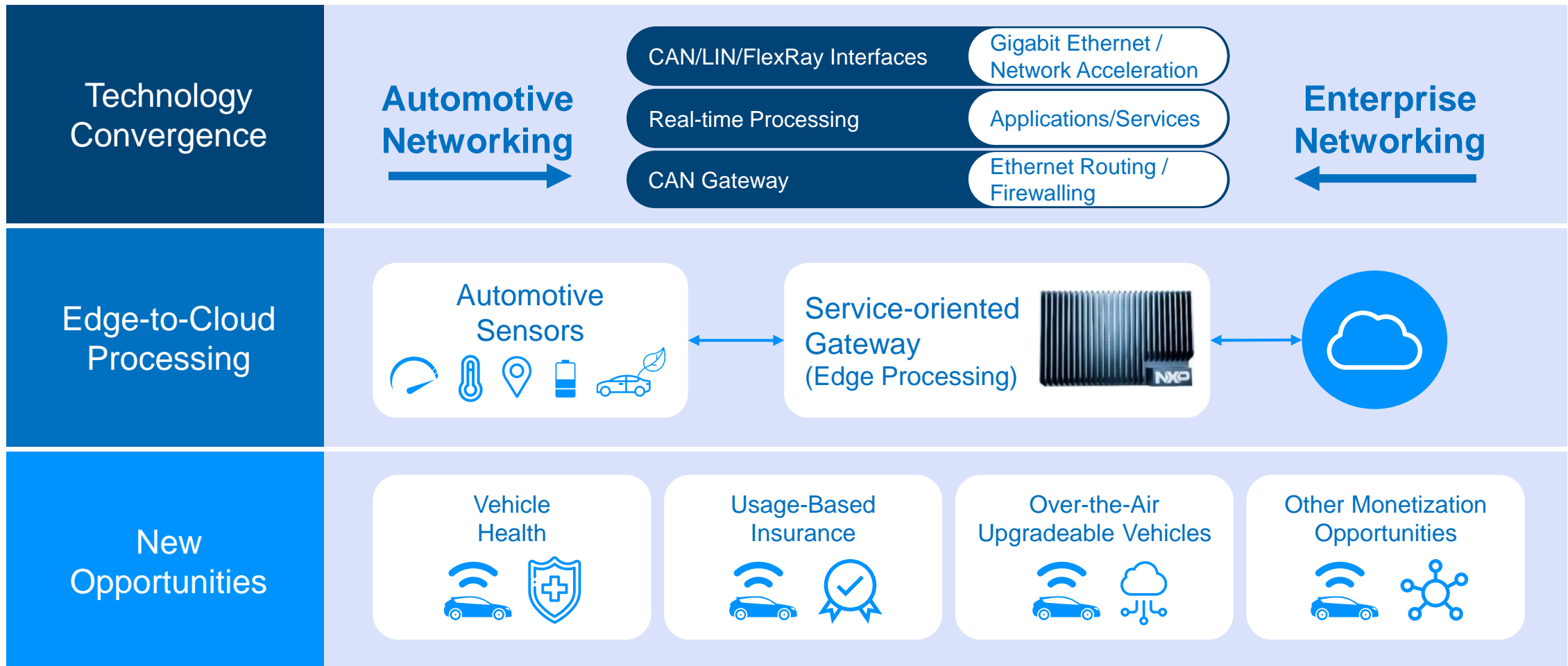
Safety & Security

- ASIL D Functional Safety Support
- Advanced Hardware Security Engine

Applications

- Service-oriented Gateway
- Domain Controller
- ADAS/AD Safety Controller

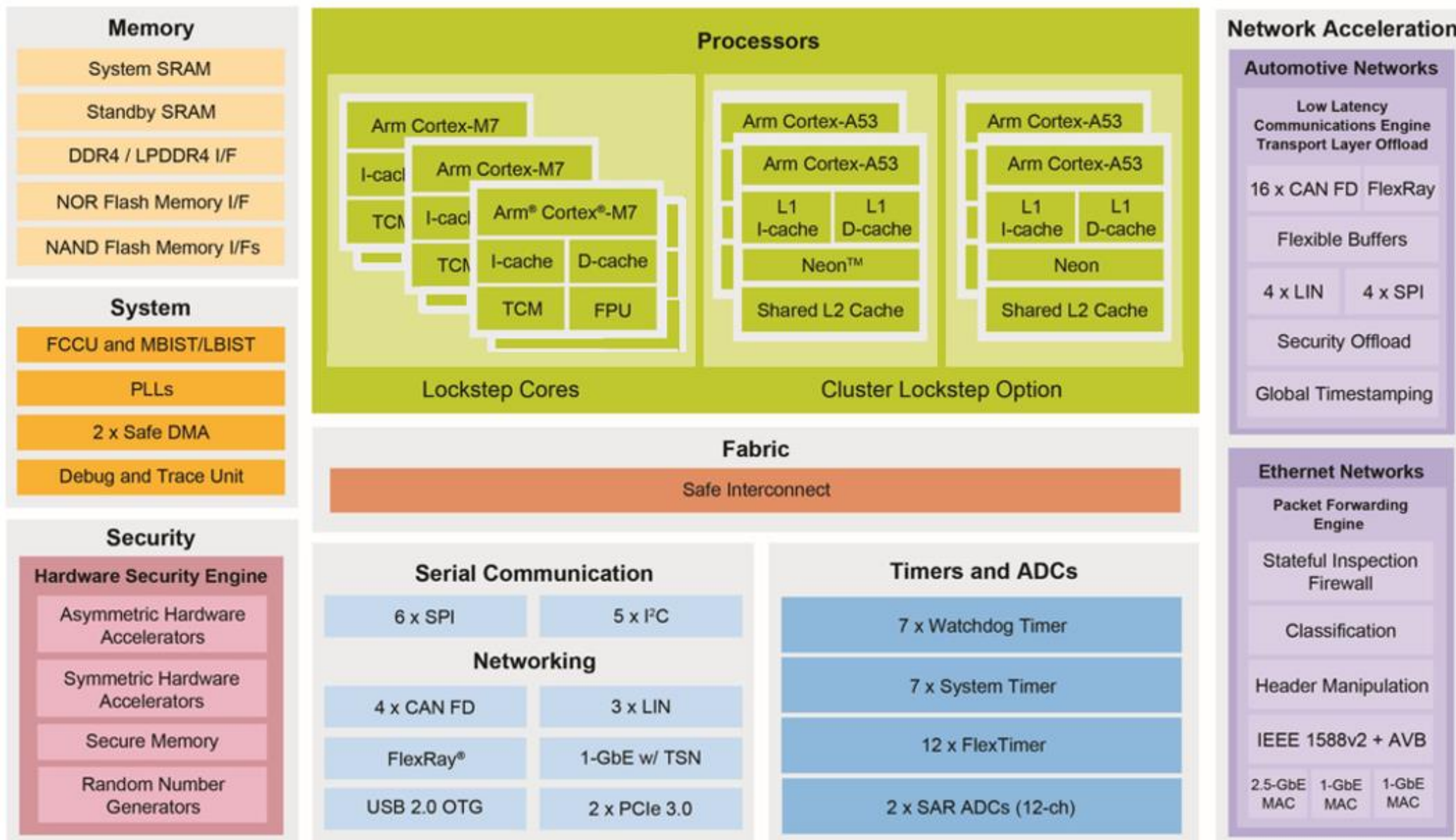
S32G: Bringing Together Automotive and Enterprise Worlds to Enable Disruptive Opportunities



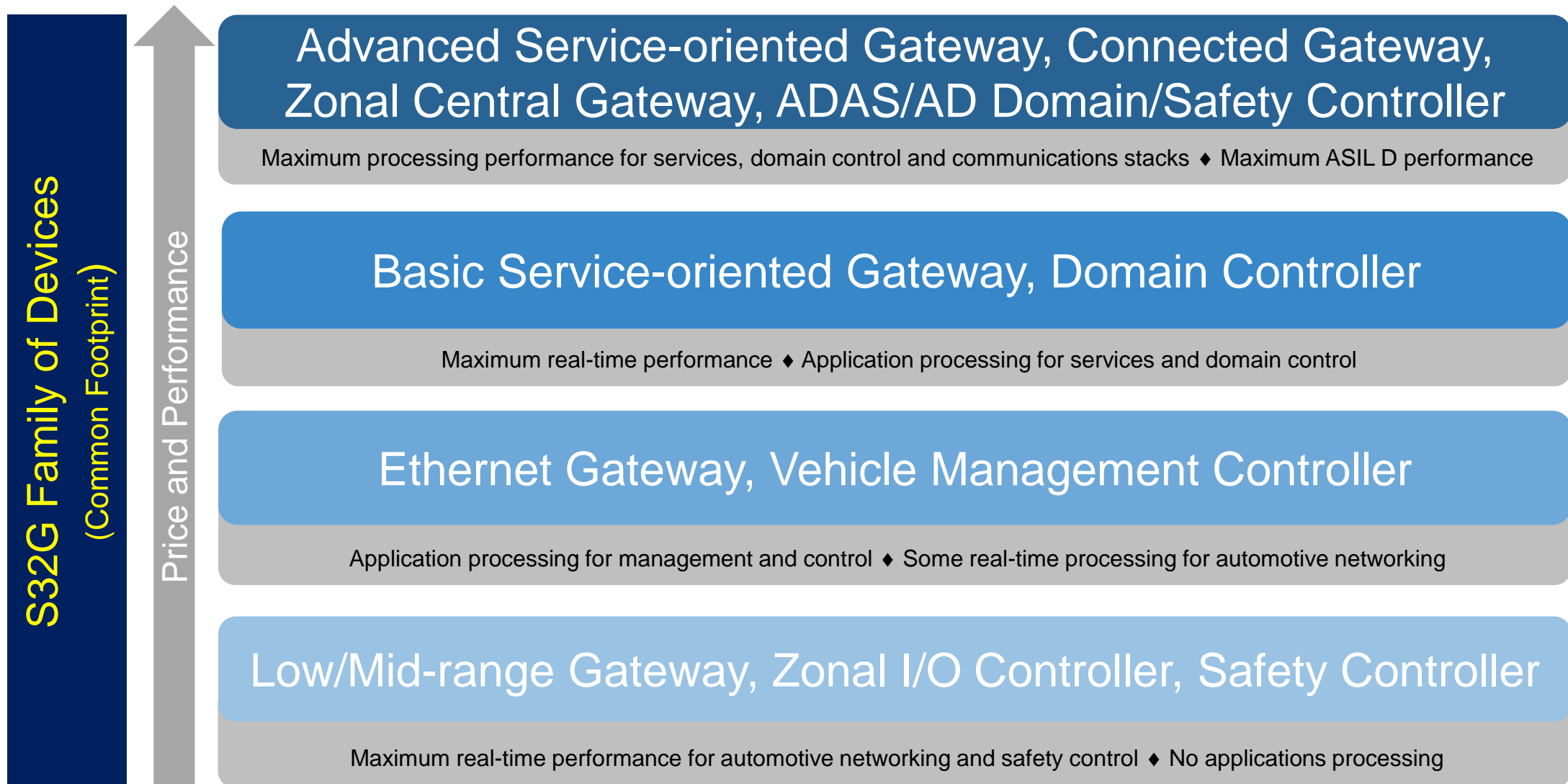
S32G is Optimized for Target Applications

| Capability | Gateway | Domain Controller | AD/ADAS Safety Controller |
|---|----------|-------------------|---------------------------|
| Microcontrollers for real-time processing | ✓ | ✓ | ✓ |
| Microprocessors for applications and services | ✓ | ✓ | ✓ |
| Embedded hardware security for trusted boot and services | ✓ | ✓ | ✓ |
| Low-latency, deterministic CAN network acceleration | ✓ | ✓ | ✓ |
| Ethernet network acceleration for routing and firewalling | ✓ | ✓ | ✓ |
| PCI Express interfaces for co-processors and storage | ✓ | ✓ | ✓ |
| ISO 26262:2018 ASIL D support for functional safety | Optional | Optional | ✓ |
| ASIL D applications processing on Cortex-A cores | Optional | Optional | ✓ |

S32G274A Vehicle Network Processor High-level Block Diagram

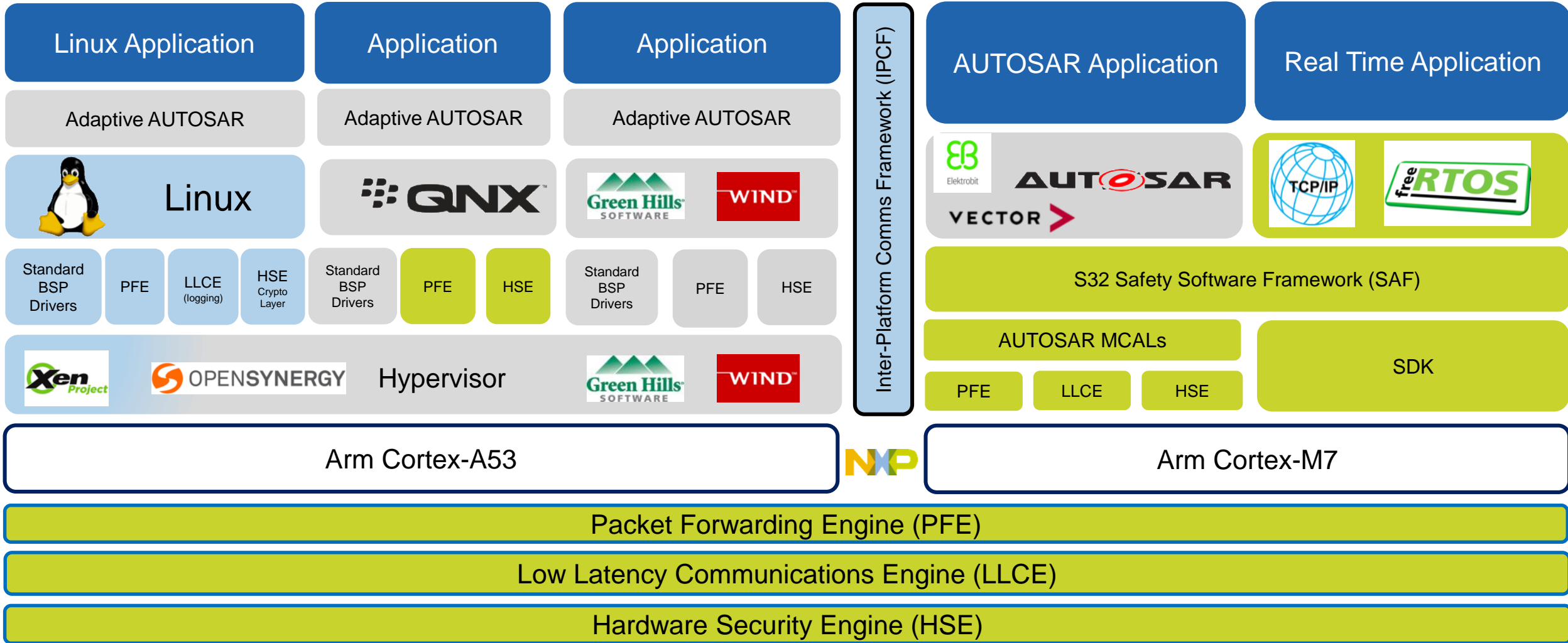


S32G Scalable Family Applications*



S32G Software Support

S32G
Shared Memory



Production Quality






Firmware

Linux/AUTOSAR Reference

Third Party



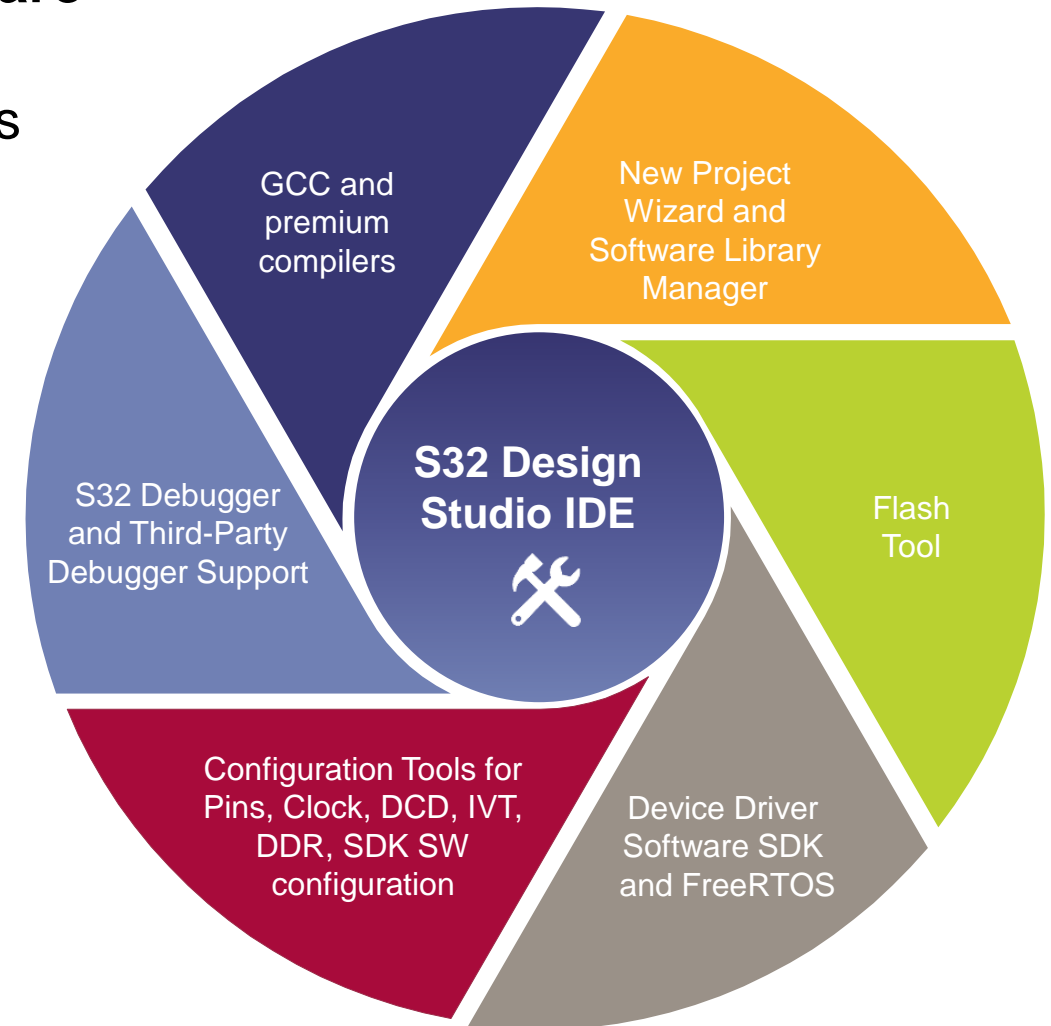
Current S32G Ecosystem Partners*

| | | | | |
|--|--|--|---|---|
| <p>Cortex-A53 Operating Systems</p>   <p>Linux</p>   | <p>Hypervisor / Virtualization</p>     | <p>Debuggers / Probes</p>    | <p>Data Analytics</p>  | <p>Cloud and OTA Services</p>   |
| <p>Cortex-M7 Operating Systems</p>  | <p>Hardware Platforms</p>  | <p>Compilers</p>    | <p>Security / IDPS Anomaly Detection</p>   | <p>Virtual Prototyping</p>  |
| <p>Classic AUTOSAR</p>  | <p>Adaptive AUTOSAR</p>  | <p>Others</p> <p>To Be Announced</p> | | |

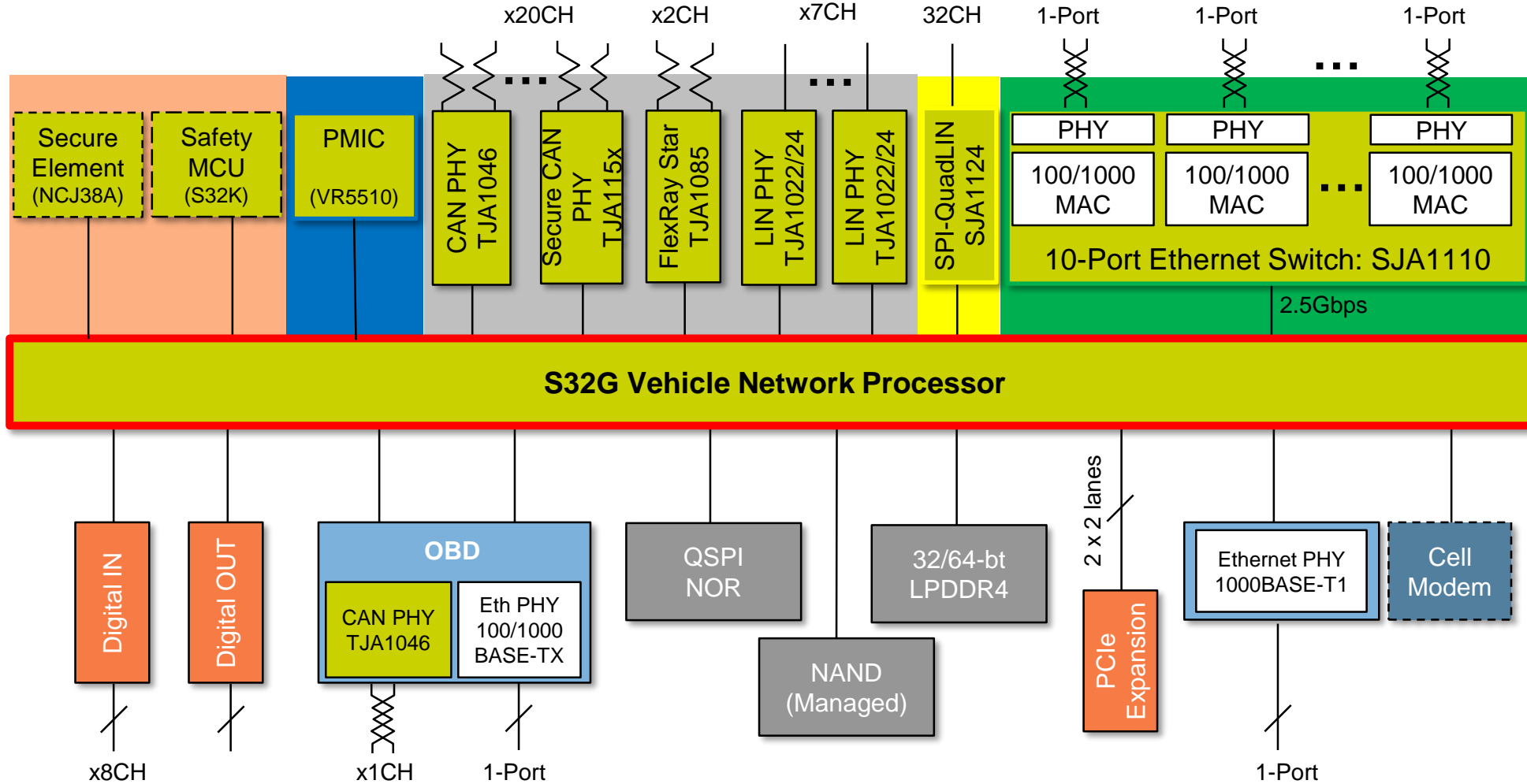
S32 Design Studio Software Development Environment

One integrated environment with tools and software

- GCC and premium industry compilers for all Arm® cores
- NXP S32 Debugger and 3rd party debugger support
- S32 Configuration Tools:
 - Pins, Clocks, DDR, DCD, IVT, Peripheral, SW Config
- New Project Wizard
- Software Library Manager
- Flash Tool
- Platform software SDKs
 - S32 SDK, Drivers, Initialization Functions
 - FreeRTOS
- Operating system development support
 - Linux/FreeRTOS



NXP S32G System Solution



NXP Devices

VR5510 ASIL D PMIC

- 60V direct connect
- All system supplies
- ASIL D Functional Safety

SJA1110 Ethernet Switch

- Highest connectivity
- 2.5Gb/s Interfaces
- Integrated Security

S32G Highly-Integrated Solution

- Real-time MCU
- High-performance MPU
- HW offload for Ethernet & Auto Communications
- Integrated Security Engine

SJA1124 SPI-QuadLIN

- Integrated MACs + PHYs
- Reduced MCU Pins

NXP PHY Portfolio

- CAN, LIN, FlexRay

Broader NXP Portfolio

- Secure Element, MCU

Key Benefits of NXP S32G System Solution

BOM Optimization

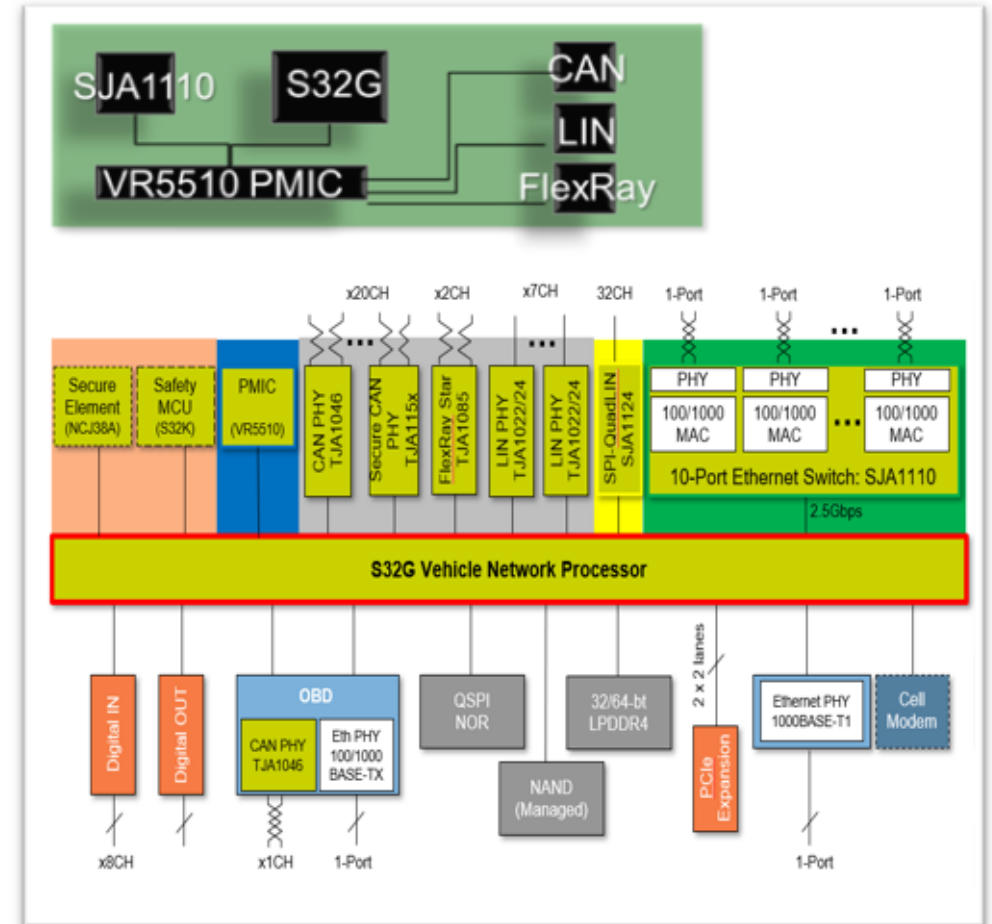
- Reduced BOM via common PMIC for S32G, switch, transceivers, DDR
- Optimal PCB layout via optimized pin alignment
- Optimal Ethernet BOM with HW/SW-compatible switch variants
- Integrated Ethernet PHYs lowers cost and reduces design effort

Differentiating Capabilities

- High-speed 2.5Gbps SGMII link between Ethernet switch and S32G
- Strong, layered security partitioning between Ethernet switch and S32G
- Strong functional safety support across S32G/Ethernet Switch/PMIC
- Integrated ASIL D support with VR5510 power management (PMIC)

Faster Time to Market

- Common reference design hardware platform with design information
- Common software tools and drivers
- Common AVB/gPTP software for important redundancy capabilities
- Common NXP support



Multiple S32G Hardware Platforms*

Lab / Desktop

NXP
S32G-VNP-EVB



S32G Evaluation Board

Available Now

Lab/Desktop

NXP
S32G-VNP-RDB



S32G Reference Design Board

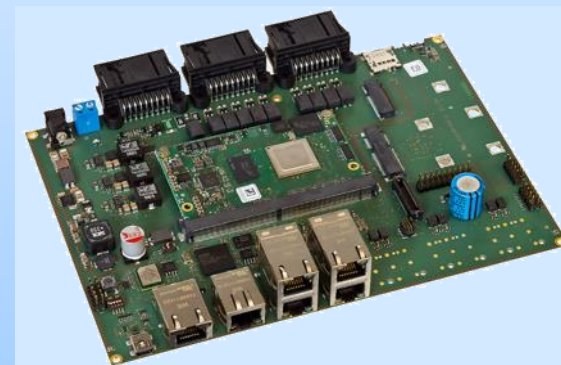
Early Access Now, Broader Availability June'20

In-Vehicle



Embedded

MicroSys miriac™
SBC-S32G274A



S32G Single Board
Computer + SoM

Available 2Q'20

- ✓ S32G Reference Design Board (RDB)
- ✓ Software Enablement
- ✓ Demonstrations



Carmakers

Application Developers

Cloud & Service Providers

Proof of concept

Benchmarking

Vehicle data insights

New services deployment

Innovation platform

Software development

Test and validation

Demo showcase

Symbiotic compute

Over-the-Air (OTA) updates

Machine learning deployment

Edge service deployment

Accelerating Transformation Across the Automotive Ecosystem

Summary

S32G delivers advanced levels of performance, security, ASIL D safety and system integration

S32G enables edge-to-cloud processing as well as ECU consolidation to simplify vehicle architectures

NXP offers a system solution for the S32G along with a strong ecosystem



NXP

S32G

For More Information

- Contact your local NXP sales representative for questions involving pricing and schedule or to obtain further technical information under Non-Disclosure Agreement
- Check nxp.com/s32g for more information about the S32G processor

The screenshot shows the NXP website page for the S32G processor. The navigation bar includes the NXP logo, 'Products', 'Applications', 'Design', 'Support', and 'Company'. A search bar is located on the right. The breadcrumb trail reads: 'Processors and Microcontrollers > Arm Processors > S32 Automotive Platform > S32G274A for Vehicle Network Processing'. The main heading is 'S32G Safe and Secure Vehicle Network Processor', with 'Follow', 'Email', and 'Share' icons to its right. Below the heading is a navigation menu with tabs for 'OVERVIEW', 'DOCUMENTATION', 'TOOLS & SOFTWARE', and 'TRAINING & SUPPORT'. The 'OVERVIEW' tab is active. On the left, a 'Jump To' section lists 'Overview & Features' and 'Target Applications'. The main content area is divided into three columns: 'Overview', 'Features', and a third column. The 'Overview' section is marked 'PRE-PRODUCTION' and describes the S32G processor's capabilities, including ASIL D safety and real-time processing. It notes that silicon and enablement are available for approved customers and provides a link to contact a sales representative. A red warning message states: 'This page contains information on a preproduction product. Specifications and information herein are subject to change without notice.' The 'Features' section lists several key capabilities: Quad Arm Cortex-A53 cores with Arm Neon technology, Triple Arm Cortex-M7 lockstep cores, Low Latency Communications Engine (LLCE), Packet Forwarding Engine (PFE), Hardware Security Engine (HSE), and an AEC-Q100 Grade 2 device.

NXP Products Applications Design Support Company

Processors and Microcontrollers > Arm Processors > S32 Automotive Platform > S32G274A for Vehicle Network Processing

S32G Safe and Secure Vehicle Network Processor

Follow Email Share

OVERVIEW DOCUMENTATION TOOLS & SOFTWARE TRAINING & SUPPORT

Jump To
Overview & Features
Target Applications

Overview

PRE-PRODUCTION

The S32G vehicle network processor combines ASIL D safety, hardware security, high-performance real-time and application processing, and network acceleration for service-oriented gateways, domain controllers and safety co-processors. Providing more than 10 times the performance and networking of NXP's previous family of automotive gateway devices, the versatile S32G processor is enabling the next generation of vehicle gateways and architectures.

S32G silicon and enablement (documentation, software, and boards) are available for approved customers. Please contact your local [NXP sales representative](#) for more information

This page contains information on a preproduction product. Specifications and information herein are subject to change without notice.

Features

- Quad Arm® Cortex®-A53 cores with Arm Neon™ technology organized in two clusters of two cores with optional cluster lockstep for applications and services
- Triple Arm Cortex-M7 lockstep cores for real-time applications
- Low Latency Communications Engine (LLCE) for automotive networks acceleration
- Packet Forwarding Engine (PFE) for Ethernet networks acceleration
- Hardware Security Engine (HSE) for secure boot and accelerated security services
- Advanced functional safety hardware and software for ASIL D systems
- AEC-Q100 Grade 2 device (-40°C to 105°C)



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