

**Table of Contents**

2	Notes
3	Power
4	Mother Board connector
5	CAN Interface
6	SPI/RS232 & LIN interface
7	FlexRay
8	Fast Ethernet Controller
9	User I/O
10	User Peripherals Inc Prototyping

**MPC57xx EVB MOTHER BOARD**

**Revisions**

Rev	Description	Date	Approved
X1	Original release to Agile BRA070 ECO32585	4-AUG-2011	Martin Vaupel
X2	Rotated J6 to ensure unique fitting of daughter cards Release to Agile BRA070 ECO33096	2-SEP-2011	Martin Vaupel
A	Release to Agile BRA085 ECO33358	20-SEP-2011	Martin Vaupel
AX1	Update componets to match FIL stock Updated some jumper symbols to be jumper not fitted Modified the 1.25v power LED circuit Added decoupling cap to RS232 circuit Added Jumpers to Ethernet page Changed value of RN55	12-APR-2012	Martin Vaupel
B	Release to FIL for Assembly	19-APR-2012	Martin Vaupel
EX1	MODS for Production revision Changed some Through Hole connectors to Surface Mount	14-SEP-2012	Martin Vaupel
C	Release to FIL for Assembly	11-OCT-2012	Martin Vaupel

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ICAP Classification: FCP: FIUQ: X PUBI:			
Designer: Martin Vaupel	Drawing Title: <b>MPC57xx MOTHER BOARD</b>		
Drawn by: Martin Vaupel	Page Title: <b>Title</b>		
Approved: David McMenamin	Size C	Document Number SCH-27237 PDF: SPF-27237	Rev C
Date: Thursday, October 11, 2012 Sheet 1 of 10			

- All Jumpers are 2mm spaced
- All port headers are 2.54mm spaced
- All caps are 10% accurate
- All res are 1% accurate

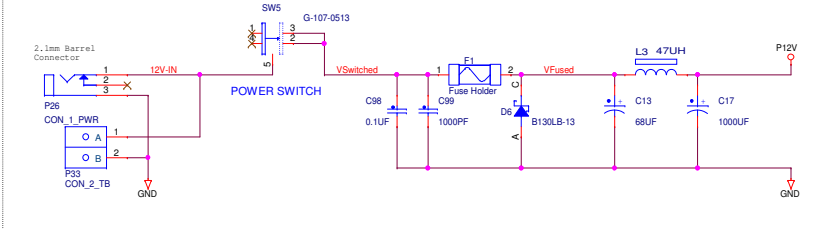


ICAP Classification: FCP: FUC: X PUBI:	
Drawing Title: <b>MPC57xx MOTHER BOARD</b>	
Page Title: <b>Notes</b>	
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Date: Thursday, October 11, 2012	Sheet 2 of 10

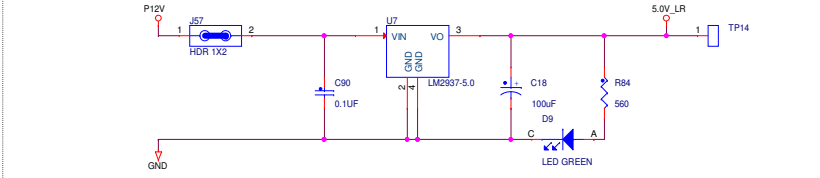
Regulators were designed to supply provide up to lamp. -> That might not be enough to suit Matterhorn and will be checked.

Regulators were designed using design tool on national semiconductors web site.

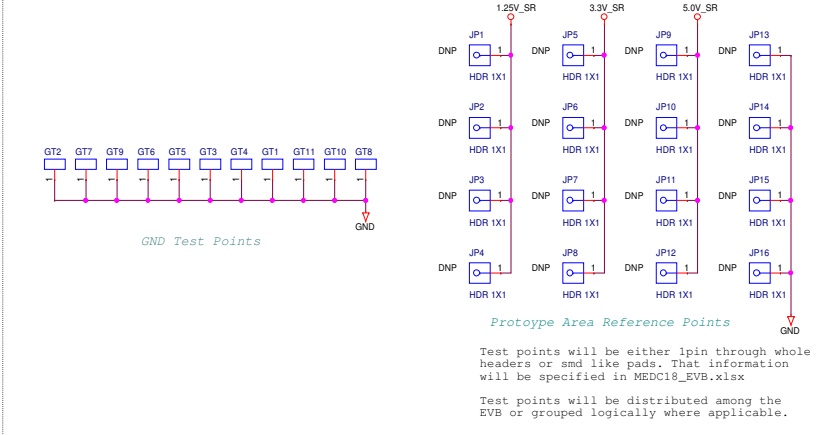
**Power supply input and filter**



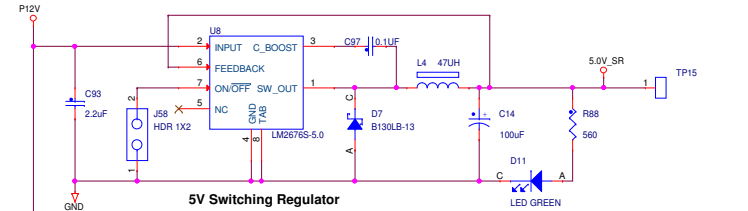
**5V Linear Regulator**



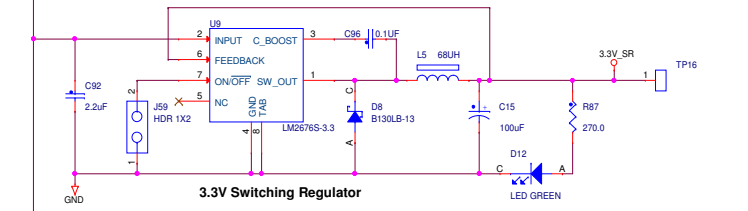
**Test and reference points**



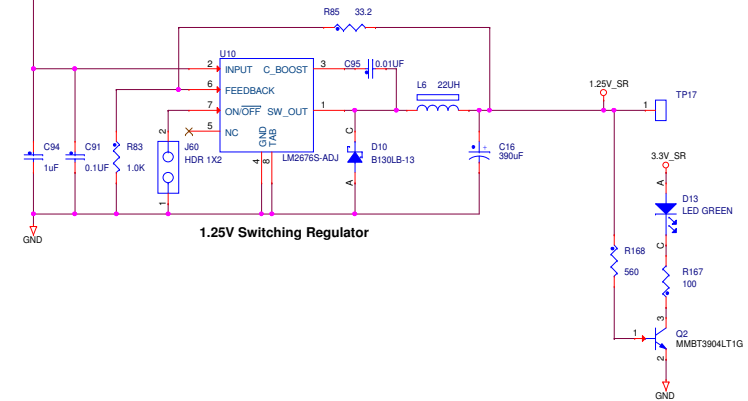
**Switching Regulators**



**5V Switching Regulator**

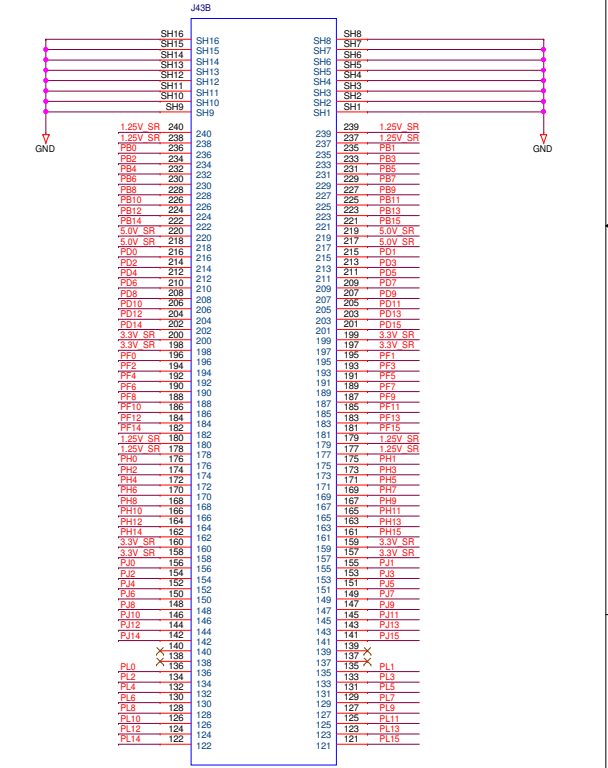
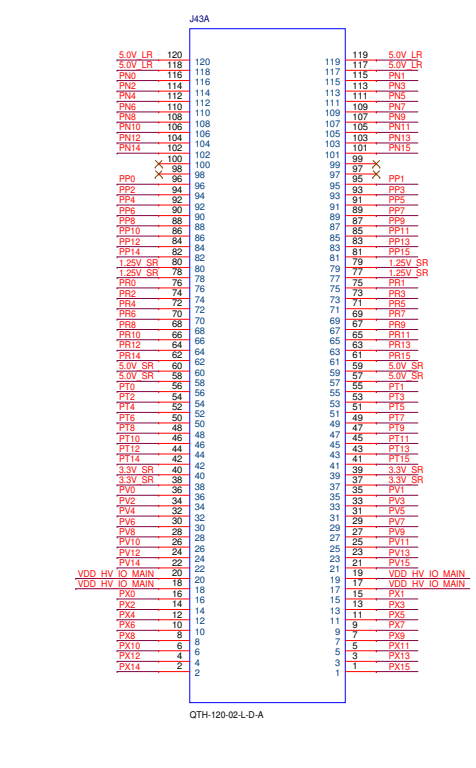
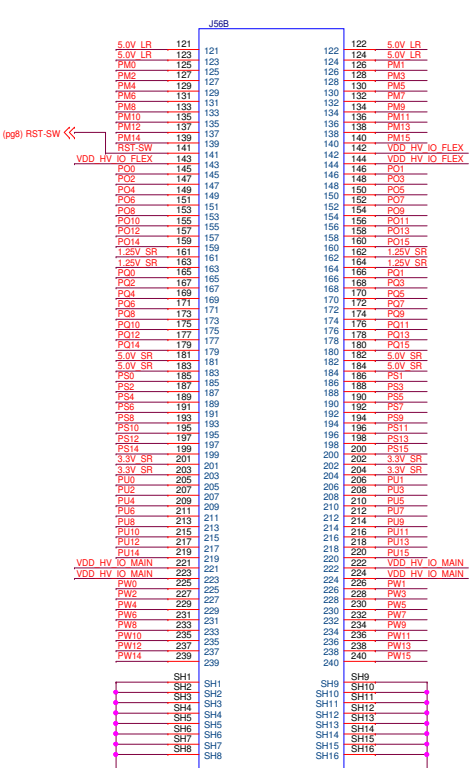
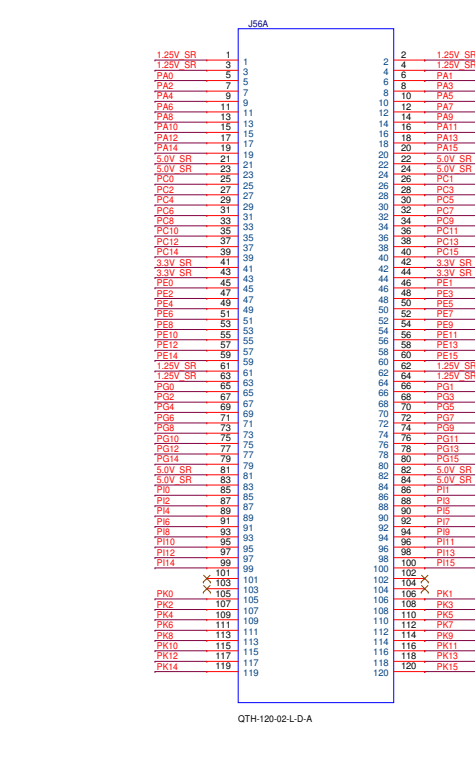
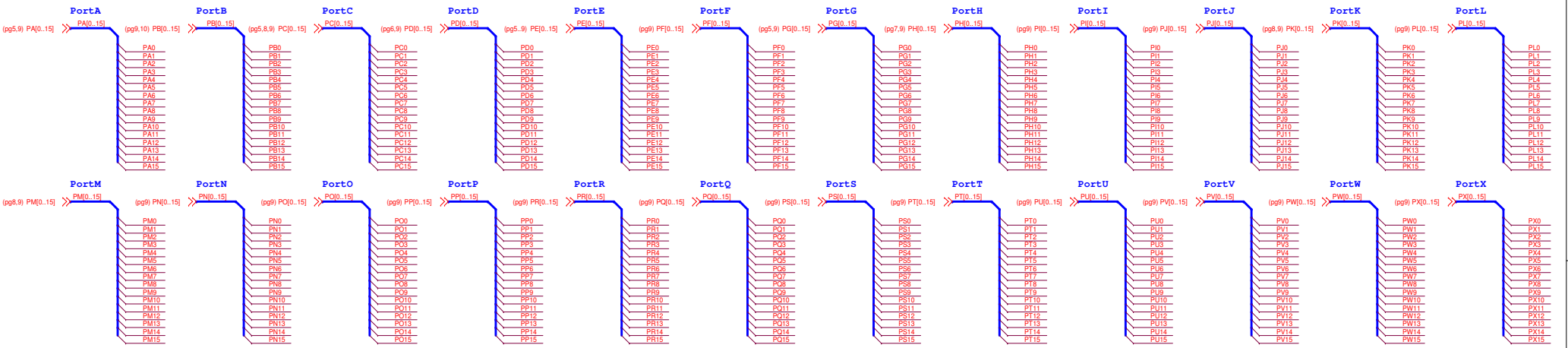


**3.3V Switching Regulator**

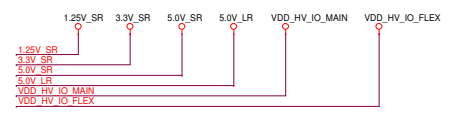


**1.25V Switching Regulator**



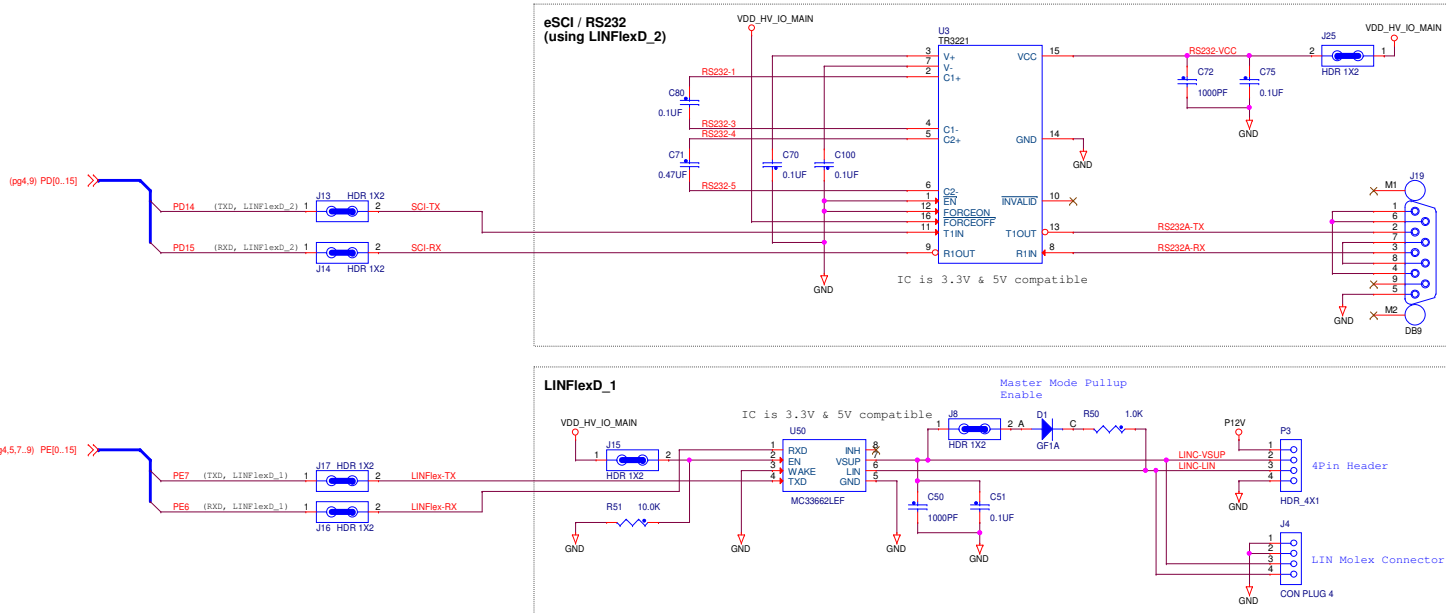


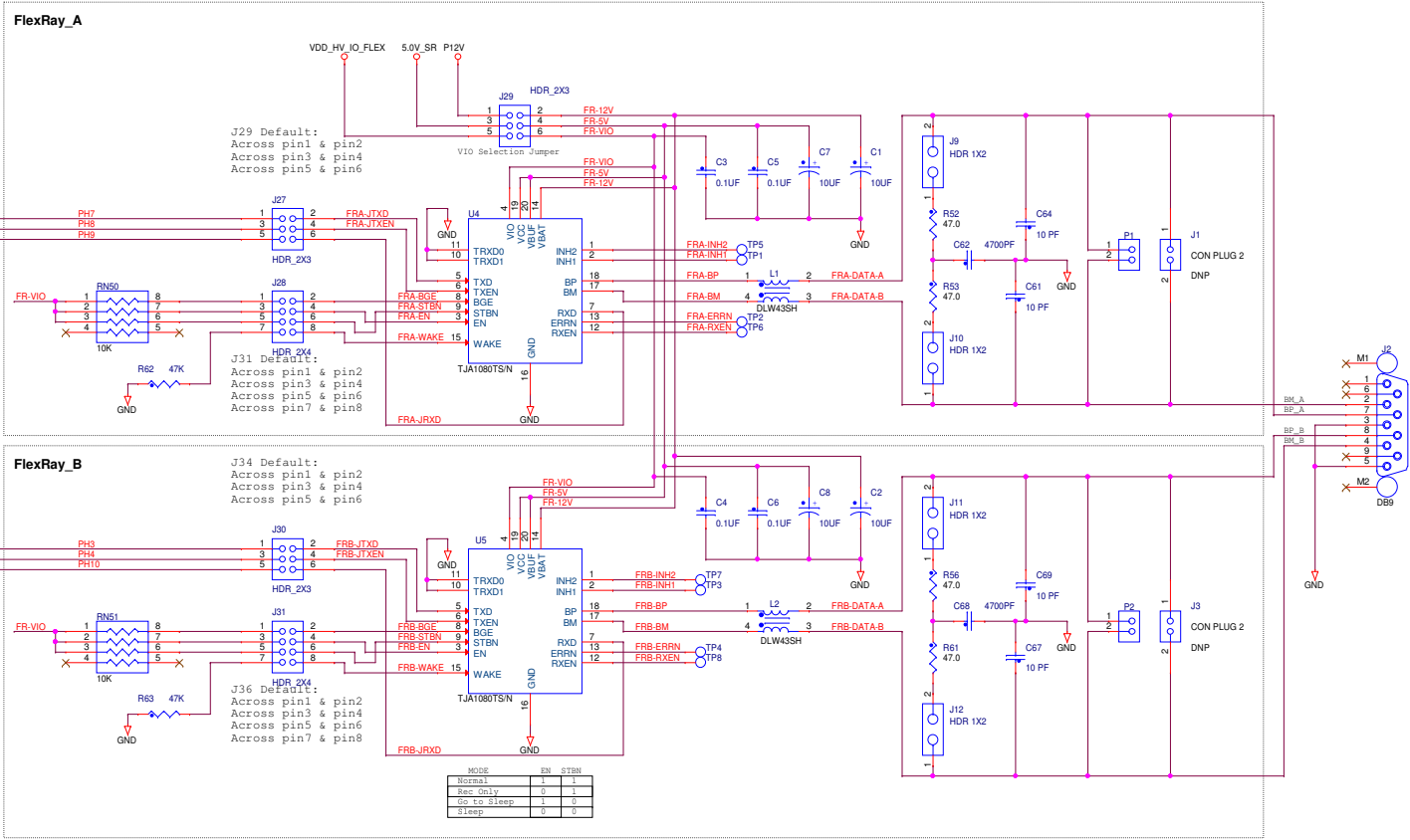
RST-SW is the signal coming from the reset switch (on daughter card). RST-MCU is connected to ESR0 and PORST via jumper (on daughter card).



Freescale logo and document information including ICAP Classification, Drawing Title (MPC57xx MOTHER BOARD), Page Title (Mother Board connector), and Date (Thursday, October 11, 2012).

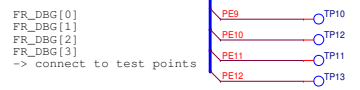






(pg4.9) PH[0..15]

(pg4.6.8.9) PE[0..15]



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Drawing Title: **MPC57xx MOTHER BOARD**

Page Title: **FlexRay**

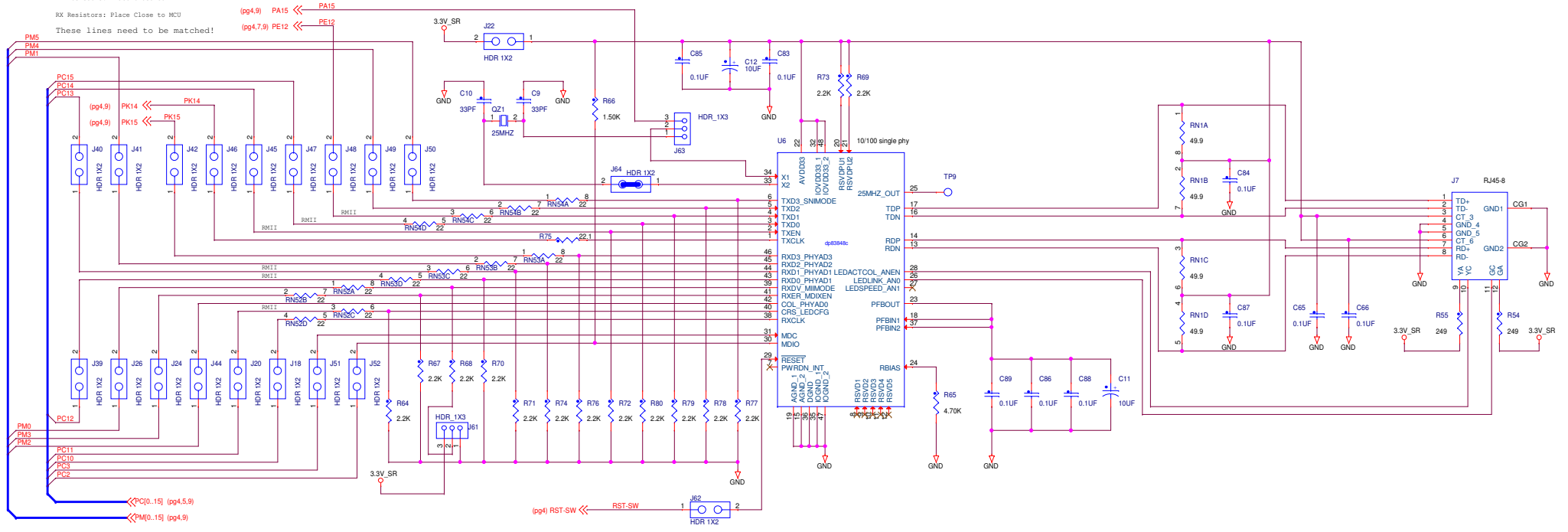
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Date: Thursday, October 11, 2012 Sheet 7 of 10

TX Resistors: Place Close to PI

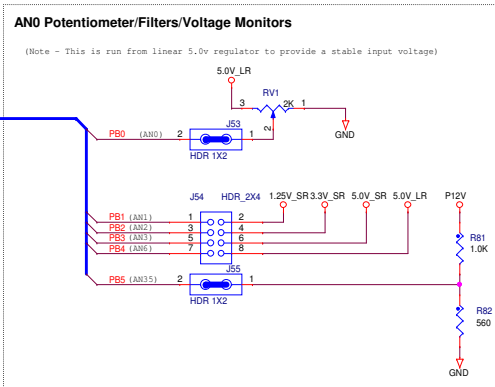
RX Resistors: Place Close to MCU

These lines need to be matched!









(pg4.9) PB[0..15]

Prototyping Area Still missing.  
To be added later.

