


Table of Contents

2	Notes
3	LCD CONNECTIONS
4	JM128

Revisions

Rev	Description	Date	Approved
A	Original Release	2/03/2010	M.N.
B	Parts changed: J3-J5, C19 and C30	2/23/2010	M.N.
C	Changes as per ECR04862	06/07/2010	M.N.

		Microcontroller Solutions Group 6501 William Cannon Drive West Austin, TX 78758-8598	
<small>This document contains information proprietary to Freescale Semiconductor and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of Freescale Semiconductor.</small>			
Designer: Michael Norman		ICAP Classification: FOP: PUB: FUC: X	
Drawn by: L&T		Page Title: LCD MODULE FOR TOWER	
Approved: Michael Norman		Document Number: SCH-26267 PDF-26267	
Date: Wednesday, June 09, 2010		Sheet 1 of 4	

1. Unless Otherwise Specified:

All resistors are in ohms, 5%, 1/8 Watt

All capacitors are in uF, 20%, 50V

All voltages are DC

All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:

_B Denotes - Active-Low Signal

<> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



ICAP Classification: FCP: FIUC: X PUBI:

Drawing Title: **LCD MODULE FOR TOWER**

Page Title: **NOTES**

Size C	Document Number SCH-26267 PDF:26267	Rev C
-----------	--	----------

Date: Wednesday, June 09, 2010 Sheet 2 of 4

