

Using DMA and SPI

When using DMA triggered by the SPI module in a requested automatic mode there are some premises we need to have in mind:

- The module ignores attempts to push data to a full TX FIFO;
- When the FIFO is not completely full a transfer from the DMA is requested;
- To ensure the synchrony between the dSPI with the DMA the transfers to the PUSHR must be of 4 bytes, this includes 2 bytes command and 2 bytes data, and the number of iterations of the DMA must be equal to the number of elements in the source buffer.