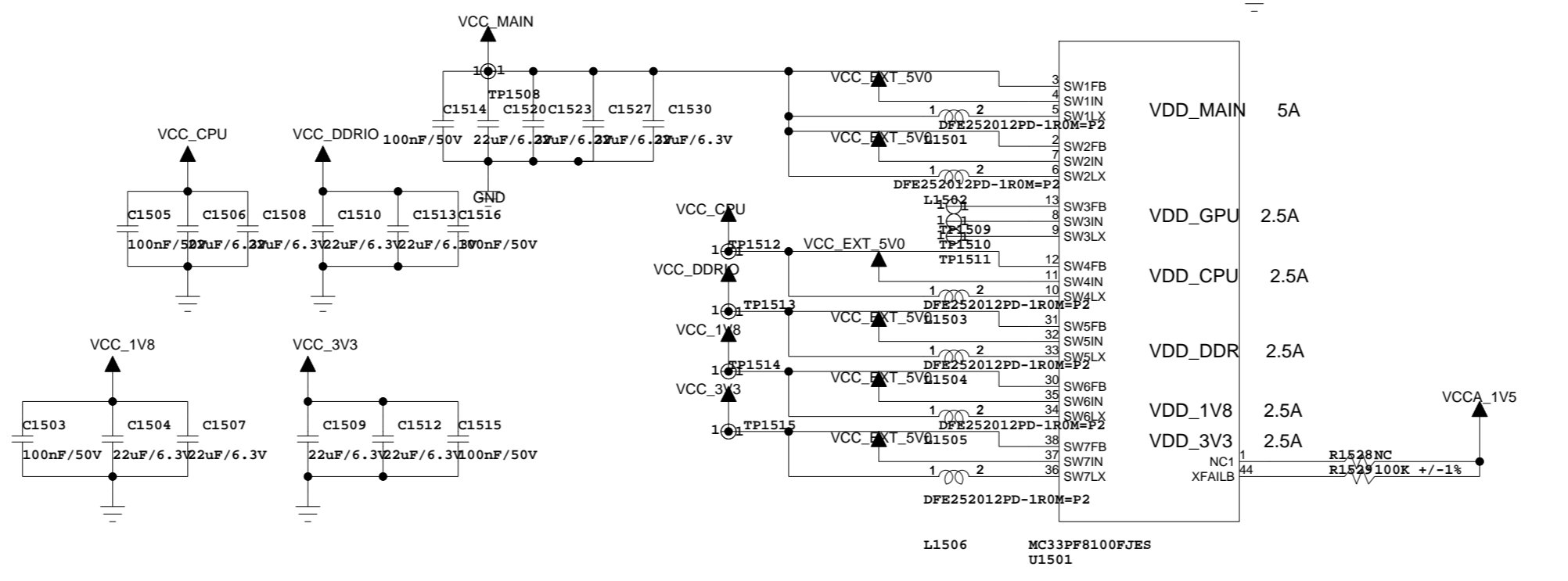
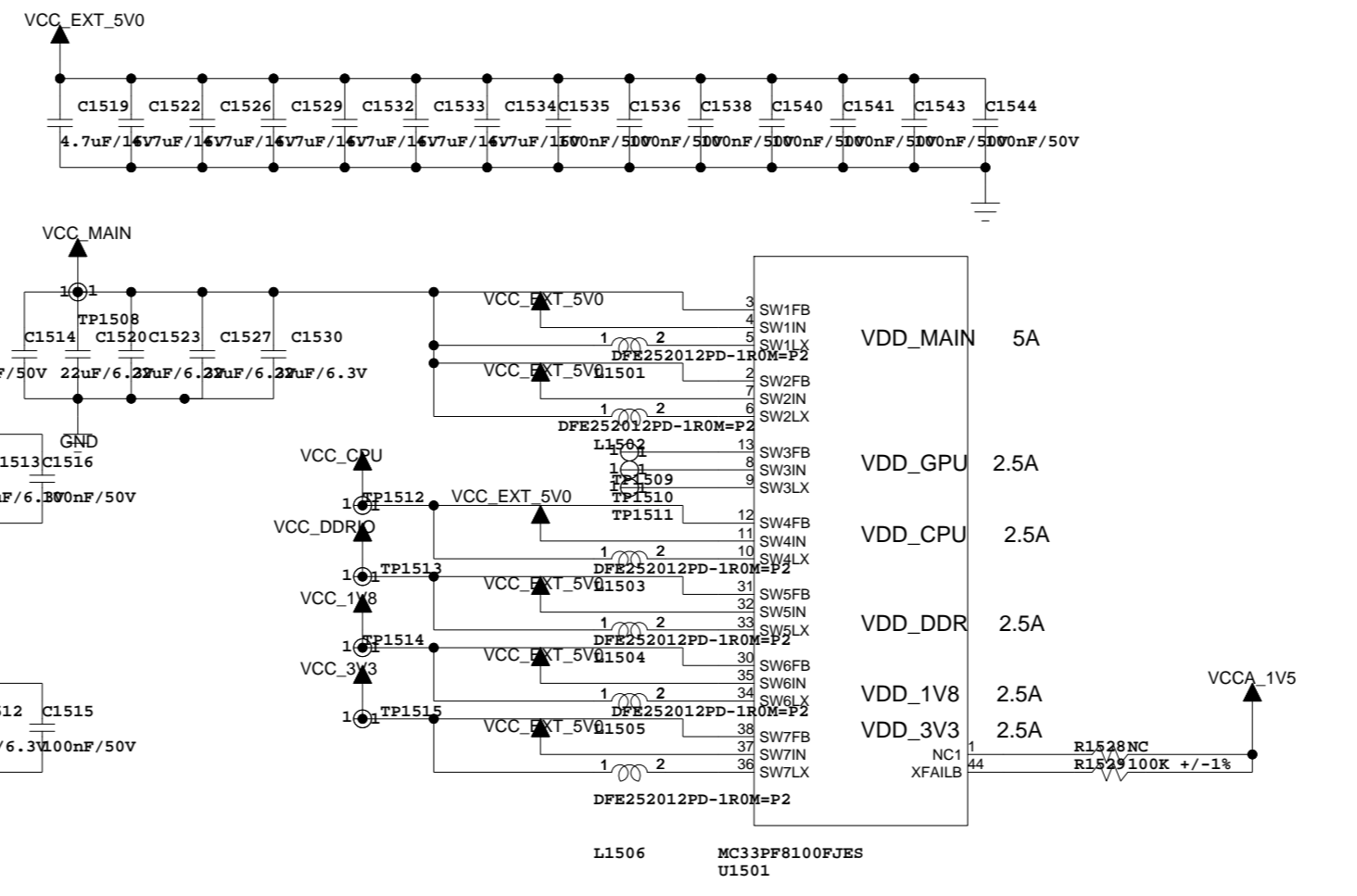
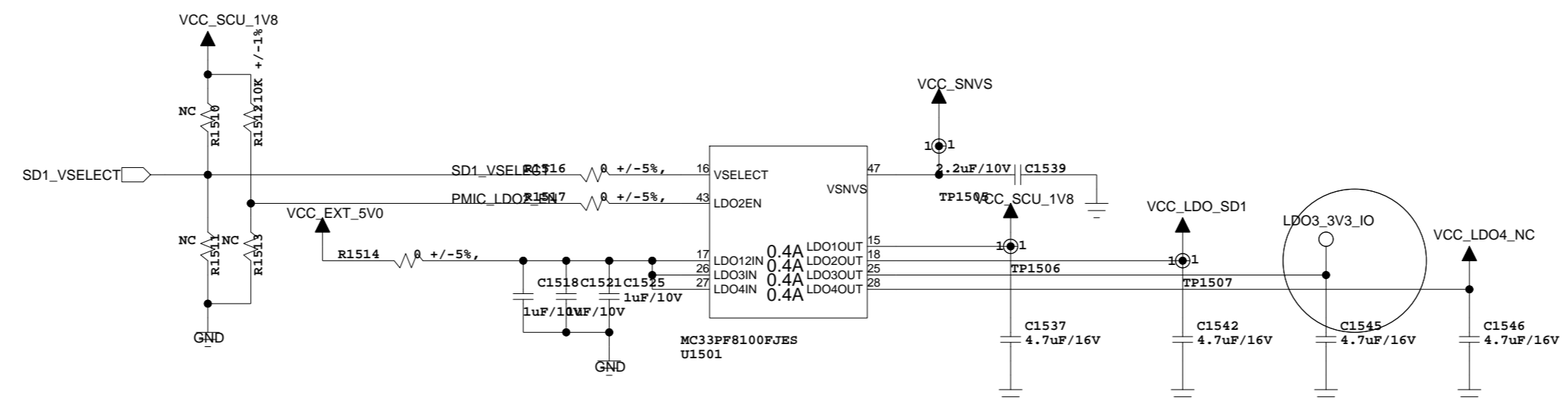
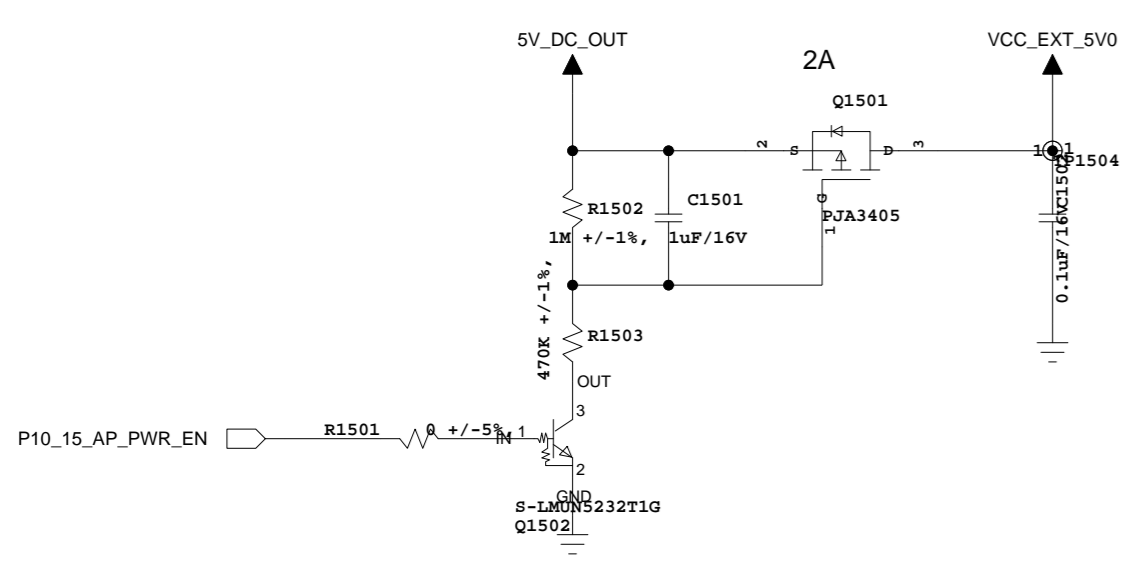
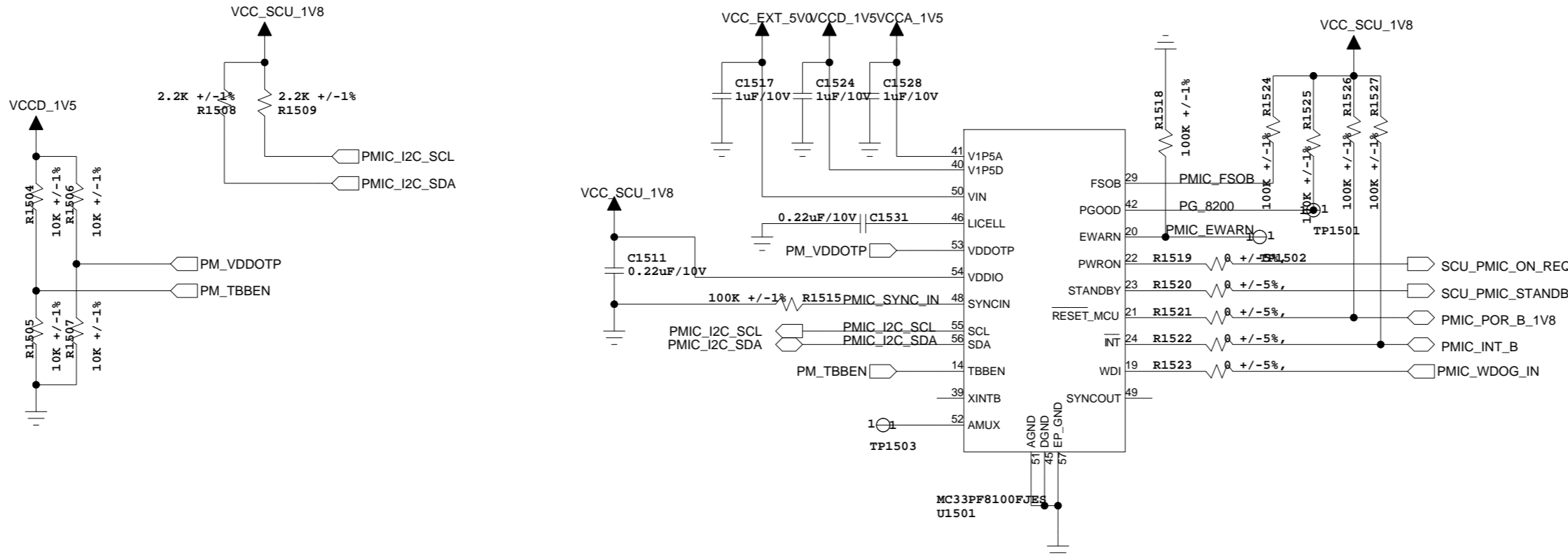
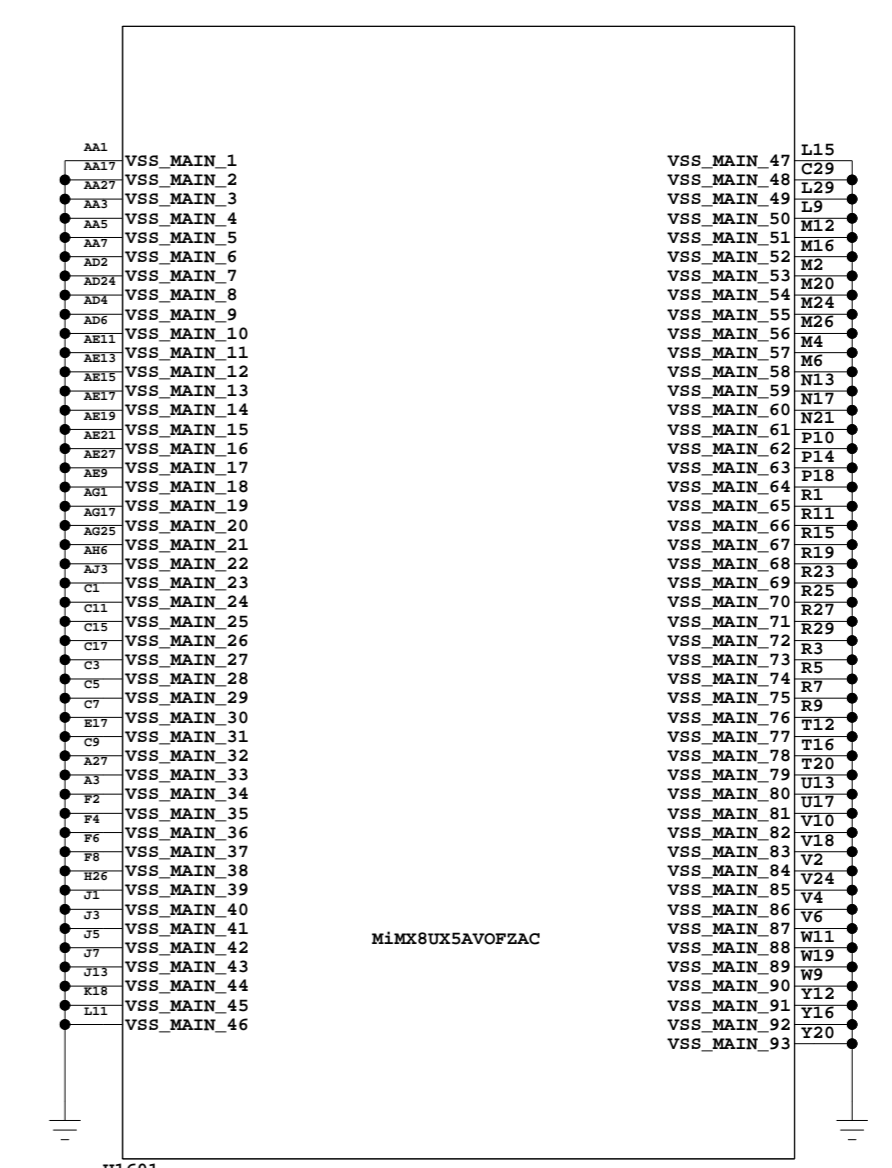
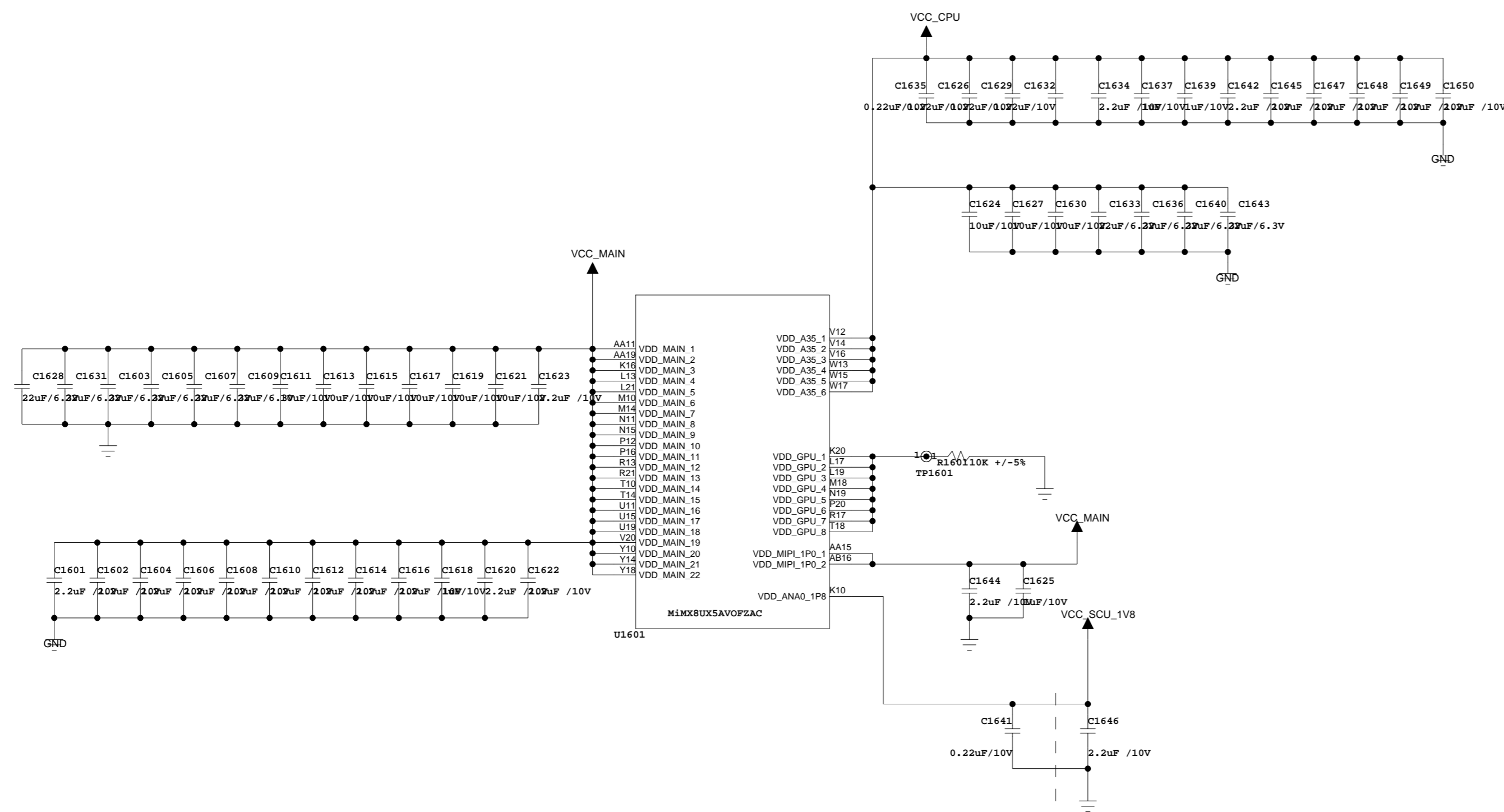
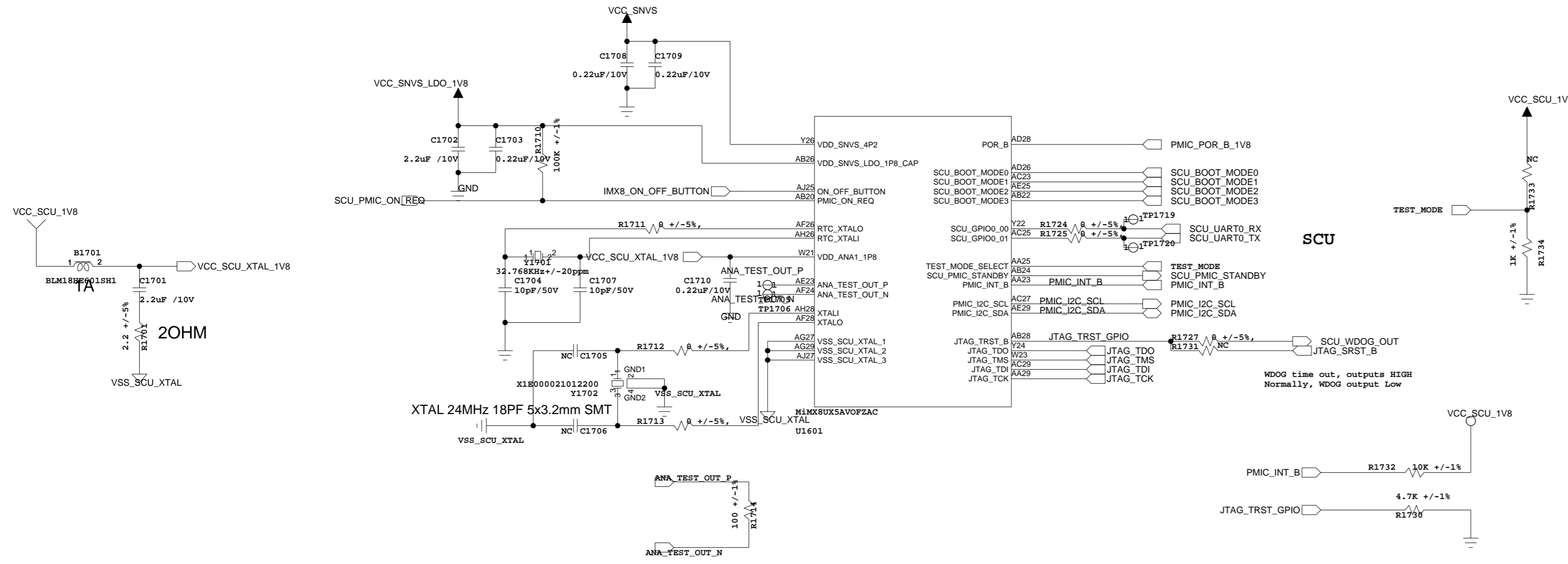


PMIC POWER SUPPLY

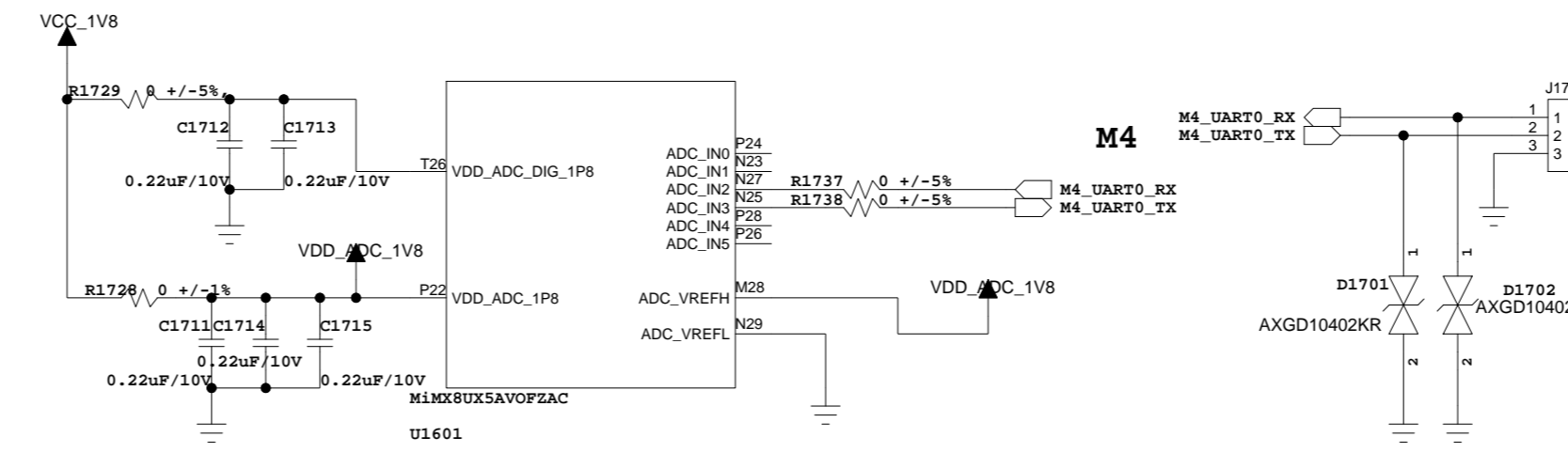
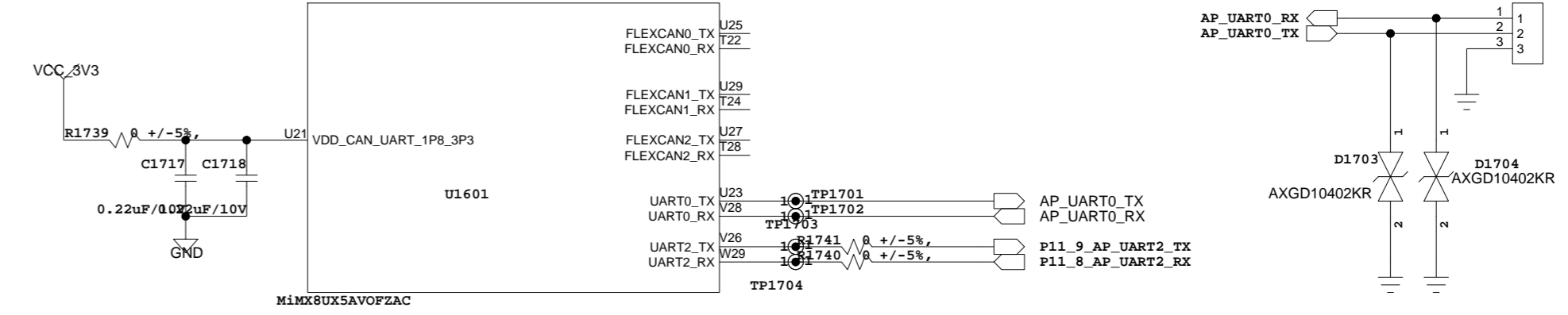




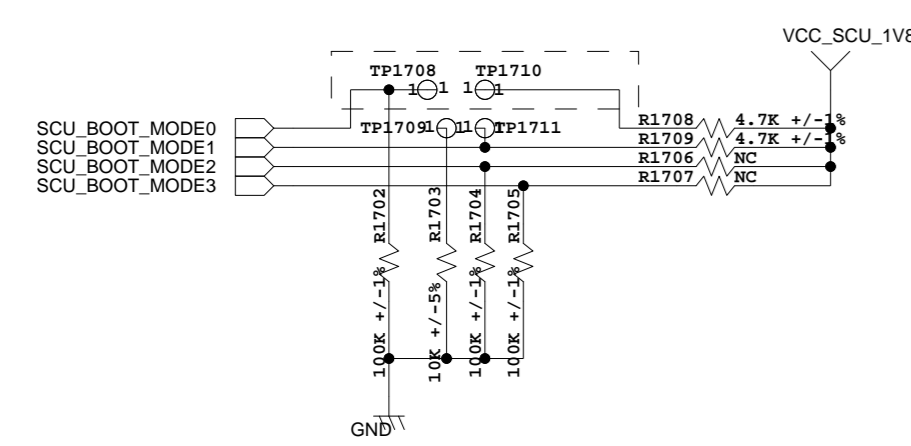
i.MX8DX BOOT



UART / CAN

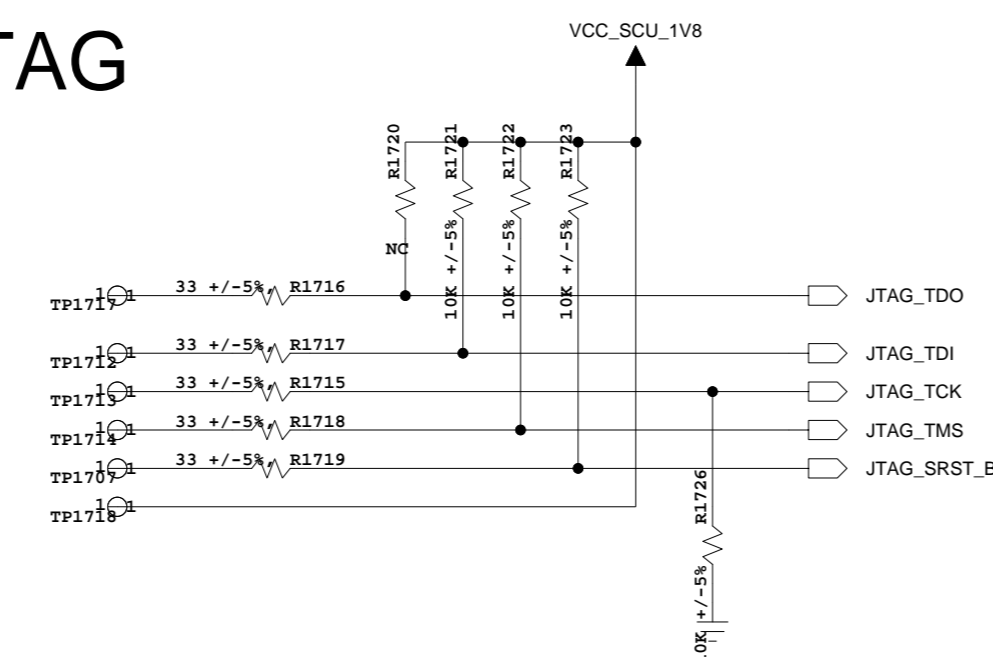


BOOT Selection



- SCU_Boot_Mode 3 2 1 0
- BOOT From Fuse ----- 0 0 0 0
- Serial Download ----- 0 0 0 1
- eMMC0 ----- 0 0 1 0
- SD1 boot ----- 0 0 1 1
- NAND 8-bit 128page---- 0 1 0 0
- NAND 8-bit 32page---- 0 1 0 1
- QSPI 3B READ ----- 0 1 1 0
- QSPI Hyperflash3.3V--- 0 1 1 1
- SCU_PRIVATE_BOOT_I2C- 1 1 0 0
- Reserved ----- 1 1 0 1
- Infinite LoopMode---- 1 1 1 0
- TEST MODE ----- 1 1 1 1

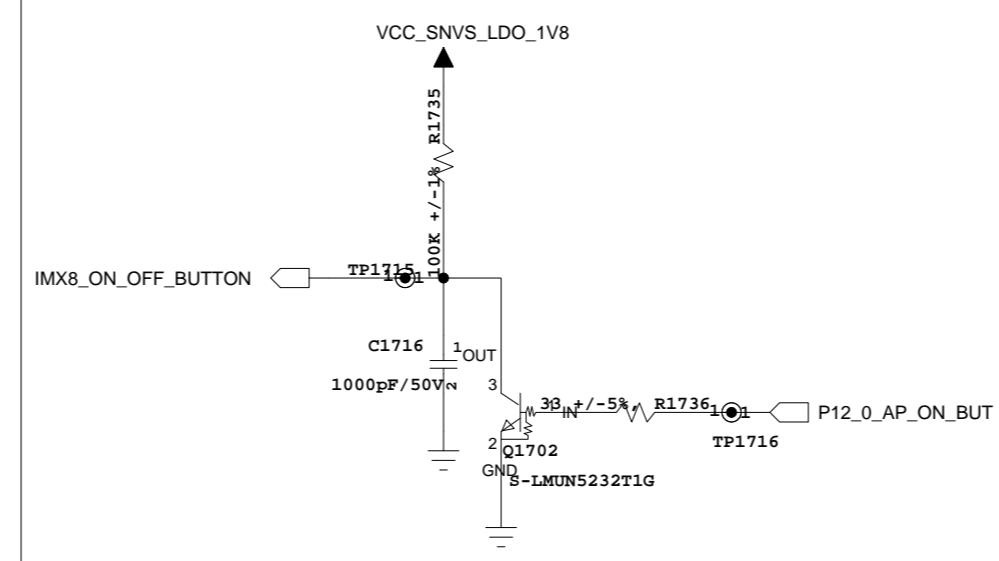
JTAG



MX8QM On-Chip 50 kohm Pulls

 JTAG_TMS = PU
 JTAG_TCK = PD
 JTAG_TDI = PU
 TEST_MODE_SELECT = PD

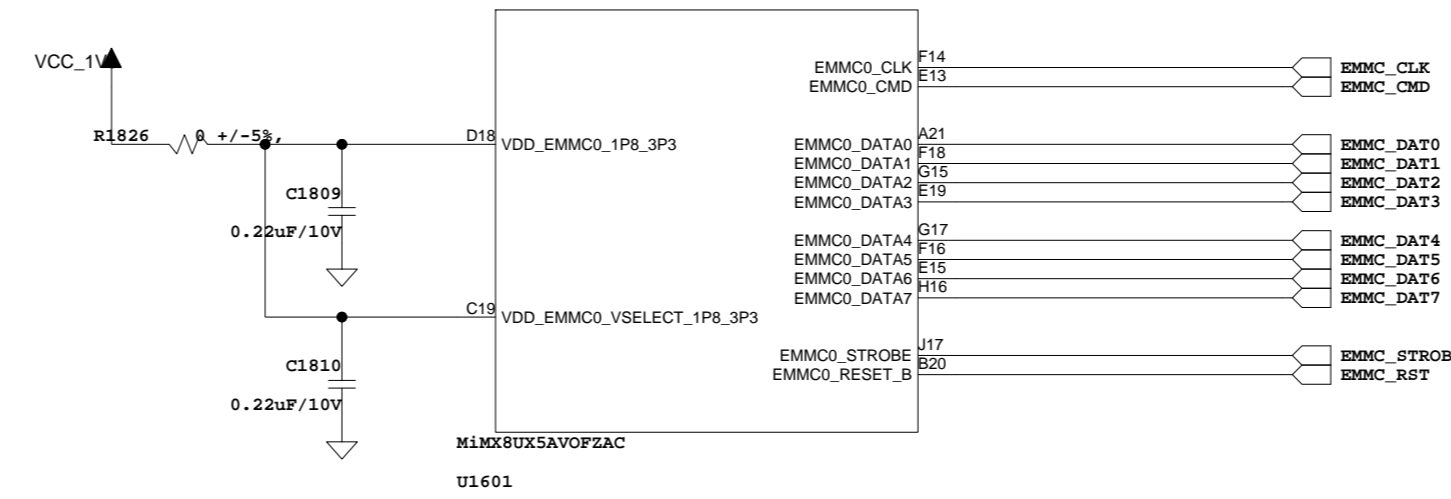
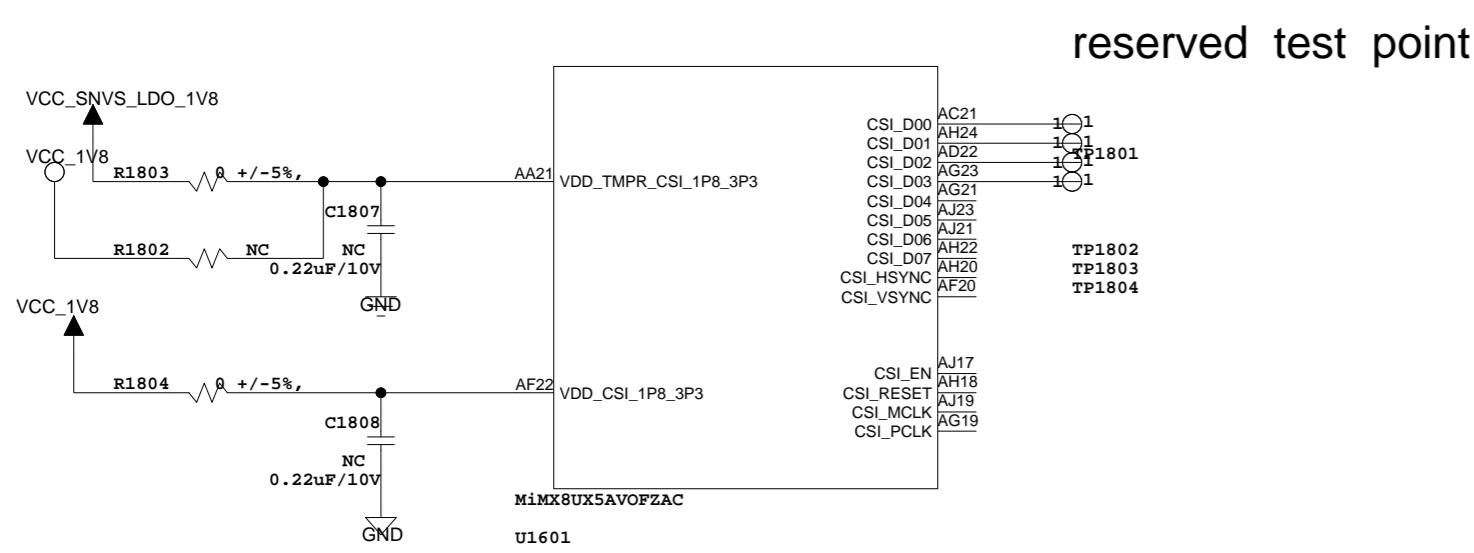
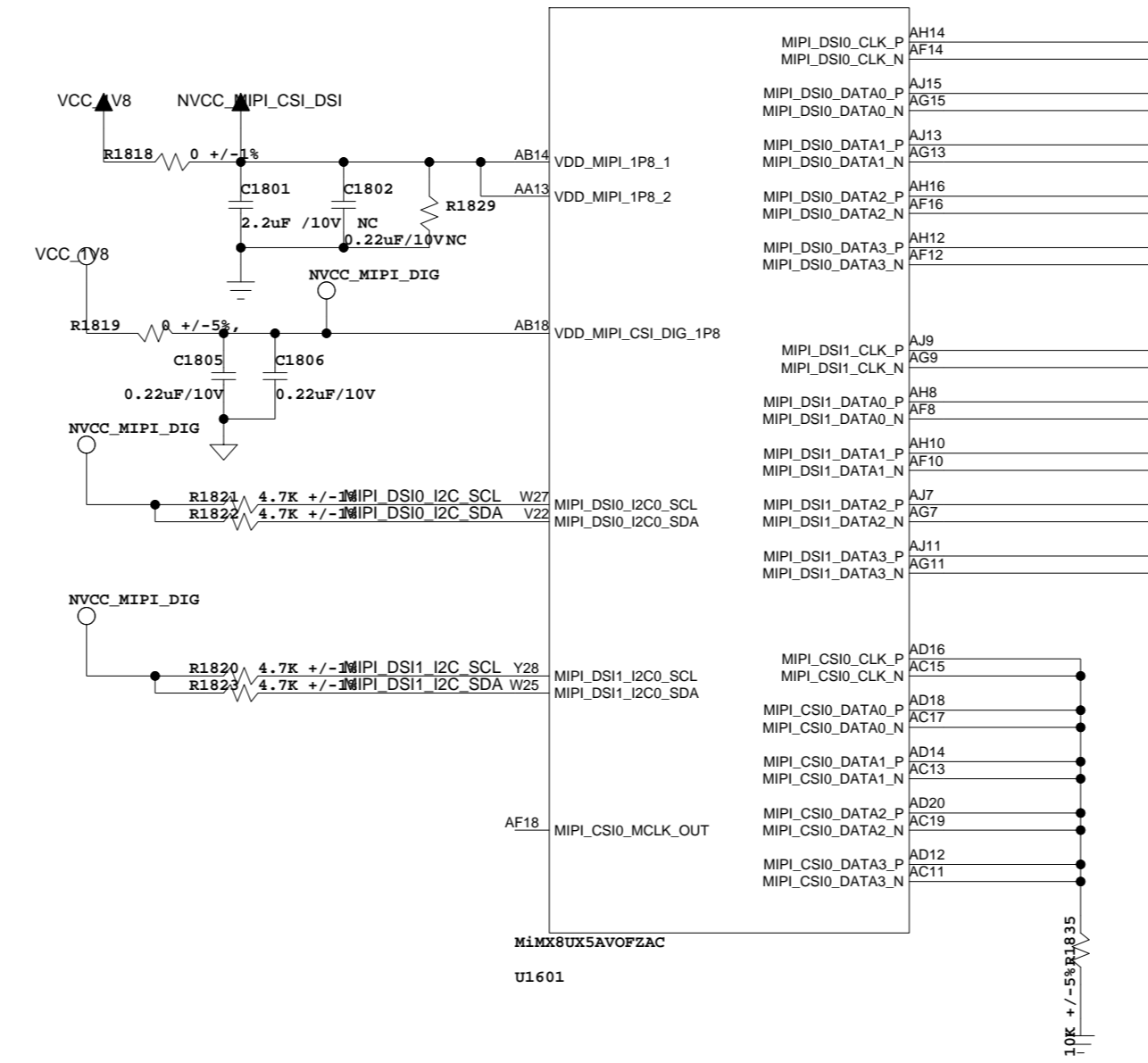
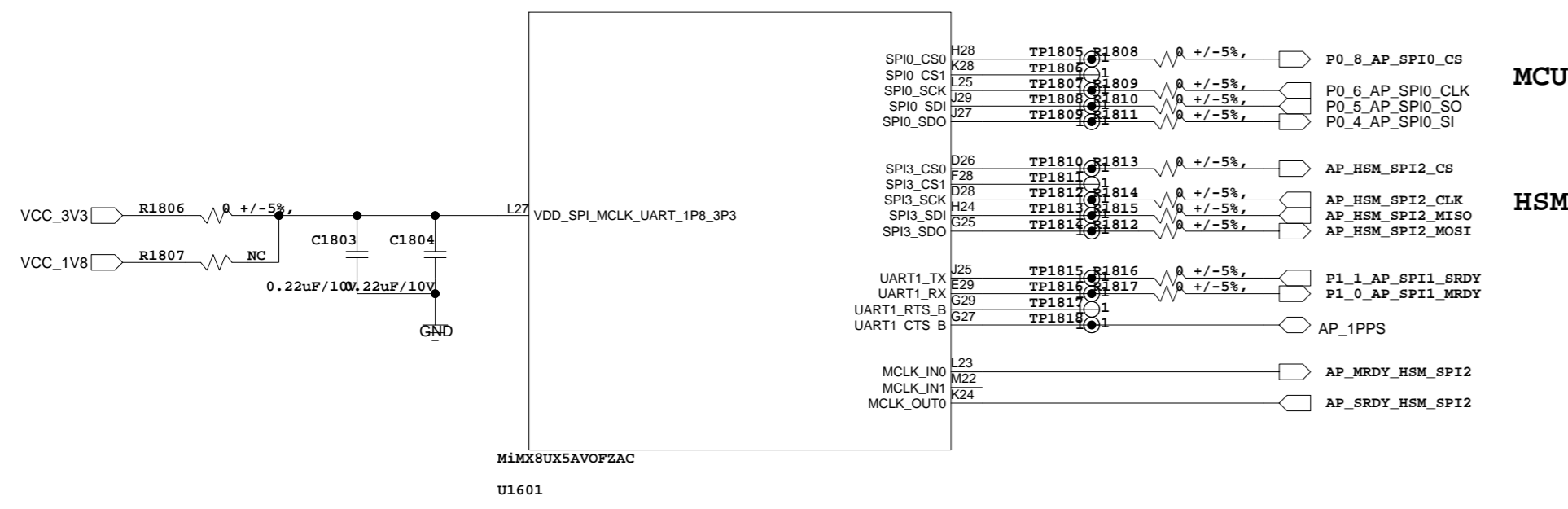
Power On Switch



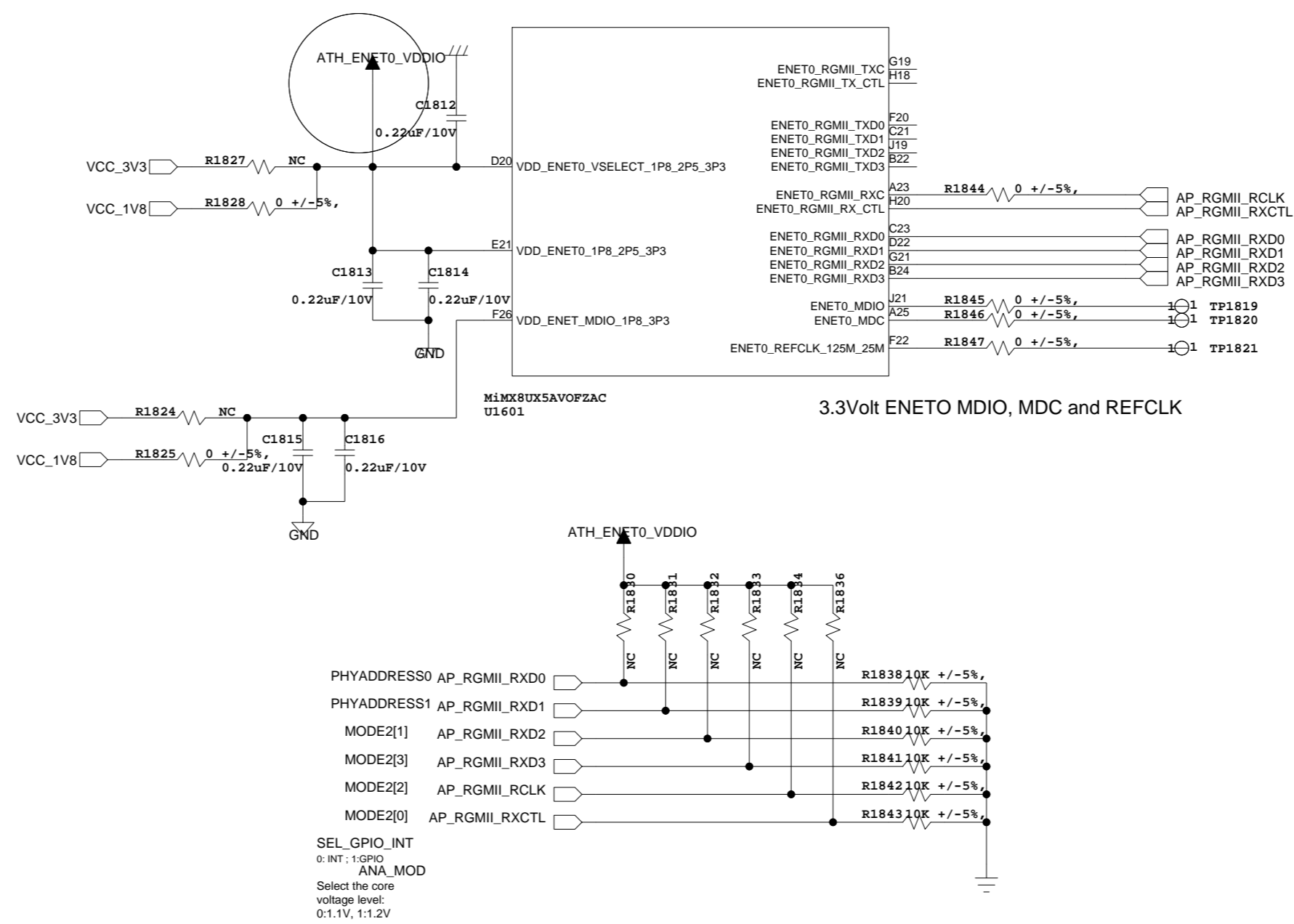
Note: Hold for 5 sec for force off.
 Hold for 0.5 sec to turn on

SCU_WDOG_OUT --- R1744 --- PMC_WDOG_IN

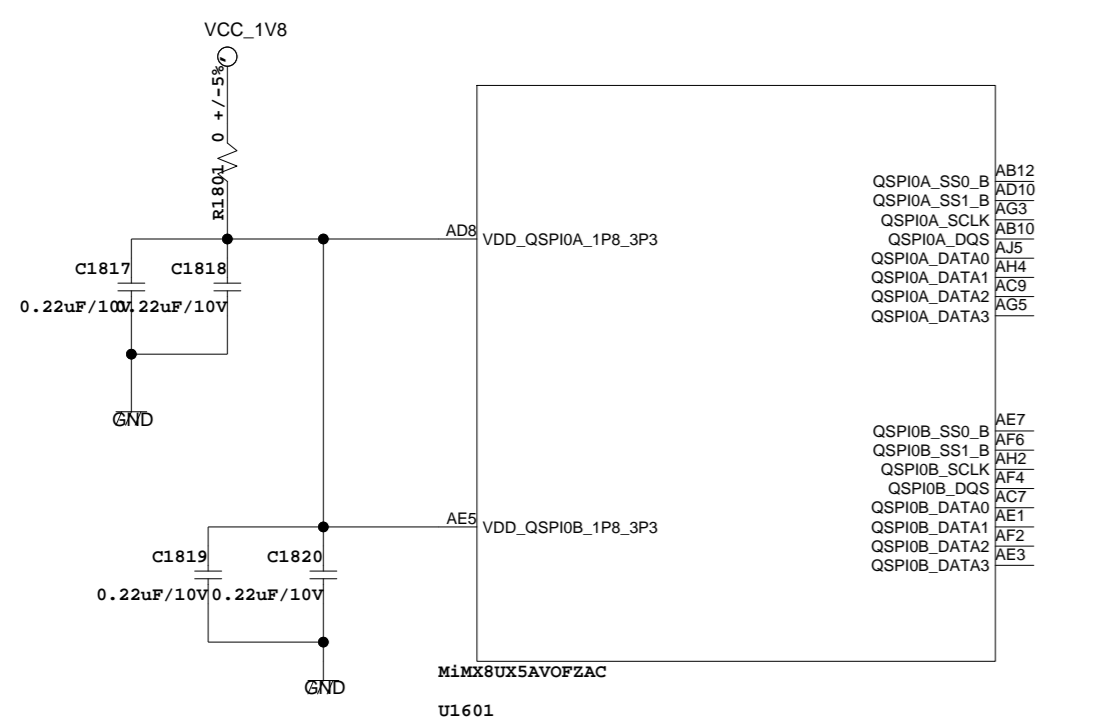
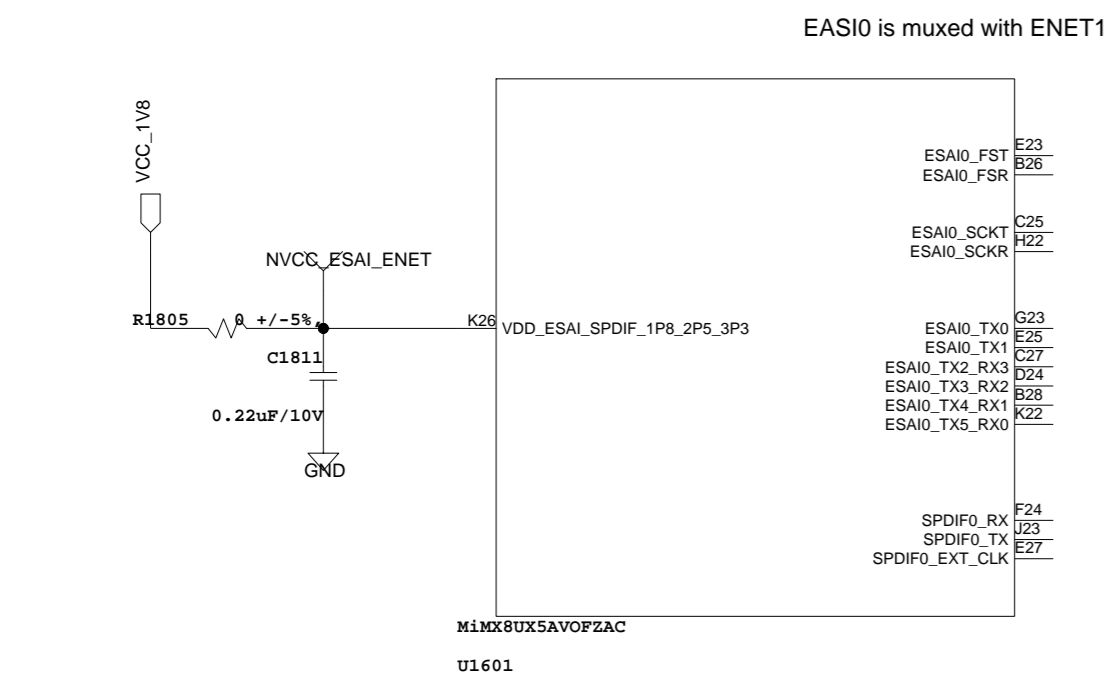
MIPI DSI/CSI CONNECTOR



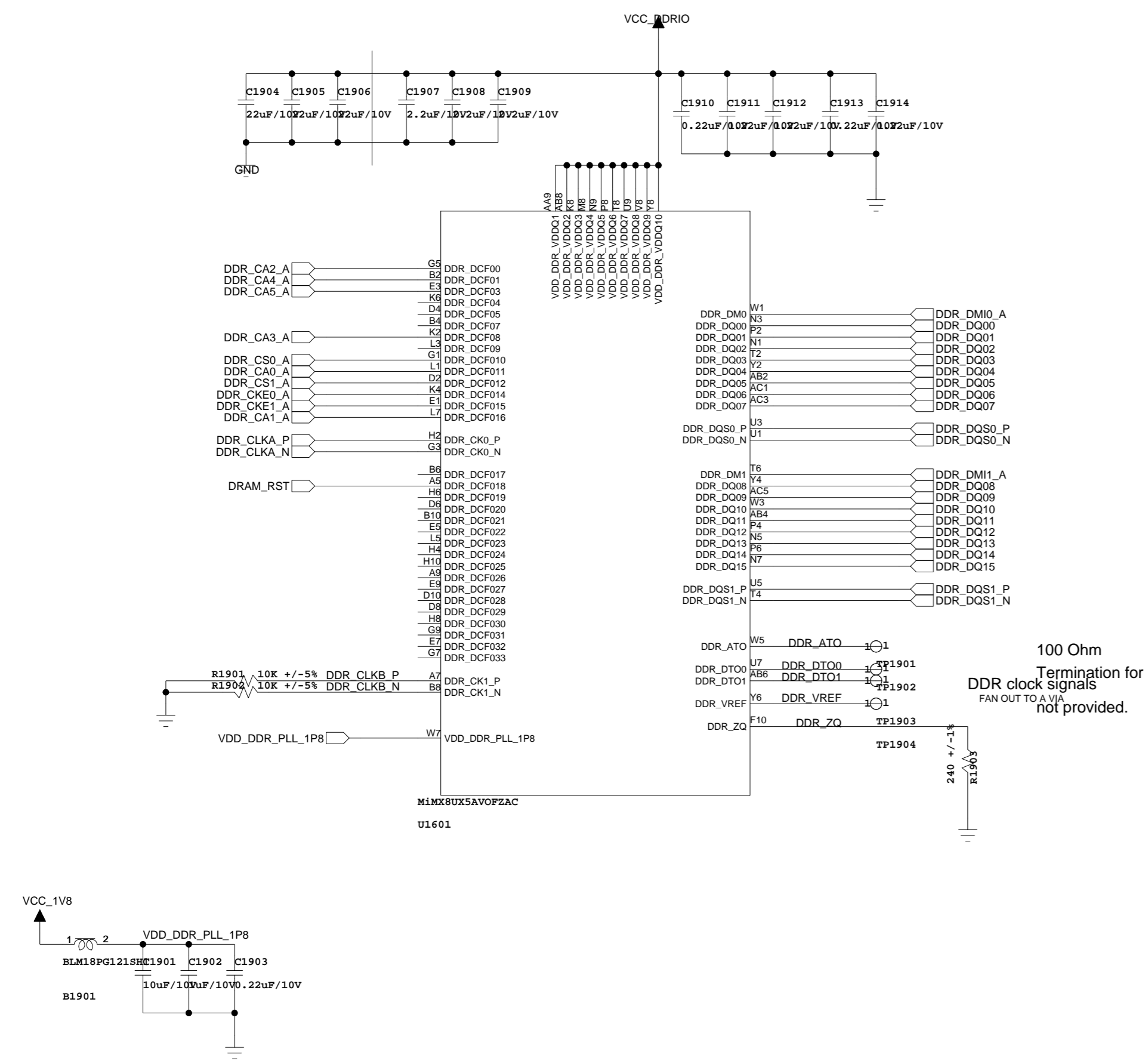
ETHERNET PHY



ESAI0 Interface

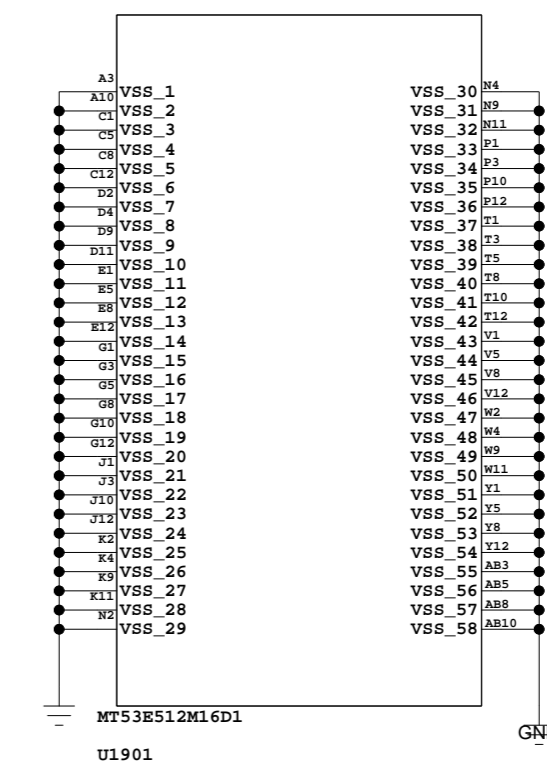
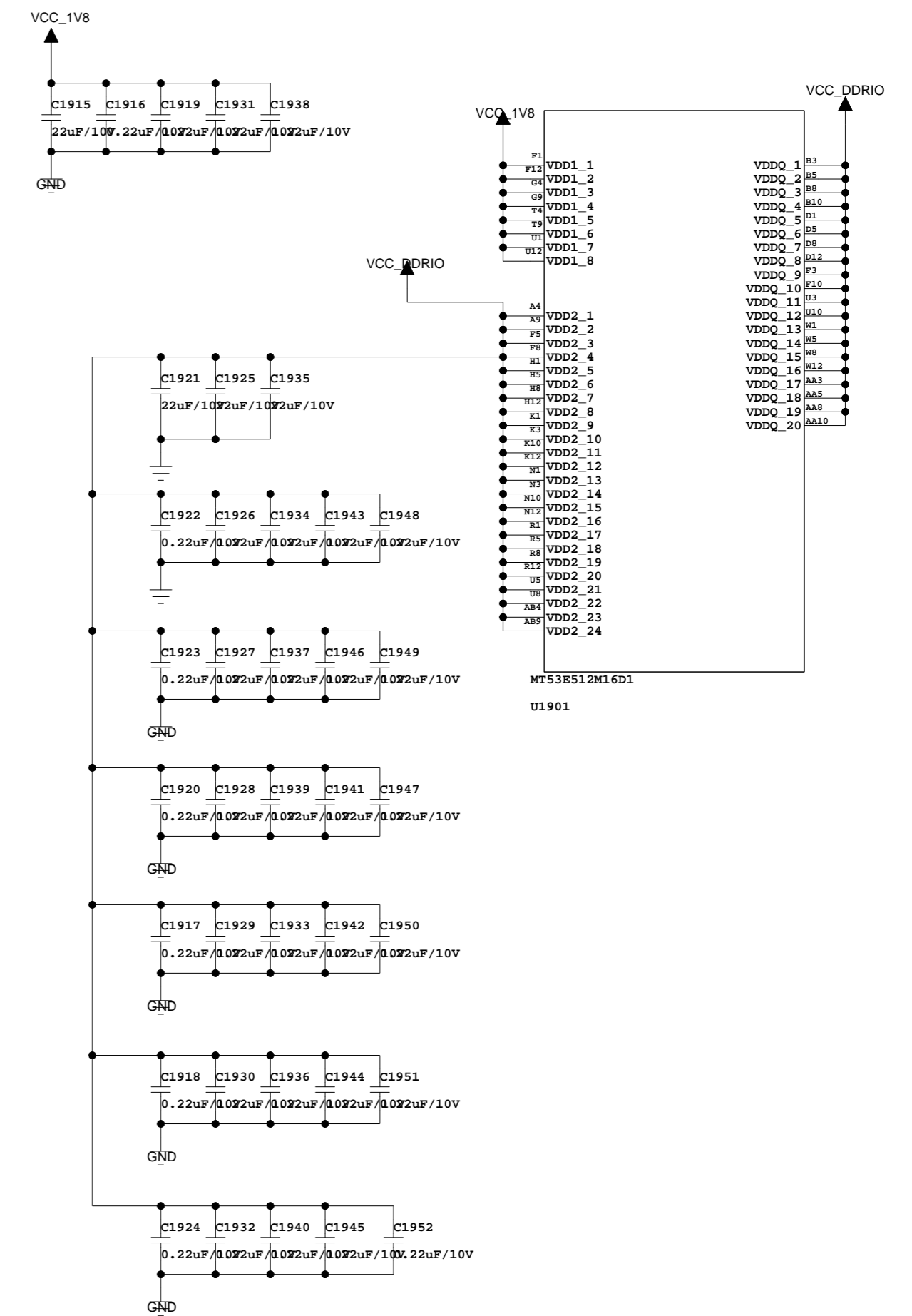
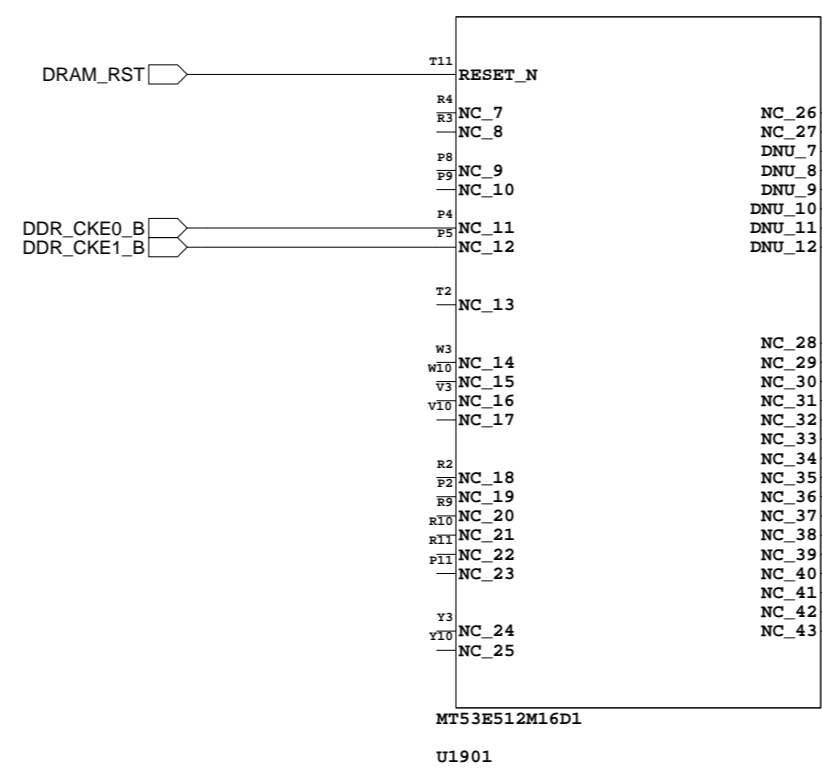
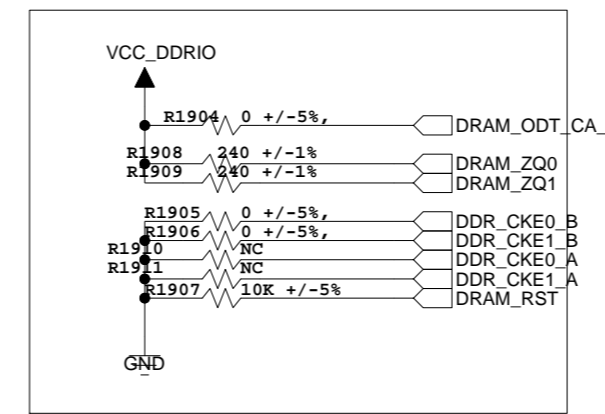
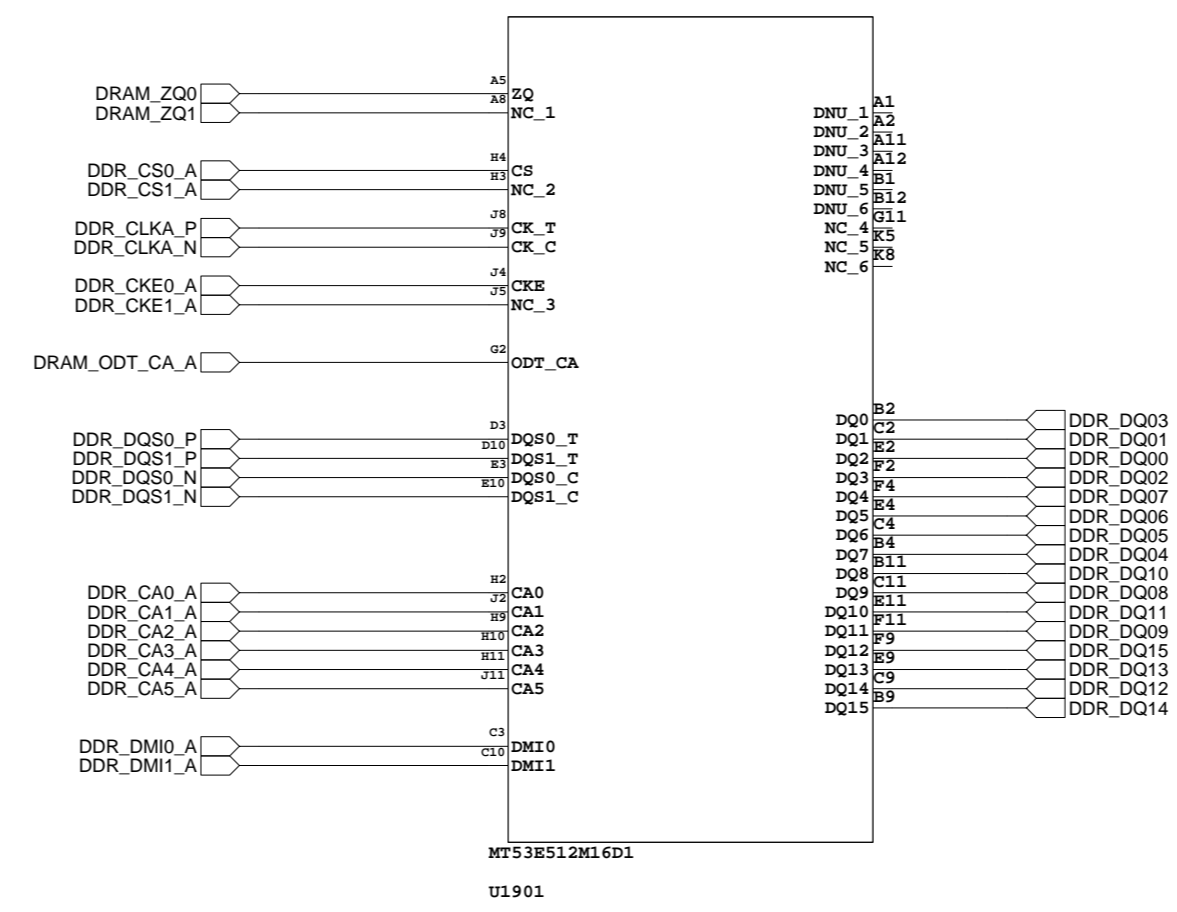


LPDDR4 DRAM

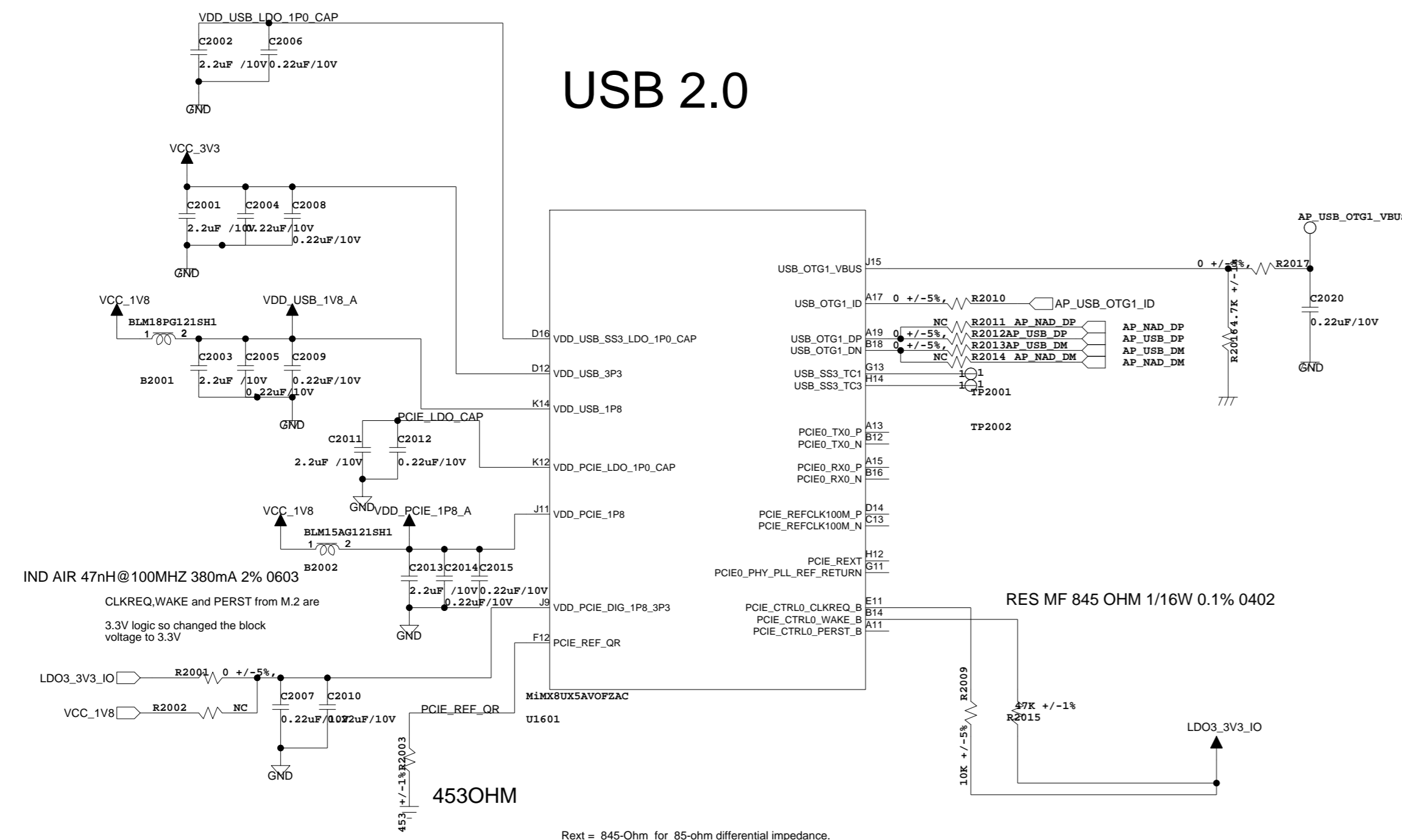


Pullup ODT_CA_A/B on LPDDR4 memory.
Do not connect this pin to processor

100 Ohm Termination for DDR clock signals not provided.
FAN OUT TO A WIP



USB 2.0



RES MF 845 OHM 1/16W 0.1% 0402
 Rext = 845-Ohm for 85-ohm differential impedance.
 Rext = 909-Ohm for 90-ohm differential impedance.
 Rext = 1.02kOhm for 100-ohm differential impedance.

/OR	S	Function
L	L	D=1D
L	R	D=2D

