

i.MX 6 DDR Stress Tester Release Notes

1 Overview

- This document contains important information about the package contents, supported features, and known issues/limitations.

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2 Release Contents

This release package consists of the following package file:

- DDR_Stress_Tester_V1.0.2.zip

Table 1 shows the files available in DDR_Stress_Tester_V1.0.2.zip.

Table 1. DDR_Stress_Tester_V1.0.2.zip

File/Folder	Description
Binary\DDR_Stress_Tester.exe	Executable file which runs on PC host to download the test image to the target board.
Binary\ddr-stress-test-mx6dl.bin	Test image which runs on i.MX6DL or i.MX6S board and do the DDR calibration and stress test.
Binary\ddr-stress-test-mx6dq.bin	Test image which runs on i.MX6Q or i.MX6D board and do the DDR calibration and stress test.
Binary\ddr-stress-test-mx6sl.bin	Test image which runs on i.MX6SL board and do the DDR calibration and stress test.
Binary\scripts\MX6_series_boards	This folder contains the DDR initialization scripts which are for FSL reference boards
Documentation	This folder contains the following document: <ul style="list-style-type: none">• i.MX6_DDR_Stress_Tester_V1.0.2_Release_Notes• i.MX6_DDR_Stress_Tester_V1.0.2_User_Guide

3 Basic Features

The following are the features supported:

- Support i.MX6Q, i.MX6D, i.MX6DL, iMX6S, and i.MX6SL DDR calibration.
- Support DDR3 write leveling, DQS gating, Read/Write Delay auto-calibration.
- Support LPDDR2 Read/Write Delay auto-calibration.
- Support 16 bits, 32 bits, and 64 bits data bus.
- Support fixed-mapping 2-channel LPDDR2.
- Support DDR stress test between the frequency 135MHz and 672 MHz

4 Know issues

The following features are not supported in the current release:

- 4KB interleaving mode 2-channel LPDDR2.

5 Defect Fixes

The following are the main defect fixes:

- BYTE 3 calibration result is not accurate for 64 bits data bus.

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