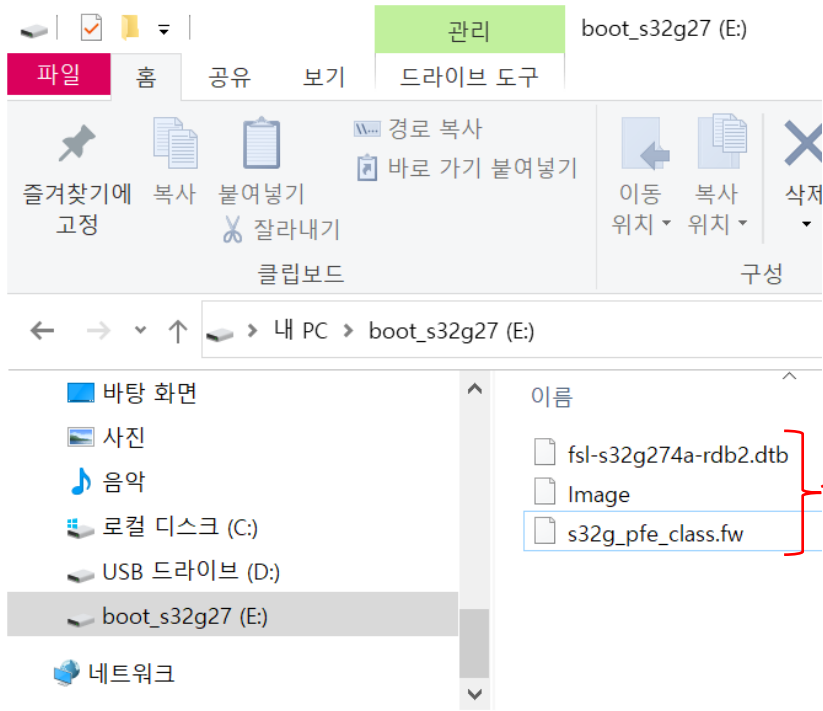


1. I can boot up using the SD card in S32G-VNP-RDB2 board.



<SD card>

```
COM7 - PuTTY
U-Boot 2020.04+geef88755a7 (Mar 03 2021 - 07:18:34 +0000)

CPU: NXP S32G274A rev. 2.1.0
Reset cause: Power-On Reset
Model: NXP S32G2XX
Board: NXP S32G274A-RDB
DRAM: 3,5 GiB
CA53 core 1 running.
CA53 core 2 running.
CA53 core 3 running.
All (4) cores are up.
MMC: FSL_SDHC: 0
Loading EnVironment from MMC... OK
Using external clock for PCIe0
Configuring PCIe0 as RootComplex(x2)
Using external clock for PCIe1
Frequency 125Mhz configured for PCIe1
Configuring PCIe1 as SGMII(x2) [XPCS0 2.5G, XPCS1 OFF]
PCIe0: Failed to get link up
Pcie0: LINK DBG 1: 0x00000000, LINK DBG 2: 0x00000800 (expected 0x000000d1)
DEBUG_R0: 0x00630700, DEBUG_R1: 0x05200000
PCI: Failed autoconfig bar 20
PCI: Failed autoconfig bar 24
PCIe1: Not configuring PCIe, PHY not configured
In: serial
Out: serial
Err: serial
Board revision: RDB2/GLDBOX Revision C
Net: EQOS phy: rgmii @ 1

Warning: eth_eqos (eth0) using random MAC address - 96:24:77:69:7e:77
eth0: eth_eqos PFE: emac0: sgmi emac1: none emac2: rgmii

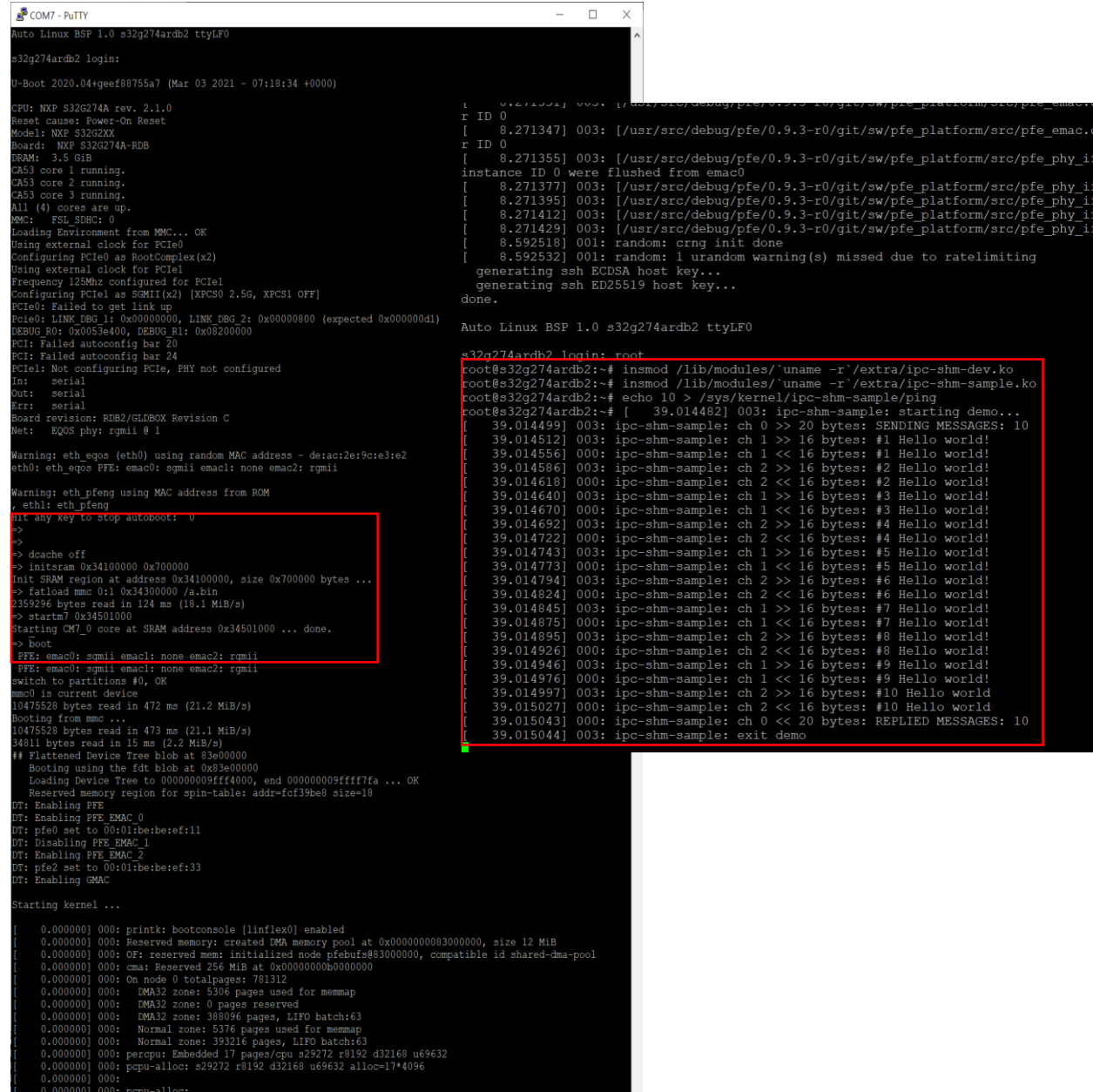
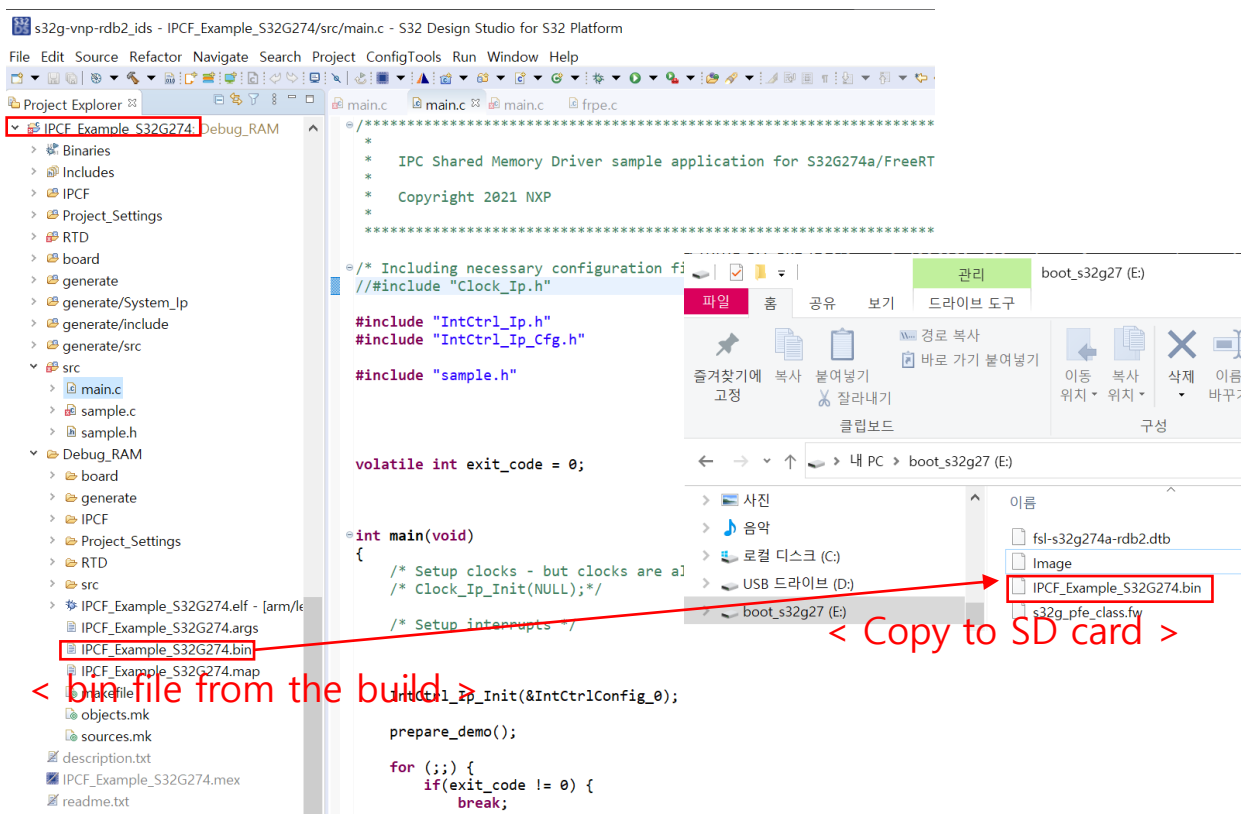
Warning: eth_pfeng using MAC address from ROM
, eth1: eth_pfeng
Hit any key to stop autoboot: 0
PFE: emac0: sgmi emac1: none emac2: rgmii
PFE: emac0: sgmi emac1: none emac2: rgmii
switch to partitions #0, OK
mmc0 is current device
10475528 bytes read in 457 ms (21.9 MiB/s)
Booting from mmc ...
10475528 bytes read in 457 ms (21.9 MiB/s)
34811 bytes read in 14 ms (2.4 MiB/s)
## Flattened Device Tree blob at 83e00000
Booting using the fdt blob at 0x83e00000
Loading Device Tree to 000000009fff4000, end 000000009ffff7fa ... OK
Reserved memory region for spin-table: addr=fcf39be8 size=18
DT: Enabling PFE
DT: Enabling PFE_EMAC_0
DT: pfe0 set to 00:01:be:be:ef:11
DT: Disabling PFE_EMAC_1
DT: Enabling PFE_EMAC_2
DT: pfe2 set to 00:01:be:be:ef:33
DT: Enabling GMAC

Starting kernel ...

[ 0.000000] 000: printk: bootconsole [linflex0] enabled
[ 0.000000] 000: Reserved memory: created DMA memory pool at 0x0000000083000000, size 12 MiB
[ 0.000000] 000: OF: reserved mem: initialized node pfebufs@83000000, compatible id shared-dma-pool
```

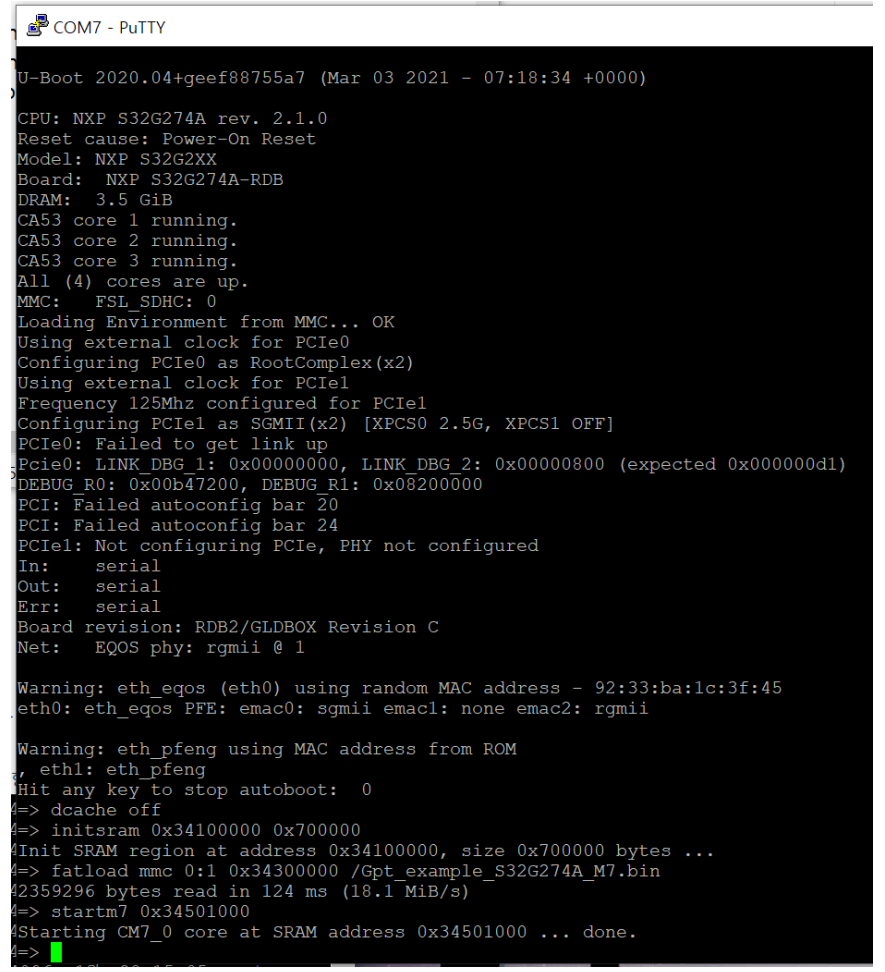
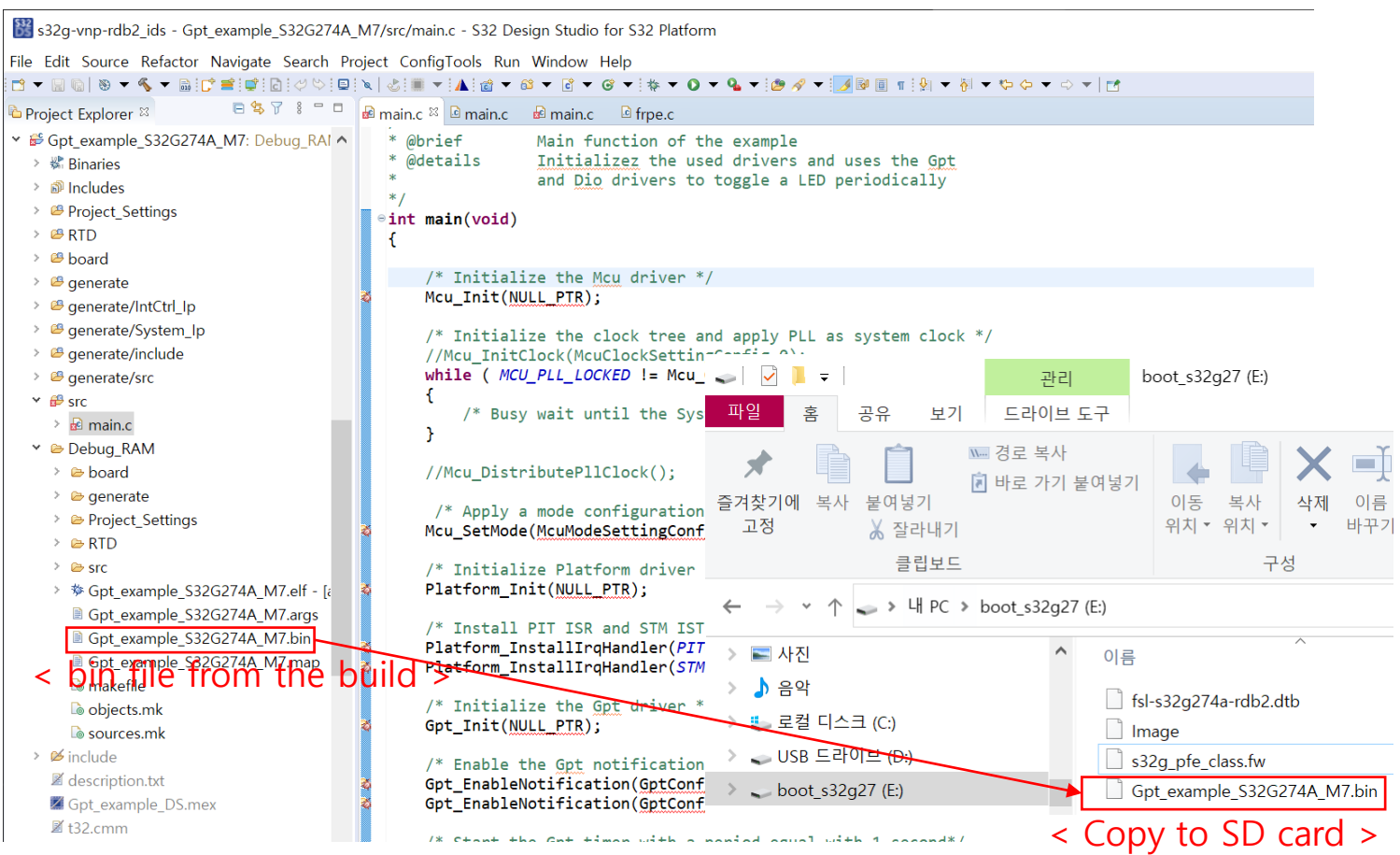
< boot screen from SD card >

2. Create a Bin file using IPCF\_Example.  
 Add it to the SD card, boot it, and check the operation of the M7 core.  
 => It's OK



< Check the operation of IPCF Example by booting from SD card and entering according to the manual. >

- In the same way, create a Bin file using Gpt\_Example, add it to the SD card, boot it, and check the operation of the M7 core.
    - Match the .ld file with IPCF\_Example's .ld file.
    - I checked the init\_vector address in the .map file.
    - It was confirmed that it works by uploading the elf file to the Ram using the Trace32 debugger without booting to the SD card in advance.
    - Add bin file to SD card and boot.
    - Enter Startm7 0x34501000.
    - I See that "Starting CM7\_0 core at SRAM address 0x34501000 ... done ." and then I have to input "boot".
- But the screen freezes, so input of "boot" is not possible.



<After typing Startm7 0x34501000, the command window hangs and "boot" input is not possible.>