General fields for describing the i.MX board design.

Tool Version	IOMux Tool (IOMux.exe) version 3.4.0.3
Board	
Board revision	
Version	
Company	
Contact	
Contact Email	
Copyright	/* * Copyright (C) 2012, [Your Company Here] All Rights Reserved. * IT IS EXPECTED THAT THIS TEXT BE REPLACED WITH THE BC * PROVIDER'S COPYRIGHT INFORMATION. THIS TEXT WILL BE * THE TOP OF ALL SOURCE FILES GENERATED FOR THIS BOARI */
Description	

Assign voltage levels to power pins.

CSI_REXT	0.000
DSI_REXT	0.000
HDMI_VPH	0.000
NVCC_CSI	0.000
NVCC_DRAM	0.000
NVCC_EIM0	0.000
NVCC_EIM1	0.000
NVCC_EIM2	0.000
NVCC_ENET	0.000
NVCC_GPIO	0.000
NVCC_JTAG	0.000
NVCC_LCD	0.000
NVCC_LVDS_2I	0.000
NVCC_MIPI	0.000
NVCC_NAND	0.000
NVCC_PLL	0.000
NVCC_RGMII	0.000
NVCC_SD1	0.000
NVCC_SD2	0.000
NVCC_SD3	0.000
PCIE_VPH	0.000
SATA_VPH	0.000
VDD_HIGH_CA	0.000
VDD_SNVS_CAI	0.000
VDD_SNVS_IN	0.000
VDD_USB_CAP	0.000

i.MX6DQ - <board> Rev. <board revision> (IOMux Design File Version:

[102 of 197 muxed pins in use] **4 conflicts!**

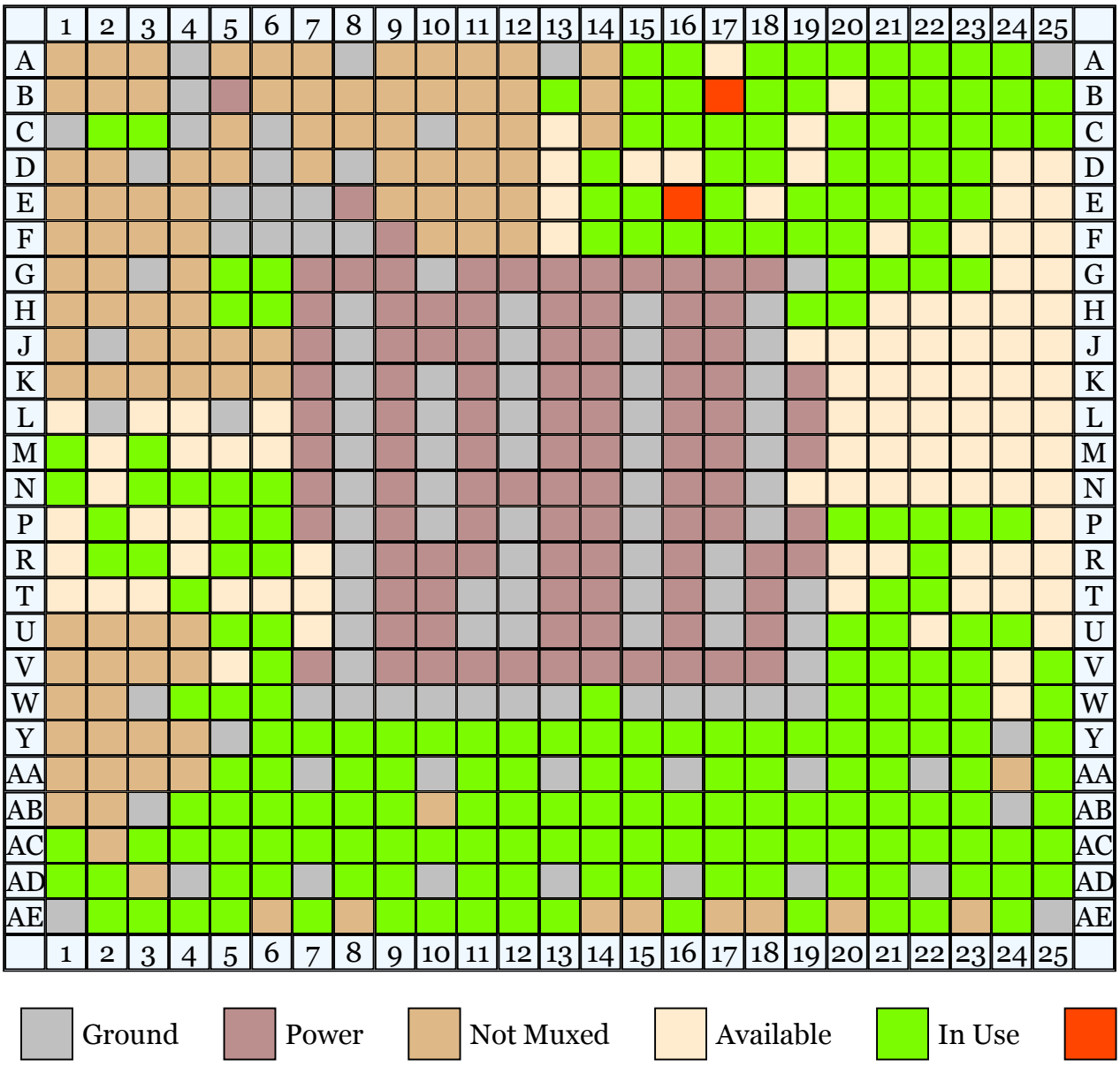
Peripheral/Signal	Ball	PadNet Name (Routing)	Power Group	GPIO
ecspi1 (7 of 8)				
ECSPI1_MISO	N4	CSI0_DATA06 (ALT2)	0.000V - NVCC_CSI	gpio5
ECSPI1_MOSI	P2	CSI0_DATA05 (ALT2)	0.000V - NVCC_CSI	gpio5
ECSPI1_SCLK	N1	CSI0_DATA04 (ALT2)	0.000V - NVCC_CSI	gpio5
ECSPI1_SS0	N3	CSI0_DATA07 (ALT2)	0.000V - NVCC_CSI	gpio5
ECSPI1_SS1	G21	EIM_DATA19 (ALT1)	0.000V - NVCC_EIM0	gpio3
ECSPI1_SS2	F22	EIM_DATA24 (ALT3)	0.000V - NVCC_EIM0	gpio3
ECSPI1_SS3	G22	EIM_DATA25 (ALT3)	0.000V - NVCC_EIM0	gpio3
ecspi2 (5 of 8)				
ECSPI2_MISO	U24	DISP0_DATA17 (ALT2)	0.000V - NVCC_LCD	gpio5
ECSPI2_MOSI	T21	DISP0_DATA16 (ALT2)	0.000V - NVCC_LCD	gpio5
ECSPI2_SCLK	U23	DISP0_DATA19 (ALT2)	0.000V - NVCC_LCD	gpio5
ECSPI2_SS0	V25	DISP0_DATA18 (ALT2)	0.000V - NVCC_LCD	gpio5
ECSPI2_SS1	T22	DISP0_DATA15 (ALT3)	0.000V - NVCC_LCD	gpio5
ecspi3 (5 of 8)				
ECSPI3_MISO	P23	DISP0_DATA02 (ALT2)	0.000V - NVCC_LCD	gpio4
ECSPI3_MOSI	P22	DISP0_DATA01 (ALT2)	0.000V - NVCC_LCD	gpio4
ECSPI3_SCLK	P24	DISP0_DATA00 (ALT2)	0.000V - NVCC_LCD	gpio4
ECSPI3_SS0	P21	DISP0_DATA03 (ALT2)	0.000V - NVCC_LCD	gpio4
ECSPI3_SS1	P20	DISP0_DATA04 (ALT2)	0.000V - NVCC_LCD	gpio4
ecspi4 (5 of 8)				
ECSPI4_MISO	E23	EIM_DATA22 (ALT1)	0.000V - NVCC_EIM0	gpio3
ECSPI4_MOSI	G23	EIM_DATA28 (ALT2)	0.000V - NVCC_EIM0	gpio3
ECSPI4_SCLK	H20	EIM_DATA21 (ALT1)	0.000V - NVCC_EIM0	gpio3
ECSPI4_SS0	G20	EIM_DATA20 (ALT1)	0.000V - NVCC_EIM0	gpio3
ECSPI4_SS1	H19	EIM_ADDR25 (ALT1)	0.000V - NVCC_EIM0	gpio5
enet (31 of 39)				
ENET_COL	U6	KEY_ROW1 (ALT1)	0.000V - NVCC_GPIO	gpio4
ENET_CRS	U5	KEY_COL3 (ALT1)	0.000V - NVCC_GPIO	gpio4
ENET_MDC	V20	ENET_MDC (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_MDIO	V23	ENET_MDIO (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_REF_CLK	R2	GPIO16 (ALT2)	0.000V - NVCC_GPIO	gpio7
ENET_RX_CLK	P6	GPIO18 (ALT1)	0.000V - NVCC_GPIO	gpio7
ENET_RX_DATA0	W21	ENET_RX_DATA0 (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_RX_DATA1	W22	ENET_RX_DATA1 (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_RX_DATA2	W6	KEY_COL2 (ALT1)	0.000V - NVCC_GPIO	gpio4
ENET_RX_DATA3	W5	KEY_COL0 (ALT1)	0.000V - NVCC_GPIO	gpio4
ENET_RX_EN	U21	ENET_CRS_DV (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_RX_ER	W23	ENET_RX_ER (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_TX_CLK	V22	ENET_REF_CLK (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_TX_DATA0	U20	ENET_TX_DATA0 (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_TX_DATA1	W20	ENET_TX_DATA1 (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_TX_DATA2	W4	KEY_ROW2 (ALT1)	0.000V - NVCC_GPIO	gpio4
ENET_TX_DATA3	V6	KEY_ROW0 (ALT1)	0.000V - NVCC_GPIO	gpio4
ENET_TX_EN	V21	ENET_TX_EN (ALT1)	0.000V - NVCC_ENET	gpio1
ENET_TX_ER	P5	GPIO19 (ALT6)	0.000V - NVCC_GPIO	gpio4
RGMII_RD0	C24	RGMII_RD0 (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_RD1	B23	RGMII_RD1 (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_RD2	B24	RGMII_RD2 (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_RD3	D23	RGMII_RD3 (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_RX_CTL	D22	RGMII_RX_CTL (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_RXC	B25	RGMII_RXC (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_TD0	C22	RGMII_TD0 (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_TD1	F20	RGMII_TD1 (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_TD2	E21	RGMII_TD2 (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_TD3	A24	RGMII_TD3 (ALT1)	0.000V - NVCC_RGMII	gpio6

RGMII_TX_CTL	C23	RGMII_TX_CTL (ALT1)	0.000V - NVCC_RGMII	gpio6
RGMII_TXC	D21	RGMII_TXC (ALT1)	0.000V - NVCC_RGMII	gpio6
gpio2 (0 of 32)				
gpio7 (0 of 14)				
gpmi (15 of 19)				
NAND_ALE	A16	NAND_ALE (ALT0)	0.000V - NVCC_NAND	gpio6
NAND_CE0_B	F15	NAND_CS0_B (ALT0)	0.000V - NVCC_NAND	gpio6
NAND_CE1_B	C16	NAND_CS1_B (ALT0)	0.000V - NVCC_NAND	gpio6
NAND_CLE	C15	NAND_CLE (ALT0)	0.000V - NVCC_NAND	gpio6
NAND_DATA00	A18	NAND_DATA00 (ALT0)	0.000V - NVCC_NAND	gpio2
NAND_DATA01	C17	NAND_DATA01 (ALT0)	0.000V - NVCC_NAND	gpio2
NAND_DATA02	F16	NAND_DATA02 (ALT0)	0.000V - NVCC_NAND	gpio2
NAND_DATA03	D17	NAND_DATA03 (ALT0)	0.000V - NVCC_NAND	gpio2
NAND_DATA04	A19	NAND_DATA04 (ALT0)	0.000V - NVCC_NAND	gpio2
NAND_DATA05	B18	NAND_DATA05 (ALT0)	0.000V - NVCC_NAND	gpio2
NAND_DATA06	E17	NAND_DATA06 (ALT0)	0.000V - NVCC_NAND	gpio2
NAND_DATA07	C18	NAND_DATA07 (ALT0)	0.000V - NVCC_NAND	gpio2
NAND_RE_B	B17	SD4_CMD (ALT1)	0.000V - NVCC_NAND	gpio7
NAND_WE_B	E16	SD4_CLK (ALT1)	0.000V - NVCC_NAND	gpio7
NAND_WP_B	E15	NAND_WP_B (ALT0)	0.000V - NVCC_NAND	gpio6
i2c1 (2 of 2)				
I2C1_SCL	N5	CSI0_DATA09 (ALT4)	0.000V - NVCC_CSI	gpio5
I2C1_SDA	N6	CSI0_DATA08 (ALT4)	0.000V - NVCC_CSI	gpio5
i2c2 (2 of 2)				
I2C2_SCL	E22	EIM_EB2 (ALT6)	0.000V - NVCC_EIM0	gpio2
I2C2_SDA	C25	EIM_DATA16 (ALT6)	0.000V - NVCC_EIM0	gpio3
ipu2 (1 of 60)				
IPU2_DI0_PIN01	B16	NAND_READY (ALT1)	0.000V - NVCC_NAND	gpio6
mmdc (111 of 121)				
DRAM_ADDR00	AC14	DRAM_ADDR00	0.000V - NVCC_DRAM	
DRAM_ADDR01	AB14	DRAM_ADDR01	0.000V - NVCC_DRAM	
DRAM_ADDR02	AA14	DRAM_ADDR02	0.000V - NVCC_DRAM	
DRAM_ADDR03	Y14	DRAM_ADDR03	0.000V - NVCC_DRAM	
DRAM_ADDR04	W14	DRAM_ADDR04	0.000V - NVCC_DRAM	
DRAM_ADDR05	AE13	DRAM_ADDR05	0.000V - NVCC_DRAM	
DRAM_ADDR06	AC13	DRAM_ADDR06	0.000V - NVCC_DRAM	
DRAM_ADDR07	Y13	DRAM_ADDR07	0.000V - NVCC_DRAM	
DRAM_ADDR08	AB13	DRAM_ADDR08	0.000V - NVCC_DRAM	
DRAM_ADDR09	AE12	DRAM_ADDR09	0.000V - NVCC_DRAM	
DRAM_ADDR10	AA15	DRAM_ADDR10	0.000V - NVCC_DRAM	
DRAM_ADDR11	AC12	DRAM_ADDR11	0.000V - NVCC_DRAM	
DRAM_ADDR12	AD12	DRAM_ADDR12	0.000V - NVCC_DRAM	
DRAM_ADDR13	AC17	DRAM_ADDR13	0.000V - NVCC_DRAM	
DRAM_ADDR14	AA12	DRAM_ADDR14	0.000V - NVCC_DRAM	
DRAM_ADDR15	Y12	DRAM_ADDR15	0.000V - NVCC_DRAM	
DRAM_CAS	AE16	DRAM_CAS	0.000V - NVCC_DRAM	
DRAM_CS0	Y16	DRAM_CS0	0.000V - NVCC_DRAM	
DRAM_CS1	AD17	DRAM_CS1	0.000V - NVCC_DRAM	
DRAM_DATA00	AD2	DRAM_DATA00	0.000V - NVCC_DRAM	
DRAM_DATA01	AE2	DRAM_DATA01	0.000V - NVCC_DRAM	
DRAM_DATA02	AC4	DRAM_DATA02	0.000V - NVCC_DRAM	
DRAM_DATA03	AA5	DRAM_DATA03	0.000V - NVCC_DRAM	
DRAM_DATA04	AC1	DRAM_DATA04	0.000V - NVCC_DRAM	
DRAM_DATA05	AD1	DRAM_DATA05	0.000V - NVCC_DRAM	
DRAM_DATA06	AB4	DRAM_DATA06	0.000V - NVCC_DRAM	
DRAM_DATA07	AE4	DRAM_DATA07	0.000V - NVCC_DRAM	
DRAM_DATA08	AD5	DRAM_DATA08	0.000V - NVCC_DRAM	
DRAM_DATA09	AE5	DRAM_DATA09	0.000V - NVCC_DRAM	
DRAM_DATA10	AA6	DRAM_DATA10	0.000V - NVCC_DRAM	

DRAM_DATA11	AE7	DRAM_DATA11	0.000V - NVCC_DRAM
DRAM_DATA12	AB5	DRAM_DATA12	0.000V - NVCC_DRAM
DRAM_DATA13	AC5	DRAM_DATA13	0.000V - NVCC_DRAM
DRAM_DATA14	AB6	DRAM_DATA14	0.000V - NVCC_DRAM
DRAM_DATA15	AC7	DRAM_DATA15	0.000V - NVCC_DRAM
DRAM_DATA16	AB7	DRAM_DATA16	0.000V - NVCC_DRAM
DRAM_DATA17	AA8	DRAM_DATA17	0.000V - NVCC_DRAM
DRAM_DATA18	AB9	DRAM_DATA18	0.000V - NVCC_DRAM
DRAM_DATA19	Y9	DRAM_DATA19	0.000V - NVCC_DRAM
DRAM_DATA20	Y7	DRAM_DATA20	0.000V - NVCC_DRAM
DRAM_DATA21	Y8	DRAM_DATA21	0.000V - NVCC_DRAM
DRAM_DATA22	AC8	DRAM_DATA22	0.000V - NVCC_DRAM
DRAM_DATA23	AA9	DRAM_DATA23	0.000V - NVCC_DRAM
DRAM_DATA24	AE9	DRAM_DATA24	0.000V - NVCC_DRAM
DRAM_DATA25	Y10	DRAM_DATA25	0.000V - NVCC_DRAM
DRAM_DATA26	AE11	DRAM_DATA26	0.000V - NVCC_DRAM
DRAM_DATA27	AB11	DRAM_DATA27	0.000V - NVCC_DRAM
DRAM_DATA28	AC9	DRAM_DATA28	0.000V - NVCC_DRAM
DRAM_DATA29	AD9	DRAM_DATA29	0.000V - NVCC_DRAM
DRAM_DATA30	AD11	DRAM_DATA30	0.000V - NVCC_DRAM
DRAM_DATA31	AC11	DRAM_DATA31	0.000V - NVCC_DRAM
DRAM_DATA32	AA17	DRAM_DATA32	0.000V - NVCC_DRAM
DRAM_DATA33	AA18	DRAM_DATA33	0.000V - NVCC_DRAM
DRAM_DATA34	AC18	DRAM_DATA34	0.000V - NVCC_DRAM
DRAM_DATA35	AE19	DRAM_DATA35	0.000V - NVCC_DRAM
DRAM_DATA36	Y17	DRAM_DATA36	0.000V - NVCC_DRAM
DRAM_DATA37	Y18	DRAM_DATA37	0.000V - NVCC_DRAM
DRAM_DATA38	AB19	DRAM_DATA38	0.000V - NVCC_DRAM
DRAM_DATA39	AC19	DRAM_DATA39	0.000V - NVCC_DRAM
DRAM_DATA40	Y19	DRAM_DATA40	0.000V - NVCC_DRAM
DRAM_DATA41	AB20	DRAM_DATA41	0.000V - NVCC_DRAM
DRAM_DATA42	AB21	DRAM_DATA42	0.000V - NVCC_DRAM
DRAM_DATA43	AD21	DRAM_DATA43	0.000V - NVCC_DRAM
DRAM_DATA44	Y20	DRAM_DATA44	0.000V - NVCC_DRAM
DRAM_DATA45	AA20	DRAM_DATA45	0.000V - NVCC_DRAM
DRAM_DATA46	AE21	DRAM_DATA46	0.000V - NVCC_DRAM
DRAM_DATA47	AC21	DRAM_DATA47	0.000V - NVCC_DRAM
DRAM_DATA48	AC22	DRAM_DATA48	0.000V - NVCC_DRAM
DRAM_DATA49	AE22	DRAM_DATA49	0.000V - NVCC_DRAM
DRAM_DATA50	AE24	DRAM_DATA50	0.000V - NVCC_DRAM
DRAM_DATA51	AC24	DRAM_DATA51	0.000V - NVCC_DRAM
DRAM_DATA52	AB22	DRAM_DATA52	0.000V - NVCC_DRAM
DRAM_DATA53	AC23	DRAM_DATA53	0.000V - NVCC_DRAM
DRAM_DATA54	AD25	DRAM_DATA54	0.000V - NVCC_DRAM
DRAM_DATA55	AC25	DRAM_DATA55	0.000V - NVCC_DRAM
DRAM_DATA56	AB25	DRAM_DATA56	0.000V - NVCC_DRAM
DRAM_DATA57	AA21	DRAM_DATA57	0.000V - NVCC_DRAM
DRAM_DATA58	Y25	DRAM_DATA58	0.000V - NVCC_DRAM
DRAM_DATA59	Y22	DRAM_DATA59	0.000V - NVCC_DRAM
DRAM_DATA60	AB23	DRAM_DATA60	0.000V - NVCC_DRAM
DRAM_DATA61	AA23	DRAM_DATA61	0.000V - NVCC_DRAM
DRAM_DATA62	Y23	DRAM_DATA62	0.000V - NVCC_DRAM
DRAM_DATA63	W25	DRAM_DATA63	0.000V - NVCC_DRAM
DRAM_DQM0	AC3	DRAM_DQM0	0.000V - NVCC_DRAM
DRAM_DQM1	AC6	DRAM_DQM1	0.000V - NVCC_DRAM
DRAM_DQM2	AB8	DRAM_DQM2	0.000V - NVCC_DRAM
DRAM_DQM3	AE10	DRAM_DQM3	0.000V - NVCC_DRAM
DRAM_DQM4	AB18	DRAM_DQM4	0.000V - NVCC_DRAM
DRAM_DQM5	AC20	DRAM_DQM5	0.000V - NVCC_DRAM
DRAM_DQM6	AD24	DRAM_DQM6	0.000V - NVCC_DRAM
DRAM_DQM7	Y21	DRAM_DQM7	0.000V - NVCC_DRAM

DRAM_ODT0	AC16	DRAM_ODT0	0.000V - NVCC_DRAM	
DRAM_ODT1	AB17	DRAM_ODT1	0.000V - NVCC_DRAM	
DRAM_RAS	AB15	DRAM_RAS	0.000V - NVCC_DRAM	
DRAM_RESET	Y6	DRAM_RESET	0.000V - NVCC_DRAM	
DRAM_SDBA0	AC15	DRAM_SDBA0	0.000V - NVCC_DRAM	
DRAM_SDBA1	Y15	DRAM_SDBA1	0.000V - NVCC_DRAM	
DRAM_SDBA2	AB12	DRAM_SDBA2	0.000V - NVCC_DRAM	
DRAM_SDCKE0	Y11	DRAM_SDCKE0	0.000V - NVCC_DRAM	
DRAM_SDCKE1	AA11	DRAM_SDCKE1	0.000V - NVCC_DRAM	
DRAM_SDCLK0_P	AD15	DRAM_SDCLK0_P	0.000V - NVCC_DRAM	
DRAM_SDCLK1_P	AD14	DRAM_SDCLK1_P	0.000V - NVCC_DRAM	
DRAM_SDQS0_P	AE3	DRAM_SDQS0_P	0.000V - NVCC_DRAM	
DRAM_SDQS1_P	AD6	DRAM_SDQS1_P	0.000V - NVCC_DRAM	
DRAM_SDQS2_P	AD8	DRAM_SDQS2_P	0.000V - NVCC_DRAM	
DRAM_SDQS3_P	AC10	DRAM_SDQS3_P	0.000V - NVCC_DRAM	
DRAM_SDQS4_P	AD18	DRAM_SDQS4_P	0.000V - NVCC_DRAM	
DRAM_SDQS5_P	AD20	DRAM_SDQS5_P	0.000V - NVCC_DRAM	
DRAM_SDQS6_P	AD23	DRAM_SDQS6_P	0.000V - NVCC_DRAM	
DRAM_SDQS7_P	AA25	DRAM_SDQS7_P	0.000V - NVCC_DRAM	
DRAM_SDWE	AB16	DRAM_SDWE	0.000V - NVCC_DRAM	
sjc (6 of 7)				
JTAG_MOD	H6	JTAG_MOD	0.000V - NVCC_JTAG	
JTAG_TCK	H5	JTAG_TCK	0.000V - NVCC_JTAG	
JTAG_TDI	G5	JTAG_TDI	0.000V - NVCC_JTAG	
JTAG_TDO	G6	JTAG_TDO	0.000V - NVCC_JTAG	
JTAG_TMS	C3	JTAG_TMS	0.000V - NVCC_JTAG	
JTAG_TRSTB	C2	JTAG_TRSTB	0.000V - NVCC_JTAG	
uart1 (2 of 8)				
UART1_RX_DATA	M3	CSI0_DATA11 (ALT3)	0.000V - NVCC_CSI	gpio5
UART1_TX_DATA	M1	CSI0_DATA10 (ALT3)	0.000V - NVCC_CSI	gpio5
uart2 (2 of 4)				
UART2_RX_DATA	R5	GPIO08 (ALT4)	0.000V - NVCC_GPIO	gpio1
UART2_TX_DATA	R3	GPIO07 (ALT4)	0.000V - NVCC_GPIO	gpio1
usdhc1 (7 of 14)				
SD1_CD_B	T4	GPIO01 (ALT6)	0.000V - NVCC_GPIO	gpio1
SD1_CLK	D20	SD1_CLK (ALT0)	0.000V - NVCC_SD1	gpio1
SD1_CMD	B21	SD1_CMD (ALT0)	0.000V - NVCC_SD1	gpio1
SD1_DATA0	A21	SD1_DATA0 (ALT0)	0.000V - NVCC_SD1	gpio1
SD1_DATA1	C20	SD1_DATA1 (ALT0)	0.000V - NVCC_SD1	gpio1
SD1_DATA2	E19	SD1_DATA2 (ALT0)	0.000V - NVCC_SD1	gpio1
SD1_DATA3	F18	SD1_DATA3 (ALT0)	0.000V - NVCC_SD1	gpio1
usdhc2 (7 of 14)				
SD2_CD_B	R6	GPIO04 (ALT6)	0.000V - NVCC_GPIO	gpio1
SD2_CLK	C21	SD2_CLK (ALT0)	0.000V - NVCC_SD2	gpio1
SD2_CMD	F19	SD2_CMD (ALT0)	0.000V - NVCC_SD2	gpio1
SD2_DATA0	A22	SD2_DATA0 (ALT0)	0.000V - NVCC_SD2	gpio1
SD2_DATA1	E20	SD2_DATA1 (ALT0)	0.000V - NVCC_SD2	gpio1
SD2_DATA2	A23	SD2_DATA2 (ALT0)	0.000V - NVCC_SD2	gpio1
SD2_DATA3	B22	SD2_DATA3 (ALT0)	0.000V - NVCC_SD2	gpio1
usdhc3 (6 of 12)				
SD3_CLK	D14	SD3_CLK (ALT0)	0.000V - NVCC_SD3	gpio7
SD3_CMD	B13	SD3_CMD (ALT0)	0.000V - NVCC_SD3	gpio7
SD3_DATA0	E14	SD3_DATA0 (ALT0)	0.000V - NVCC_SD3	gpio7
SD3_DATA1	F14	SD3_DATA1 (ALT0)	0.000V - NVCC_SD3	gpio7
SD3_DATA2	A15	SD3_DATA2 (ALT0)	0.000V - NVCC_SD3	gpio7
SD3_DATA3	B15	SD3_DATA3 (ALT0)	0.000V - NVCC_SD3	gpio7
usdhc4 (6 of 12)				
SD4_CLK	E16	SD4_CLK (ALT0)	0.000V - NVCC_NAND	gpio7
SD4_CMD	B17	SD4_CMD (ALT0)	0.000V - NVCC_NAND	gpio7

SD4_DATA0	D18	SD4_DATA0 (ALT1)	0.000V - NVCC_NAND	gpio2
SD4_DATA1	B19	SD4_DATA1 (ALT1)	0.000V - NVCC_NAND	gpio2
SD4_DATA2	F17	SD4_DATA2 (ALT1)	0.000V - NVCC_NAND	gpio2
SD4_DATA3	A20	SD4_DATA3 (ALT1)	0.000V - NVCC_NAND	gpio2
wdog1 (1 of 2)				
WDOG1_B	R22	DISP0_DATA08 (ALT3)	0.000V - NVCC_LCD	gpio4


1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
A B C D E F G H J K L M N P R T U V W Y AA AB AC AD AE
Ground
Power
Not Muxed
Available
In Use