

2048 byte (main) + 64 byte (spare)

ECC bits = 32, NFC_CFG[PAGE CNT]=1, Sector size = 2048+64

Virtual page layout

VirtualPage_0

Min Byte address	Max Byte address	Size	Area	Description
0	2048	2048	Main0(page0)	User data
2048	2052	4	Spare0	Reserved
2052	2112	60	Spare0	ECC for main/spare

Physical page layout

Min Byte address	Max Byte address	Size	Area	Description
0	2048	2048	Main0(page0)	User data
2048	2052	4	Spare0	Reserved
2052	2112	60	Spare0	ECC for main/spare

2048 byte(main) + 64 byte (spare)

ECC bits =8, NFC_CFG[PAGE CNT]=4, Sector size = 512+16

Virtual page layout

VirtualPage_0

Min Byte address	Max Byte address	Size	Area	Description
0	512	512	Main0(page0)	User data
512	513	1	Spare0(page0)	Reserved
513	528	15	Spare0(page0)	ECC for main/spare

VirtualPage_1

Min Byte address	Max Byte address	Size	Area	Description
0	512	512	Main1(page1)	User data
512	513	1	Spare1(page1)	Reserved
513	528	15	Spare1(page1)	ECC for main/spare

VirtualPage_2

Min Byte address	Max Byte address	Size	Area	Description
0	512	512	Main2(page2)	User data
512	513	1	Spare2(page2)	Reserved
513	528	15	Spare2(page2)	ECC for main/spare

VirtualPage_3

Min Byte address	Max Byte address	Size	Area	Description
0	512	512	Main3(page3)	User data
512	513	1	Spare3(page3)	Reserved
513	528	15	Spare3(page3)	ECC for main/spare

Physical page layout

Min Byte address	Max Byte address	Size	Area	Description
0	511	512	Main0(page0)	User data
512	1023	512	Main1(page1)	User data
1024	1535	512	Main2(page2)	User data
1536	2047	512	Main3(page3)	User data
2048	2049	1	Spare0(page0)	Reserved
2049	2050	1	Spare1(page1)	Reserved
2050	2051	1	Spare2(page2)	Reserved
2051	2052	1	Spare3(page3)	Reserved
2052	2067	15	Spare0(page0)	ECC for main/spare
2067	2082	15	Spare1(page1)	ECC for main/spare
2082	2097	15	Spare2(page2)	ECC for main/spare
2097	2112	15	Spare3(page3)	ECC for main/spare