# Getting Started with AUTOSAR MCAL

#### **Steve Mihalik**

Senior Field Application Engineer

October 2018 | AMF-AUT-T3380



Ĥ



Company Public – NXP, the NXP logo, and NXP secure connections for a smarter world are trademarks of NXP B.V. All other product or service names are the property of their respective owners. © 2018 NXP B.V.

#### Abstract

This session is intended for engineers either getting started or are interested in seeing how to use AUTOSAR MCAL to generate and implement code.

"Hello world" level examples are presented using Tresos configuration tool to generate AUTOSAR defined structures.

These structures are combined with NXP written drivers and user code to build an application.



# Agenda

Introduction

- Recommended workflow
- Port module Port pin configurations (GPIO)
- Dio module Add Digital I/O to control GPIO
- Gpt module General Purpose Timer (RTC)
- Interrupt Ap Add Interrupt capability to GPT
- Reference: NXP AUTOSAR Installation







## NXP AUTOSAR Licensing Summary rev. Sept 07 2018

	Feature	Eval- uation	DISM	Expired	Produc- tion⁴
MCAL,	Latest updates <sup>1</sup> for that version put in your NXP account	Yes	Yes	No	No
OS	Installation license for your current or new PC	Yes	Yes	No	Yes
	Generated code can be used by application code	Yes	Yes	Yes	Yes
Tresos	Version used for testing is in your account	Yes	Yes	Yes but <sup>2</sup>	Yes
	Use license <sup>3</sup> put in your account (renewed quarterly)	Yes	Yes	No	Yes
	Generated code's configuration can be modified	Yes	Yes	No	Yes
Support	On-line support	No	Yes	No	No
1   Indate	s include Hot Fives & RTM releases for that version			SAR VA	reinne

Updates include not fixes & R I wireleases for that version. New AUTUSAR versions, e.g., 4.2 to 4.3, require additional license

<sup>2</sup> You can still download Tresos from your account but not use it due to expired license.

<sup>3</sup> Notes: 1.) Tresos license only works with NXP products.
2.) Tresos license for versions 17.0 or later is an "activation code"; earlier versions is a "license file".

<sup>4</sup> Production licenses for licensed version do not expire



#### **Installation Process**

- 1. Get AUTOSAR installation SW to your NXP account
- 2. Download MCAL / OS installation SW from your NXP account
- 3. Download, install / renew Tresos & license code or file
- 4. Download, install make utility
- 5. Install MCAL (and/or OS)
- 6. Sample application installation & build configuration
- 7. Additional SW: Compiler, Java





## **Recommended Workflow**

#### Windows:

1. Duplicate & rename sample application's folder\*

#### Tresos:

- 1. Close any previous projects (reduces risk of configuring module of wrong project)
- 2. Import (copy) sample application's Tresos project\*
- 3. Rename project to same name as sample application's folder\*
- 4. Load project's configuration
- 5. Sanity check: Generate and ensure no errors
- 6. Modify as desired
- 7. Generate

#### Windows:

- 1. Copy output (.c and .h files) from Tresos project's output folder to sample application's folder
- 2. Modify application code as desired
- 3. Build

\* First time only



### Duplicate & Rename Sample Application

 Copy an MCAL sample application folder. Examples:

MPC5748G\_MCAL4\_0\_sample\_ap\_devkit or

MPC574XG\_MCAL4\_0\_RTM\_1\_0\_4\_Sampl e\_Application\_6M

Paste

- Rename to Calypso6M\_MCAL4\_0\_sample\_ap \_devkit
- <u>Note</u>: slides show alternate name: Calypso3M\_MCAL\_4\_0\_Sample\_Ap



### Importing Sample Application into Tresos

1. Start Tresos. Use 64 bit if there is an option and you have Windows 64 bit. (First time: click yes to create workspace directory)

File - Import - General - Existing Projects into Workspace -> Click Next

2. First time: Check "Copy" box to copy workspace from NXP folder to EB folder.)
3. Browse to root directory (Tresos folder). Example: C:WXP\AUTOSAR\MPC574XG\_MCAL4\_0\_RTM\_1\_0\_4\Calypso6M\_sample\_ap\_devkit \Tresos\_

4. Click OK -> Click Finish

	·	🔡 Import	🔢 Import
File	EB tresos 21.0.0 - worksp Edit Search Project New	Select Create new projects from an archive file or directory.	Import Projects         Select a directory to search for existing Eclipse projects.
	Save Save All Close Close All Import	Select an import source: type filter text General CVS SVN SVN CTeam	Select root directory: C:\NXP\AUTOSAR\MPC574XG_MCAL4_0_RTM_1_0_4\Calypso3M_MCAL_4_0_Sample_Ap\Tresos Browse Select archive file: Browse Projects: Image: C:\NXP\AUTOSAR\MPC574XG_MCAL4_0_RTM_1_0_4\Calypso3M_MCAL_4_0_Sample_Ap\Tresos\workspace Select A
	Export <sup>Los</sup> Switch Workspace Open File Exit	Rat Next > Finish	COMPANY PUBLIC 9

# Suggestion: Rename Tresos Project

• Right click on project – Rename (optional).

 Enter new name. Example: Calypso6M\_MCAL4\_0\_sample\_ap

🔢 Rename F	Resource	2				×
New name:	Calypso	03M_N	1CAL_4_0	_Sample	_Ap	
						6
Preview	v >		ОК		Cancel	









€3,

# **Code Generation**

• Right Click on configuration (under project name) and select Generate Project



- Note: If there are generation errors be sure the Os module is disabled and not enabled to Generate. (Right click on configuration [PPC] & select Module Configs). Os modules requires AUTOSAR OS for this example.
- Click OK when done





## After generation, copy new generated files to application





### Build MCAL project – Windows Option

- If needed, update launch.bat as done previously with installation for path names and adding "pause" at end.
  - Example application folder path: C:\NXP\AUTOSAR\MPC574XG\_MCAL4\_0\_RTM\_1\_0\_4\Calypso6M\_sample\_ap

 Build: Double click on launch.bat file in your application folder from Windows explorer.

 If build aborts at adc.c (1<sup>st</sup> module in list), then the compiler path may be the problem. Verify the path in launch.bat. Make sure your compiler license is valid too.



## Example sample application launch.bat paths

::TRESOS

SET TRESOS\_DIR=C:\EB\tresos14\_2\_1

::GHS (recommend comp\_201416 since MCAL and OS ver 4.0 were tested with it) SET GHS\_DIR=C:\ghs\comp\_201714

::DIAB

::SET DIAB\_DIR=C:/Tools/WindRiver/diab/5.9.4.8

::Path to the plugins folder

SET PLUGINS\_DIR=C:\NXP\AUTOSAR\MPC574XG\_MCAL4\_0\_RTM\_1\_0\_4\eclipse\plugins

SET MAKE\_DIR=C:\gnuwin32

::SSC is required when OS is used

::SSC can still be defined if OS is not installed or not defined at all

SET SSC\_ROOT=C:\NXP\AUTOSAR\MPC574xG\_AUTOSAR\_OS\_4\_0\_92\_RTM\_1\_0\_3

Tip: add "pause" to the last line in the launch.bat file





launch.bat





## Method to clean project

 In the project's <u>bin directory</u>, select all files <u>except if there are debugger files</u> like run.cmm or any other .cmm file and then delete those selected files. Example:



 If you do a clean of the project, be sure to rebuild by executing launch.bat to prepare for the next exercise, download and debug.







## Port and Dio example: GPIO

A simple GPIO application will be created that reads a pushbutton swtich and outputs its state to an LED. Overall summary:

- Tresos:
  - Port Module: Add two GPIO pin configurations for LED output and switch input on EVB (done)
  - Dio Module: Name the two port pins
- Application code:
  - Code loop: read switch when write switch state to LED
- Ports to be used on MPC5748G Mother-Daughter EVB are:
  - Switch 2: PA2
  - LED 4: PG5

Switch	Number	MCU Pin
SW3	1	PA1
SW4	2	PA2
OTT		DEO

#### Table 23. User LEDs (DS2, DS3,

Table 20. User Pushbutton St

	Number	MCU Pin
DS2	1	PG2
DS3	2	PG3
DS7	3	PG4
DS8	4	PG5

#### **Port APIs**

- void Port\_Init( const Port\_ConfigType\* ConfigPtr )
- void Port\_SetPinDirection( Port\_PinType Pin, Port\_PinDirectionType Direction )
  void Port\_RefreshPortDirection( void )
- void Port\_GetVersionInfo( Std\_VersionInfoType\* versioninfo )
- void Port\_SetPinMode( Port\_PinType Pin, Port\_PinModeType Mode)



## Mapping pins to Port module

 From the I/O spreadsheet attached to the MPC5748G reference manual, MSCR numbers and function's SSS values are listed for



• Example's GPIO pins:

ports:

Description	Use	Port and Channel (Used for <u>Dio port and channel</u> <u>identification</u>	SIUL_MSCR# (Used for <u>Port pin</u> <u>identification</u> )	
Switch2	GPIO input	PA2	2	
LED4	GPIO output	PG5	101	N

# Port Module Example Project Workflow

Use a minimal application to duplicate. Go up to Tresos Step 6.

#### \* First time only

#### 1. Windows:

1. Duplicate & rename sample application's folder\* to Calypso6M\_MCAL4\_0\_gpio\_ap

#### 2. Tresos:

- 1. Close any previous projects (reduces risk of configuring module of wrong project)
- 2. Import (copy) mcu application's Tresos project\* (Import application's **Tresos** folder). Example: *C:\NXP\AUTOSAR\MPC574XG\_MCAL4\_0\_RTM\_1\_0\_4*\Calypso6M\_MCAL4\_0\_mcu\**Tresos**
- 3. Rename project to Calypso6M\_mcal4\_0\_gpio\_ap
- 4. Load project's configuration
- 5. Sanity check: Generate and ensure no errors
- 6. Modify as desired
- 7. Generate
- 3. Windows:
  - 1. Copy output (.c and .h files) from Tresos project's **output** folder to sample application's **cfg** folder
  - 2. Modify application code as desired
  - 3. Build



### Summary 1: Remove PortPins except JTAG; add 2 GPIO

Port module

Tab: PortConfigSet Index 0: PortConfigSet\_0 Tab: PortContainer Index 0: PortContainer\_0 Tab: PortPin Index n: PortPin names (remove all port pins except JTAG (DCI) pins, add 2 PortPins) Index of 1<sup>st</sup> added PortPin Tab: General (name LED4, PCR 101, output, GPIO mode, portpin high level) Index of 2<sup>nd</sup> added PortPin Tab: General (name Switch2, PCR 2, input, GPIO mode)



### Port (1): Enable Port module

- Enable Port module (if not already enabled) for configuration in Tresos project:
  - Right click on Port module
  - Click on Enable





# Port (2): Enable code generation

- Enable modules as needed that will be configured to generate code:
  - Right click on PPC
  - Select Module Configurations...
  - In Generate column, check box for:
    - Port
  - -Click OK



Module Configurations

#### **Module Configurations**

Edit the table below to change the set of module configurations for this project

Available Modules	E		Module	Configurations	ø
> All Modules			Enable	Generate	Module
A No Cluster		8			Adc (V1.0
Adc (V1.0.4, AS4.0.3)				<b>~</b>	Base (V1.
Base (V1.0.4, AS4.0.3)	Ξ				Can (V1.0
Can (V1.0.4, AS4.0.3)				$\checkmark$	Dem (V1.
CanIf (V1.0.4, AS4.0.3	3)			~	Dio (V1.0
Dem (V1.0.4, AS4.0.3)					

## Port (3): Select PortConfigSet 0 1

- Double click on Port module's configuration ("Port")
- Click on PortConfigSet tab

B B Decourses (1/1 0 4 AC4 0 2)

🖢 Project Explorer 🛛 🦳 📄 🔄 🖓 🖓 🖓	🖹 Port (Port) 🛛								
Calypso3M_MCAL4_0_sample_ap	-								
Calypso6M_MCAL_4_0_Sample_Ap	Port								
🔺 🥩 Calypso6M_mcal4_0_gpio_ap_devkitevb									
4 🛷 PPC (PA, MPC574XG)	lame 🗁 Port								
▷ 💁 Adc (V1.0.4, AS4.0.3)									
Base (V1.0.4, AS4.0.3)	General PortConfigSet Published Information								
▷ 🚇 Can (V1.0.4, AS4.0.3)	General Portconngset Published Information								
Dem (V1.0.4, AS4.0.3)									
▷ 👺 Dio (V1.0.4, AS4.0.3)	Config Variant			-	/ -				
EcuM (V1.0.4, AS4.0.3)	Variante Ostbullu			•					
▷ 🚇 Eth (V1.0.4, AS4.0.3)	<ul> <li>PortGeneral</li> </ul>								
▷ 👺 Fee (V1.0.4, AS4.0.3)									
Fls (V1.0.4, AS4.0.3)	Name 🗁 PortGeneral								
▷ 💁 Gpt (V1.0.4, AS4.0.3)									
▷ 🚇 Icu (V1.0.4, AS4.0.3)	Port Development Error Detect			<u> </u>		Port SetPinDirection Api			4 -
▷ 🚇 Lin (V1.0.4, AS4.0.3)		<u> ^</u>	V				N <sub>1</sub>	V	
Mcl (V1.0.4, AS4.0.3)	Port SetPinMode Api			<u> </u>		Port VersionInfo Api			<u> </u>
Mcu (V1.0.4, AS4.0.3)		<u> </u>	V	<i>•</i> •			<u> </u>	V	
	Port SetPinMode Does Not Touch GPIO Levels	X		/ -		Port Enable User Mode Support	X		л -
Port (V1.0.4, AS4.0.3)		<u> 1</u>		<i>•</i> •			A		
Port.									
▷ @ Pwm (V1.0.4, AS4.0.3)									

## Port (4): Select PortConfigSet 0 2

 Double click on PortConfigSet's Index 0 🗐 Port (Port) 🔀 Port Port P> Name General PortConfigSet Published Information Multiple Configuration: PortConfigSet Index 🗁 Name X PortPin ... X 🔄 PortConfi... 📝 🗌 X C offer

 Click on PowerConfigSet\_0's PortContainer tab

#### PortConfigSet

Name 🕞 PortConfigSet	_0	
General PortContainer		
<ul> <li>NotUsedPortPin</li> </ul>		
Name 🗁 NotUs	edPortPin	
PortPin Wpe	🔀 🔲 🥖 🗸	PortPin Wps 🛛 📔 🥖 🗸
PortPin Ode	R 📃 🥖 🗸	
PortPin Direction	PORT_PIN_IN	<ul> <li>✓ </li> </ul>
PortPin Level Value	PORT_PIN_LEVEL_LOW	• 🦉 •





Click on the PortPin tab



🕙 Port (Port) 🖾

PortContainer



## Port (6): Delete all current PortPins in PortContainer

Port

Pins

 Select all Indices (in Index column) EXCEPT JTAG, and click X to remove all existing pins PortContainer @ 1

PortContainer 0 Name General PortPin Ð X <u></u> ÷ 2 PortPin Remove selected elements X Х X X X PortPin ... X PortPin ... X PortPin O... PortPin S... PortPin ... PortPin H... PortPin D... X PortPin ... Index Name **JTAG** X X X X X X X X 0 🗁 Adc Pot 13 <u> </u> **X** X 🗆 X- 🗆 X 🗆 🔥 🗆 X- 🗹 X 🗆 3 🕞 DCI TCK 16 1 3 🔀 🗆 🔒 🗔 X- 🗆 X- 🗌 **X** 🔒 🗌 **X** 🔒 🗌 14 🗁 DCI TDI 2 3 🔀 🗆 X 🗌 X 🗌 X- 🗆 X- 🗹 **X** X- 🗌 🔒 🗌 15 3 🗁 DCI TDO R 🔀 🗆 **X** X 🗆 <u>x</u> 🗆 X 🗆 🔀 🗔 X- 🗹 X- 🗆 17 🗟 4 DCI TMS X-X 🗆 X-X-R X 🗆 X- 🗸 13 X 🗆 X- 🗌 4 5 🗁 DSPI0 IN X X-X 🗆 X 🗆 X<sub>1</sub> X- 🗸 X 🗆 X-6 🗁 DSPI0 OUT 3 X 🗌 Xi 🗆 **X** X 🗌 X<sub>1</sub> X- 🗆 1 🖥 🔥 🗆 X-7 🗁 Icu\_measu... Xi 🗆 X 🗆 X 🗆 🔒 🗆 X 🗌 X-**X** X-12 6 8 🗁 Key1 X 🗆 X X-X 🗆 X 🗆 X<sub>1</sub> X- 🗸 X-5 🗟 9 🗁 LIN1 RXD X 🗌 Xi 🗆 X 🗆 X 🗌 Xh 🗆 X- 🗸 2 🖥 🗁 LIN1 TXD 🔥 🗆 🔥 🗔 10 X 🗌 Xi 🗆 X 🗆 X 🗌 X<sub>1</sub> X- 🗌 🔀 🗆 🔥 🗔 11 📑 11 🗁 Led1 Pwm X 🗌 Xi 🗆 X- 🗌 X 🗆 X<sub>1</sub> X- 🗌 🕞 Led2 X 🗌 X-13 9 12 X 🗌 Xi 🗆 **X** X 🗌 Xh 🗆 X-🔥 🗆 🔥 🗔 10 13 13 🕞 Led3 X-X 🗌 Xi 🗆 XI 🗆 🔥 🗆 X<sub>1</sub> X- 🗌 🔥 🗔 12 🔓 🗁 Led4 14 X 🗌 Xi 🗆 X-X-X<sub>1</sub> 8 🔒 DART LIN2... X 🔀 🗹 X 1 15 X X X 7 📑 🗁 UART\_LIN2... 🕅 X 16

#### C \*Port (Port) 🔀 Port (7): Add two PortPins PortContainer

- Name Click + twice to add two new port pins
  - PortPin 0 and PortPin 1 are added



PortContainer 0

ት 🟠

@

# Port (8) Configure PortPin method

- <u>Name</u>: LED4
- <u>PortPin ID</u>: PortPin Ids are a unique number used to identify the port configuration
  - Click on the "Calculate Value" icon to automatically get a unique number.
- PortPinPcr: (SIUL\_MSCR#) 101
- <u>PortPin Direction</u>:
   PORT\_PIN\_OUT
  - \_OUT sets MSCR bits IBE=0,OBE=1
     \_IN sets MSCR bits IBE =1, OBE =0
     \_Disabled sets MSCR both IBE,OBE=0
- PortPin Mode: GPIO
- <u>PortPin Level Value</u>: PORT\_PIN\_LEVEL\_HIGH



COMPANY PUBLIC

32

## Port (9) Go back to prior container & select other port pin

 Navigate back a container by clicking on up arrow

 Double click on other added PortPin's index to configure it

🕙 *Port (Por	t) 🛛				- 0)	ᄩ Workflows 🛛 💣 Sidel
PortPin					<b>û</b>	No workflow selected.
Name* 🧯	≥ LED4	1			( <sup>III</sup> ) Naviga	ate one container upward
😫 *Po	rt (Port)	x			_	
Port	Contai	iner				
Name	¢.	PortContainer	0			
INdiffe		r oncontainci				
Gene	ral Port	Pin				
		~				
	Port	Pin				
	Index	🗁 Name	X Po			
	0	🔄 DCI_TCK	x <u>-</u>			
	1	🔁 DCI_TDI	🔀 🗆			
	2	🔁 DCI_TDO	<b>X</b>			
	3	CI_TMS				
	4	≥ LED4	X6 🗆			
	5	PortPin_1	XG [	COMPANY PUBL	IC 33	NE



## Need help?

 Need help? Click on a configuration and see help box in workspace bottom right.

PortPin Id*	16	
fault		
»1 - 🗆	Properties	🛃 🏹 (
t 0 entries ▽	Properties for PortPinIo	(d
	Description	Pin Id of the port pin. This value will be assigned to the symbolic name
	Information	derived from the port pin container short name.
coodge	Problems	
	PostBuildVariantConditions	


# Port (11): There will be an error, like below.....

Click in problems view window to expand the errors

De	efault	
9	Error Log 🔝 Problems View 🛛	E 🗐 🗊
м	lessage	N
	🚯 Warnings(8)	
4	Errors(5)	
	Calypso6M_mcal4_0_gpio_ap_devkit(5) V-lue "17" - f	
_	Value "17" of node /AUTOSAR/TOP-LEVEL-PACKAGES/Port/ELEMENTS/Port/Port	onfigSet/PortConfigSet_0/PortContainer/PortContainer_0/PortPin/DCI_TMS/PortPinid_Is out of range:
	Value "16" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/Port/ELEMENTS/Port/PortC	onfigSet/PortConfigSet 0/PortContainer/PortContainer 0/PortPin/DCI TCK/PortPinId" is out of range: F
	Value "15" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/Port/FLEMENTS/Port/Port/ Value "15" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/Port/FLEMENTS/Port/ Value "15" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/Port/ Value "15" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/PORT/ Value "15" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/ Value "15" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/ Value "15" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/ Value "15" of node "/AUTOSAR/TOP-LEVEL-PACKAGES/ Value "15" of node "/AUTOSAR/TOP-LEVEL"	onfigSet/PortConfigSet 0/PortContainer/PortContainer 0/PortPin/DCL TD0/PortPinId" is out of range:
• Note	e 2 <sup>nd</sup> error has message port numbe	
of pi	ns is out of range	PortContainer
<ul> <li>Click</li> </ul>	k on 2 <sup>nd</sup> error	
<ul> <li>This</li> </ul>	brings up the Port Container windo	Name 🗁 PortContainer_0
- Clic	k the pencil then calculate icon for	General PortPin
FUI		
– Dou	ble click to automatically get the proper #	PortNumberOfPortPins*
<ul> <li>Click</li> </ul>	< on each remaining error, then click	on "Calculate Value" for that parameter value



Port	(12):	Navigate	back	
			-	

· Go back to prior container

图 *Port (Port) 🛛	- 8
PortPin	e 🕜 🟠
Name* 🔁 Switch2	

• Note PortPins at a glance (D by the box means default value) :

Port (P	Port (Port) 🕱							
PortCo	ortContainer							
Name	Name 🗁 PortContainer_0							
General	General PortPin							
Ind	ex	🕞 Name	X PortPin Wpe	X PortPin Wps	X PortPin Ode	X PortPin Safe Mode	X PortPin With Read Back	PortPin HysteresisControl
	0 (	DCI_TCK		<b>X</b>		<u>R</u>	R 🗆	🔒 🗆
	1 (	DCI_TDI	🔒 🗔	🔏 🗆	🔀 🗆	🔀 🗔	<u>Na</u>	<u>R</u>
	2 (	DCI_TDO	<b>X</b>	x 🗆	🔀 🗆	<u>x</u>	<u>Na</u>	<u>R</u>
	3 (	DCI_TMS	<u>x</u>	x 🗆	🔀 🗆	🔀 🗆	<u>Na</u>	<u>R</u>
	4 (	⇒ LED4	🔏 🗆	🔏 🗆	🔏 🗆	🔀 🗆	<u>B</u>	🔏 🗆
	5 (	Switch2	<b>A</b>	<b>B</b>	<b>B</b>		<u>B</u>	x 🗆
							COMPANY PUBLIC	$C \mid 3/$

#### Port (13): Generate

- As a check, Generate project (right click on PPC, select Generate Project) to ensure no errors occurred.
- Click Save icon (disk icon in upper left)







#### S32K14x Port Mapping Example - 1

• From the Sample Application User Manual: Use highlighted port, with PCR ID 96

Table 4-1. PORT and DIO Modules - Pin Configuration and DioChannel Assignment

PortPin Name	Pin ID (PCR ID)	Pin Mode	Pin Direction	Pin Level	Connected HW	Channel Assignment
PortPin_PWM_L ed1	9 <mark>6</mark> )	FTM2_CH0	Out	Low	Blue Led	-
PortPin_Led2	111	GPIO	Out	High	Green Led	Dio_Led2

Tresos Sample Application project: Open
 Port Module – Port Container tab – Double click on Index 0 (PortContainer\_0)
 click on PortPin tab –

Index	😂 Name	PortPin Passive F	ilter Enable 🛛 🖄 Ports Directic	on Changeable 🛛 🖹 PortPin Moc	de Changeable 🛛 🗎 P	ortPin Id 📄	PortPin Pcr
1	PortPin_DBG_LED	8		11	9	13 📄	64
16	GertPin_Led3	X	<b>N</b>			4 🗟	112 🔓
17	PortPin_PWM_Led1	R C		🔀 🗆	<b>B</b>	0.0	96 🗎
18	PortPin_UART1_RX		<u>x</u>		6	21 🔒	72 🔒

### S32K14x Port Mapping Example - 2

• Double click on that port pin's index, 17



Port pin parameters can be configured.

me 🗁 PortPin_PWM_Led1			
eneral			
PortPin Passive Filter Enable		PortPin Direction Changeab	le 🖹
PortPin Mode Changeable	🖻 🗆 🥒 🗸		
PortPin Id	1	~ 🔏 🔻	
PortPin Pcr	96	~ A *	
PortPin Mode	FTM2_CH0	~ / -	
PortPin DSE	B High_drive_st	rength v 🥖 🗸	
PortPin PE	PullDisabled	~ 0 -	
PortPin PS	PullDown		
PortPin Direction	PORT_PIN_OU	л	
PortPin Initial Mode	PORT_GPIO_N	NODE ~	
PortPin Level Value	PORT_PIN_LE	VEL_HIGH 🗸 🥖 🕶	





#### **DIO Example Introduction**

- Dio module is used for controlling simple Digital I/O General Purpose I/O
- This module adds to the prior PORT module example



#### **Dio APIs**

Dio\_LevelType Dio\_ReadChannel( Dio\_ChannelType ChannelId )

void Dio\_WriteChannel( Dio\_ChannelType ChannelId, Dio\_LevelType Level)

Dio\_PortLevelType Dio\_ReadPort( Dio\_PortType PortId )

void Dio\_WritePort( Dio\_PortType PortId, Dio\_PortLevelType Level )

Dio\_PortLevelType Dio\_ReadChannelGroup( const Dio\_ChannelGroupType\* ChannelGroupIdPtr )

void Dio\_WriteChannelGroup( const Dio\_ChannelGroupType\* ChannelGroupIdPtr, Dio\_PortLevelType Level)

void Dio\_GetVersionInfo( Std\_VersionInfoType\* VersionInfo )

Dio\_LevelType Dio\_FlipChannel( Dio\_ChannelType ChannelId )



# Mapping pins to Dio modules

- From the I/O • spreadsheet Verv Stro Direction attached to the LVDS Pair MPC5748G Port -Port -SIUL MSCR# MSCR SSS Modul -Descriptio Function  $\mathbf{T}$ reference PA[2] 2 0000 0000 GPIO[2] SIUL2 I/O Х manual, 22 PA[2] 0000\_0001 EOUC\_2\_G EMIOS0 0 **MSCR** 23 PA[2] 0000 0010 E2UC\_0\_X EMIOS2 0 numbers and function's SSS 753 PG[5] 101 0000\_0000 GPIO[101] SIUL2 I/O х values are PG[5] EMIOS1 754 0000 0001 E1UC\_14\_H 0 listed for ports: PG[5] 755 0000\_0010 E2UC 2 Y EMIOS2 0 DOLC1 MUZDU
- Example's GPIO pins:

Description	Use	Port and Channel (used for <mark>Dio</mark> port and channel identification	SIUL_MSCR# (Used for <u>Port pin</u> <u>identification</u> )
Switch2	GPIO input	PA2	2
LED4	GPIO output	PG5	101



#### Summary: Remove Dio Ports; add 2 ports/channels

Dio module (DioPorts and DioChannels shown for Mother-Daugher EVB – add PA[2], PG[5]) Tab: DioConfig Index 0: DioConfig\_0 Tab: DioPort (remove all DioPorts; add 2 DioPorts) Index of 1<sup>st</sup> added DioPort: DioPort\_0 Tab: General (Name DioPA, Dio Port Id = 0) DioPorts are Tab: DioChannel (Add channel) containers Index 0: DioChannel 0 Tab: General (Name Switch2, Chan Id 2) Index of 2<sup>nd</sup> added DioPort: DioPort 1 Tab: General (Name DioPG, Dio Port Id = 6) Tab: DioChannel (Add channel) Index 0: DioChannel\_0 (Name LED4, Chan Id 5)



# Dio (1): Enable module for configuration

- Note: Dio module enables writing to Power registers
- Right click on Dio module -
- Click Enable to enable configuration





# Dio (2): Enable code generation

- Enable modules that will be configured
  - Right click on PPC
  - Select Module Configurations....
  - In Generate column, check box for:
    - Dio
  - Click OK



Module Configurations

#### **Module Configurations**

Edit the table below to change the set of module configurations for this project

Available Modules 🕞	Ē		Module	Configurations	ø,
> All Modules			Enable	Generate	Module
A No Cluster		8			Adc (V1.0
Adc (V1.0.4, AS4.0.3)			$\checkmark$	$\checkmark$	Base (V1.
Base (V1.0.4, AS4.0.3)	Ξ		~		Can (V1.0
Can (V1.0.4, AS4.0.3)			~	$\checkmark$	Dem (V1.
CanIf (V1.0.4, AS4.0.3	)		~	~	Dio (V1.0
Dem (V1.0.4, AS4.0.3)					

#### Dio (3): Open Dio configuration

Double click on Dio's configuration. Dio module's General settings

ငြ္ခ *Project Explorer 🛛 🦳 🗖	Port (Port) (Dio) 🛛					
□ 🕏 🏹	Dio					
Image: Calypso3M_MCAL4_0_sample_ap						
Calypso6M_MCAL_4_0_Sample_Ap						
A Galypso6M_mcal4_0_gpio_ap_devkit	Name 🗁 Dio					
PPC (PA, MPC574XG)						
▷ 🚇 Adc (V1.0.4, AS4.0.3)	General DioConfig Published Information					
Base (V1.0.4, AS4.0.3)						
▷ 🚇 Can (V1.0.4, AS4.0.3)						
P Bem (V1.0.4, AS4.0.3)	Config Variant 🔒 VariantPreCompile	▼ 2 ▼				
A Pio (المكثر).4, AS4.0.3)						
P Dio	<ul> <li>DioGeneral</li> </ul>					
EcuM (V1.0.4, AS4.0.3)	Dio Conorol					
▷ 🚇 Eth (V1.0.4, AS4.0.3)	Name 🗁 Diogeneral					
Fee (V1.0.4, AS4.0.3)						
😰 Fee	Dio Development Error Detect 🙀	📝 🥖 👻 🛛 Dio Version Info Api 🛛 🙀 📝 🥒 👻				
Fls (V1.0.4, AS4.0.3)						
▷ 🛱 Gpt (V1.0.4, AS4.0.3)	Dio Reverse Port Bits 🙀	📄 🦉 👻 🛛 Dio Flip Channel Api 🛛 🙀 📝 🥒 👻				
▷ 🛱 Icu (V1.0.4, AS4.0.3)						
▷ 🚇 Lin (V1.0.4, AS4.0.3)	Dio Read Zero For Undefined Port Pins 🙀	📝 🥖 👻 🛛 Dio Masked Write Port Api 🛛 🙀 📃 🥖 👻				
Mcl (V1.0.4, AS4.0.3)						
Mcu (V1.0.4, AS4.0.3)	Enable Dio User Mode Support	V 45 -				
		COMPANY PUBLIC 48				

#### NP

- Select all indices (index 0 and 1)
  - Click the red X to remove those elements

- Double click on Index 0 of DioConfig
- Multiple Configuration: DioConfig Index 🕞 Name 0 🖟 📴 DioConfig... DioConfig @ 🗘 🟠 DioConfig\_0 C-Name DioPort 2. DioPort Index Dio Port... Remove selected elements B Name 🔄 Dio PA 0 0 🔄 Dio\_PG 6 1
- Dio (4): Remove all Dio ports & channels
- Click on DioConfig tab

Dio

DioConfig

Published Information

Dio

Name

## Dio (5): Add a DioPort

Click on the green + to add a port

• Double click on the new port's index

DioConfig	@ 🟠 🟠 🛛 No w
Name 🗁 DioConfig_0	
DioPort	
🖺 DioPort* 🕆	× • × •
Index 🗁 Name	Dio Po Add new element with default values
DioConfig	



DioConfig\_0

Name

# Dio (6): Configure a DioPort - 1

- Click on
   Dio Port Id (0 -> 16)\*
- Note description for the Dio Port Id. The value corresponds to Port A, B, C, etc.

DioPort		
	ասա	No workflow selected.
General DioChannel DioChannelGroup Dio Port Id (0 -> 16)*	•	No description available.
Image: Second state st	Bescription 🖾	Left Information 🖺 Errors
Last 1 entries (up to 100) warnings, errors (filtered out: infos) Message Generation of project Calypso6M_mcal4_0_gpio_ap_devkitevb has been	Name: DioPortId Numeric identifier for each port pin in NOTE: Use the fo PortA=0 PortB=1 PortC=2 PortD=3 PortC=4 PortE=4 PortF=5 PortG=6 PortH=7 PortH=7 PortI=8 PortJ=9 PortK=10 PortL=11 PortN=13 PortO=14 PortQ=16	of the DIO port. Symbolic names will be generated d for the pins which being used for configuration. llowing values to configure different ports.
GOWFAN	T'FUDLIC   J	

## Dio (7) Configure a DioPort - 2

Description	Use	SIUL_MSCR# (Used for <u>Port pin identification</u> )	Port and Channel (used for <u>Dio port and channel</u> <u>identification</u>			
Switch2	GPIO input	2	PA2			
LED4	GPIO output	101	PG5			
Die Dert Id: 0 (fer Dert A)						

-<u>Dio Port Id</u>: **0** (for Port A)

	identification
	PA2
	PG5
	*Port (Port) 🛛 🔋 *Dio (Dio) 🛛
	DioPort
	Name* 🗁 DioPA
	General DioChannel DioChannelGroup
-	Dio Port Id (0 -> 16)*



# Dio (8) Add channel to DioPort -

- Click on DioChannel tab
- Click on green + to add new DioChannel

 Double click on index 0 to configure the first channel







## Dio (10) Add additional port

- We are done configuring channels (pins) on Port A. Now we need to add Port G and then a channel on Port G.
- Click on the up arrow to navigate upward a container

- Click on the green + to add a new port.
- A new port with Index 1 is created
- Double click on Index 1 to configure the new port



# Dio (11) Configure new port, add channel

- Configure Port:
  - -<u>Name</u>: DioPG
  - <u>Dio Port Id</u>: 6 (for port G)

- Configure Channel:
  - Click on DioChannel tab
  - Click on + to add channel
  - Double click on the channel's index to bring up its configuration

ew port,	DioPort
	Name* 🗁 DioPG
	General DioChannel DioChannelGroup
	Dio Port Id (0 -> 16)*
	DioPort
	Name* 🗁 DioPG
	General DioChannelGroup
🗿 *Port (Port) 🦷 👰 *Dio (Dio) 💈	□ DioChannel*
DioPort	Index 😕 Name 🔂 Dio Channel Id
Name* 🗁 DioPG	
General DioChannel DioChar	nelGroup
Index 🕞 Name	Dio Channel Id
0 🗁 DioChannel_0	COMPANY PUBLIC 56







#### Dio (13): channel symbolic names

- AUTOSAR convention requires using the following name format.
- Format: ModulenameConf\_Containername\_channel name
- Symbolic names here for coding are generated and placed in Dio\_Cfg.h in folder c:\EB\tresos\workspace\your\_project\output\include
- DEVKIT EVB Example (0xa corresponds to MSCR10; 0x4c to MSCR76):

#define DioConf\_DioChannel\_LED4 ((uint16)0x000aU)
#define DioConf\_DioChannel\_Switch2 ((uint16)0x0004cU)



### Copy generated code to application

 Copy include & src folders' from <u>Tresos</u> workspace project's output folder

C:\EB\tresos14\_2\_1\workspace\Calypso6M\_mcal4\_0\_gpio\_ap\_devkit\output

e 🔹 [ 🏭 Open	New folder	
rites	Name	Date modified Type
sktop	👢 ерс	10/6/2017 2:43 PM File fold
wnloads	1. include	10/5/2017 5:16 PM File fold
oud Drive	👢 output	10/5/2017 1:35 PM File fold
oud Photos	📜 👢 src	10/5/2017 5:16 PM File fold

- Delete existing include & src folder in <u>application</u> project's
   cfg folder
- Paste in new include & src folders to application project's cfg folder

C:\NXP\AUTOSAR\MPC574XG\_MCAL4\_0\_RTM\_1\_0\_4\Calypso6M\_mcal4\_0\_gpio\_ap\_devkit\cfg Open New folder Name Date modified Type include 10/6/2017 3:01 PM File Src 10/6/2017 3:01 PM File



#### Edit Modules file to add newly enabled modules

- In project's root directory (which has launch.bat):
  - Edit text file "Modules" to add Dio and Port.
     (TIP: Add in alphabetical order a good habit because it makes it easier to check)

# specify modules which will be compiled and linked MODULE\_LIST := Base Det Dem **Dio** EcuM Mcu **Port** Rte



#### Add Dio code to main function in main.c ap's src folder:

```
Add global variable:
```

```
VAR(uint8, AUTOMATIC) Level_Switch2 =0;
```

Add code function to main before loop:

```
Port_Init(&PortConfigSet_0);
```

Add code inside forever loop:

```
/* Turn off (1) LED4 if Switch 2 is pressed (reads 1 if pressed)*/
Level_Switch2 = Dio_ReadChannel((Dio_ChannelType)DioConf_DioChannel_Switch2);
if (Level_Switch2 == 1) {
    Dio_WriteChannel((Dio_ChannelType)DioConf_DioChannel_LED4, 1); /* Off */
}
else {
    Dio_WriteChannel((Dio_ChannelType)DioConf_DioChannel_LED4, 0); /* On */
}
```



#### **Complete GPIO project**

- Clean Project
- Run launch.bat
- Download to board
- Run program. Validate LED is on except when switch is pressed.



## Review: MCAL Modules Used for GPIO

#### • PORT

- Configures port pins (SIUL2\_MCSR and SIUL2\_IMSR pad control register initializations)
- Pin identification: Done using Pin Configuration Register numbers (SIUL\_MSCRx)
- Generated code will be put in Port\_Init function (name is an AUTOSAR standard). User must call Port\_Init

#### · DIO

- Used for reading and writing GPIO data
- Configures name to GPIO port pins (e.g. PA1, PA2, ..., PB1, PB2, etc.)

#### - Pin identification:

Dio Port Id number: Dio Channel Id number: Corresponds to port's letter. (PortA=0, PortB=1, PortC=2, etc.) Corresponds to port Port pin's number (0, 1, 2,...)

#### References:

- User Manual for Sample MCAL Application
- AUTOSAR MCAL Integration and User Manuals for:
  - PORT Driver
  - DIO (Digital I/O) Driver

Driver	Name for a Port Pin	Name for Subset of Adjacent pins on one port	Name for a whole port
DIO Driver	Channel	Channel Group	Port
PORT Driver	Port pin		Port









# Gpt Example: Timer RTC with SXOSC Overview

#### **Configurations**:

- Mcu configurations
  - Set SOSC frequency
  - Enable SOSC
- Gpt configurations for RTC
  - Set prescaler, channel ID, channel (RTC), frequency
- Mcl
  - Enable Mcl (contains eMIOS which is needed for Gpt driver compilation.)

#### Code:

- Mcu\_InitClock: enables SXOSC and other clocks
- GptStartTimer: code to start timer



#### Suggested Project Workflow

Use again a minimal sample ap to duplicate. Go until Tresos Step 5.

#### 1. Windows:3

1. Duplicate & rename Mcu application's folder to Calypso6M\_mcal4\_0\_soxc\_gpt\_irq\*

#### 2. **Tresos**:

- 1. Close any previous projects (reduces risk of configuring module of wrong project)
- 2. Import (copy) application's Tresos project\* (Import application's **Tresos** folder). Example: *C:\WXP\AUTOSAR\MPC574XG\_MCAL4\_0\_RTM\_1\_0\_4\* Calypso6M\_mcu\_ap\**Tresos**
- 3. Rename project to Calypso6M\_mcal4\_0\_sxosc\_gpt\_irq\*
- 4. Load project's configuration
- 5. Sanity check: Generate and ensure no errors
- 6. Modify as desired
- 7. Generate
- 3. Windows:
  - 1. Copy output (.c and .h files) from Tresos project's **output** folder to sample application's **cfg** folder
- 2. Modify application code as desired
- 3. Build

\* First time only



Timer Summary 1: Configure SXOSC Mcu module Tab: McuModuleConfiguation Index 0: McuConfigPB\_0 Tab: General (Verify Slow Crystal Frequency = 32KHz) Tab: McuClockSettingConfig

Index 0: McuClockSettingConfig Tab: General (Enable SXOSC) Tab: McuSXOSC (set SXOSC divider,

etc.)



# Mcu (1): SXOSC - verify crystal frequency 1

- Double click Mcu driver configuration
- McuModuleConfiguration tab:
  - Double click on Index 0

🖹 Mo	Mcu (Mcu) 🛛						
Мс	u						
Nam	Name 🗁 Mcu						
Ger	General McuResetReasonconf McuModuleConfiguration Published I						
	Multiple Configuration: McuModuleConfiguration						
	Index	🗁 Name	12	Mcu Nu	12	Mcu Nu 🔒 Fa	
	o _	🔁 McuConfi		1		1 🔂	



# Mcu (2): SXOSC - verify crystal frequency 2

🗏 Mcu (Mcu) 🔀

#### On the General tab, verify the Slow Crystal Frequency is 32767 (default).

- This is the SXOSC crystal.

#### McuModuleConfiguration





## Mcu (3): SXOSC - Enable

- Click on McuClockSettingConfig tab
- Double click on index 0 to open McuClockSettingConfig

 Scroll down to bottom right, click to enable SXOSC (if not enabled already)





## Mcu (4): SXOSC - Set frequency, other parameters

- Click on McuSXOSC tab
  - Configure parameters as needed.
  - For this example, the default values are used so no changes are needed
  - Note other parameters
  - Note: You can configure the Mcu driver to not touch SXOSC at all using parameter SXOSC under MCU control
    - Similar capability exists for other Mcu resources

#### McuClockSettingConfig

Name	🗁 Mcu	ClockSettingConfig			]		
Gene	ral McuFIRC	McuSIRC McuSXOSC M	cuFXOS	C McuPcsCo	nfig M	cuProgressiveClkSwi	McuCLKOUT1
				·			
-	McuSXOSC						
_	Name 🔁	McuSXOSC					
	SXOSC unde	r MCU control	X	🔽 🥖 🗸		Oscillator start-	up delay bypas
	Auto level co	ontrol	X	- 🖉 🗸			
	EOCV (0 -> 2	255)		64			0 -
	SXOSC clock	interrupt mask	×	- 🦉 🗸			
	SXOSC Div (:	1 -> 32)		1			/ -
	SXOSC Outp	ut Value (1000 -> 35000)		32767.0			• 🔏 •
Timer Summary 2: Create timer channel with RTC, SXOSC

Gpt module

Tab: GptChannelConfigSet

Index 0: GptChannelConfigSet\_0

*Tab*: GptChannelConfiguration (delete current configurations, then add new one)

Index 0: GptChannel\_0

Tab: General (rename channel to RtcTimer,

GptHwCh = RTC, continuous

mode,

RTC clock source = SXOSC. Note: GptChannelTickFrequency will be set/calculated later)



## Gpt (1) - Enable Gpt, Mcl configuration and generation

- Right click on PPC
- Click on Module Configurations...



#### B Module Configurations

#### **Module Configurations**

Solit the table below to change the set of module configurations for this project.

> All Modules		Enar	Gener	Module	Name
A No Cluster	e si		~	Gpt (V1.0.4, AS4.0.3)	Gpt
Adc (V1.0.4, AS4.0.3)				Icu (V1.0.4, AS4.0.3)	Icu
Base (V1.0.4, AS4.0.3)	-			Lin (V1.0.4, AS4.0.3)	Lin
▲ III ▶			$\checkmark$	Mcl (V1.0.4, AS4.0.3)	Mcl
Default Pre-Configuration:		$\checkmark$	$\checkmark$	Mcu (V1.0.4, AS4.0.3)	Mcu
McuPreConfiguration	•	$\checkmark$	$\checkmark$	Os (V4.0.92, AS4.0.3)	Os

 Click both Enable and Generate to enable Gpt and Mcl (Micro Controller Library) module configurations and code generation.

### Gpt (2) : Delete existing channels

- Click GptChannelConfigSet tab
- Double click on GptChannels's index 0

Gpt		
Name	🕞 Gpt	
Genera	GptChannelConfigSet GptClockRefer	encePoint
Genera	GptChannelConfigSet GptClockRefer	encePoint nfigSet
General	GptChannelConfigSet GptClockRefer  Multiple Configuration: GptChannelConfiguration:	encePoint nfigSet

#### GptChannelConfigSet

🗐 Gpt (Gpt) 🖾

L.	Vame 🦻 GptChannelConfigSet_0
u channels	GptChannelConfiguration
those unneeded	CotchennelConfiguration
	Index 🗁 Name 📄 GptChan 🗎 GptHwC



#### Select all existing channels

 Click X to delete those unneeded channels



### Gpt (3) - Add Gpt channel

Add Channel: Click on Green +

 Double click on new channel's index to configure it

🕙 *Gpt (Gpt) 🛛
GptChannelConfigSet
Name 😂 GptChannelConfigSet_0
GptChannelConfiguration
GptChannelConfiguration*
Spt (Gpt)
GptChannelConfigSet
Name GptChannelConfigSet_0
GptChannelConfiguration
GptChannelConfiguration*
0 ← GptChann 0 ← GptChann 0 ← GptChann 0 ← GptChann 0 ← GptChann





## Gpt (5) - Configure Gpt chann

- <u>GptChannelMode</u>: choices are one shot or continuous. Select GPT\_CH\_MODE\_CONTINUOUS
- <u>GptChannelTickFrequency</u>: this can be configured automatically later after configuring clock source references
- <u>GptRtcChannelClkSrc</u>:
  - Click red box to enable configuration
  - Select RTC\_GPT\_CLKSRC\_SXOSC
- <u>GptChannelPrescaler</u>: See Description for prescaler value to divisor. Select "2" for divide the 32KHz SXOSC by 32.
- Other parameters can use default values

	열 *Gpt (Gpt) 🛛					
	GptChanne	lConfiguration				
II		tcTimer				
		• – –				
	General					
	GptChannel	id (0 -> 4294967295)*		0	-	4
	GptHwChan	nel*		RTC_0_CH_0	•	
	GptChannel	Mode*		GPT_CH_MODE_CONTINUOUS	•	4
	GptChannel	TickFrequency (0 -> 160000000)*		NaN	•	4
	GptChannel	ClkSrcRef*	@ <b>}</b>			
	GptRtcCł	annelClkSrc*		RTC_GPT_CLKSRC_SXOSC	•	<
	GptStmC	hannelClkSrc*		GPT_STM_SYSTEMCLOCK	•	
	GptChan	nelPrescaler (1 -> 256)*		2	•	4
	Default					
	🛃 P 🏋 🗖 🗖	🖺 Description 🛛 🖺 Informatio	on 🖺 Err	ors		
	Last 7 entries 🗢	Names OrtChannelDresseler				
		Vendor specific: The GPT module	specific c	lock prescaler value		
	🔓 🤔	Note with RTC:				
	Message	- 1: div1.				
	💧 Warning (	- 2: div32.				
	💧 Warning (	- 3: div512.				
	💧 Warning (	- 4: div512 and div32.				
		COMPANY P	UBLIC	77		

### Timer Summary 3: Create SXOSC clock reference point

Mcu module

Tab: McuModuleConfiguration

Index 0: McuConfigPB\_0

Tab: McuClockSettingConfig

Index 0: McuClockSettingConfig

Tab (far right): McuClockReferencePoint (add new reference point for SXOSC)

Index (new one): (name ClkRefPnt\_SXOSC,

freq. select = SXOSC,

automatically calc ref pt value)

## Mcu (1) – Configure clock reference point with SXOSC - 1

- To back to the Mcu configuration
- Click McuModuleConfiguration tab
- Double click index 0



Double click Index 0



## Mcu (2) - Configure clock reference point with SXOSC - 2

Click on "10" which indicates there are 10 more tabs

McuClockSettingConfig						2 1	ን 🟠	No workflow sel	
Name 🗁 McuClockSettingConfig									
General McuFIRC McuSIRC McuSXOSC McuFXO		nfig McuProgressiveClkSwi	McuCLKOUT	1 McuAuxClock2	McuAuxClock4	>> <u>10</u>	$\square$		
F40 divider (1 -> 4)	<b>a</b>						Mo	:uAuxClock5	
E40 Erequency (Hz) (1000000 -> 4000000)	-			A -			Mo	:uAuxClock6	
140 Trequency (12) (1000000 > 4000000)	4.0E7	4.0E7		<b>4</b>				McuAuxClock8	
F80 Divider Enable	X						Mo	:uAuxClock9	
							Mo	:uPll_0	
F80 divider (1 -> 4)	2						Mo	UEMIOS	
F80 Frequency (Hz) (1000000 -> 8000000)	■ 8.0E7		<b>–</b>	<i>A</i> -			Mo	:uClkMonitor_0	
	1. 0.0L7			<b>4</b>			Mo	:uFlash	
FS80 Divider Enable	X						Mo	:uRam	
							Mo	:u짓ockReferencePoint	
■ 1 ES80 divider (1 -> 8)	<b>⊡</b>								

Select McuClockReferencePoint

## Mcu (3) - Configure clock reference point with SXOSC - 3

- Click green + to add new clock reference point for SXOSC
  - This will be needed for Gpt driver

*Gpt (Gpt)	) (	🖹 *Mcu (Mcu) 🛛					
cuClo	ckSe	ettingConfig					@
ame 🖻	<b>→</b> Mo	cuClockSettingConfig					
ЛсиAuxClo	ock6 [N	//cuAuxClock8 McuAuxClock9 N	//cuPll_0	McuEMIOS	McuClkMonitor_0	McuFla	ash McuRam CcuClockReferencePoi *11
MC	cuClock	kReferencePoint					A +   A × □   &
Index	B	Name		Mcu Clock	Frequency Select	1.0	Mcu Clock Reference P Add new element wit
0	G (	ClkRefPnt_F40_CLK	B	F40		1	4.0E7
1	6 <del>7</del> (	ClkRefPnt_F80_CLK	B	F80		<u></u>	8.0E7
2	🔓 (	ClkRefPnt_FS80_CLK		FS80			8.0E7
3	🔓 (	ClkRefPnt_FXOSC		FXOSC			4.0E7
4	6 <del>7</del> (	ClkRefPnt_SYS_CLK		F160			1.6E8
5	🔄 (	ClkRefPnt_Wdg		SIRC			128000.0
6	Gr N	McuClockReferencePoint_EMIOS	_0 📑	CUSTOM			100000.0
	-						E 12000.0
	6	McuClockReferencePoint_E	EMIOS_0		ОМ		100000.0
	0.7	McuClockReferencePoint (	0	E160			1659

 Double click on added reference point's index to configure it

## Mcu (4) - Configure clock reference point with SXOSC - 4

- <u>Name</u>: ClkRefPnt\_SXOSC
- <u>Mcu Clock Frequency Select</u>: choose SXOSC
- Mcu Clock Reference Point <u>Frequency</u>: Double click on calculate value

🖹 *Gpt (Gpt) 🦷 *Mcu (Mcu) 🛛	
McuClockReferencePoint	
Name* 🗁 ClkRefPnt_SXOSC	
level	
Mcu Clock Frequency Select*	SXOSC 🗸 🎸 🗸
Mcu Clock Reference Point Frequency (0 -> 16000000)*	32767.0 🔻 📢 🗸

- Note:
  - Mcu module configures all clocks.
  - Clock reference points are a mechanism to provide clock information from Mcu module to other modules.



# Timer Summary 4: Configure a GptClockRefPt using RTC/SXOSC

Gpt module

Tab: GptClockReferencePoint

Index 0: GptClockReferencePoint\_0

Tab: General (select GptClockReference =

ClkRefPnt\_SXOSC)



📐 😤 \*Mcu (Mcu) 😤 \*Gpt (Gpt) 🖄 🗋

## Gpt (1) – Config a Gpt clock reference point with SXOSC 1

- Go back to Gpt configuration
- If needed navigate to the top of the Gpt container
- Select tab **GptClockReferencePoint**
- · Expand column widths to see all text
- Select GptClockReference of Index 0
  - Down arrow appears
- Click down arrow for GptClockReference options
- Select SXOSC



#### Gpt (2) - Config a Gpt clock reference point with SXOSC 2

- Expand Outline view in bottom left of screen, shown below
  - GptChannelConfigSet GptChannel ConfigSet\_0 GptChannelConfiguration -RtcTimer
  - <u>GptChannelSrcRef</u>: ensure GptClockReferencePoint\_0 is selected
  - Click the calculate for GptChannelTickFrequency then on the Calculate icon

Soject Explorer 🛛		🗐 *Mcu (Mcu) 🧧 *Gpt (Gpt) 🛛		
Workspace (V1.0.4, AS4.0.3)	•	GptChannelConfiguration		
EcuM (V1.0.4, AS4.0.3)	-	PtcTimer		
▷ 👺 Eth (V1.0.4, AS4.0.3)		Name 🗁		
▷ 👺 Fee (V1.0.4, AS4.0.3)				
▷ 👺 Fls (V1.0.4, AS4.0.3)	=	General		
Gpt (V1.0.4, AS4.0.3)				
🗐 Gpt				
▷ 🛱 Icu (V1.0.4, AS4.0.3)		GptChannelId (0 -> 4294967295)	0	- A -
▷ 🛱 Lin (V1.0.4, AS4.0.3)				
▷ 🛱 Mcl (V1.0.4, AS4.0.3)		GptHwChannel	RTC_0_CH_0	- 📰 <del>-</del>
utline 🖾		GptChannelMode	GPT_CH_MODE_CONTINU	JOUS 🧧 🍕 🛨
Config Variant:VariantPostBuild				
GptDemEventParameterRefs		GptChannelTickFrequency (0 -> 160000000)*	1000.0	- 24 -
Common Published Information				
🔄 🕞 tChannelConfigSet		GptChannelClkSrcRef*	/Gpt/Gpt/GptDriverConfig	guration/GptClockReferencePoint_0
GptChannelConfigSet_0			_	
GptChannelConfiguration		GptRtcChannelClkSrc	RTC_GPT_CLKSRC_SXOSC	• <b> </b>
🗁 GptChannelConfiguratio	n_0			
🕞 GptChannelId:0		GptStmChannelClkSrc	GPT_STM_SYSTEMCLOCK	-
GptHwChannel:RTC_0	)_CH_0			
GptChannelMode:GP	T_CH_MODE_CONTIN	GptChannelPrescaler (1 -> 256)	2	- & -
🔽 🔂 GptChannelTickFrequ	iency*:1000.0			
@ GptChannelClkSrcRef	*:/Gpt/Gpt/GptDriver(	GptChannelPrescalerAlternate (1 -> 256)	1	<b>~</b>

#### Generate

- Generate to check for errors
- Click on Problems View tab
- Click on Errors to expand it
- Click on Calypso6M\_... to expand it
- Right click on expanded error, Invalid value..., select Details
- Details window pops up: details say the maximum count is other than the 65535 value currently entered.
  - Details alternative: See Description window for error information
- Enter new value of 429....
- Generate and there should not be errors.

opumentanie.		KIC_U_CH_U		20 Problems	View Entry Details		
GptChannelMode		GPT_CH_MODE	_CONTINU	Code: Severity:	1019 Error		
GptChannelTickFrequency (0 -> 16000000)*		1000.0		Project nam	e: Calypso6M_mcal4_0	)_soxc_gpt_irq	-
GptChannelClkSrcRef*	Ø	/Gpt/Gpt/GptDr	riverConfig	Path: Detailed Me	/AUTOSAR/TOP-LE\ ssage:	/EL-PACKAGES/	Gpt/ELEMENTS/Gpt/GptChan
GptRtcChannelClkSrc		RTC_GPT_CLKSF	RC_SXOSC	Invalid value GptChannel	e for node "/AUTOSAR, ConfigSet/GptChannel	/TOP-LEVEL-PAC ConfigSet 0/Gpt	CKAGES/Gpt/ELEMENTS/Gpt/ ChannelConfiguration/
GptStmChannelClkSrc	B	GPT_STM_SYSTE	EMCLOCK	<u>GptChannel</u> the PITRTI, P	Configuration 0/GptCh PIT, RTC or STM channe	els is 429496729	Max": The proper value for 95.
GptChannelPrescaler (1 -> 256)		2				1	
ChannelPrescalerAlternate (1 -> 256)	B	1					
GptChan, TickValueMax (65535 -> 4294967295)	TED	65535			•		
Desuit							
🕙 E. yr Log 🛃 Problems View 🛛							
(essa w. nings(26)				?		Copy to Clip	board OK
Calypso6M mcal4 0 soxc gpt irg(1)							counter resolution) and 65535 fo
Invalid value for node "/AUTOSAR/TOP-LEV		Copy Export Log	INTS/G	1019	/AUTOSAR/TOP-LEVEL	L-PACKAG	,,
	E	Expand All					
		Details.					



#### Modules-Add new modules to be built to ap's list

- If present, remove Gio and Port modules (used in prior project)
- Add Gpt and Mcl (GPT requires Mcl to build) modules which will be compiled and linked. In Modules file of application's root folder:

#### MODULE\_LIST := Base Det Dem EcuM **Gpt McI** Mcu Rte

#### SXOSC Code

- SXOSC and other clocks are already enabled by function Mcu\_InitClock
- To get the timer to run, two functions must be added to main: Gpt\_Init(&GptChannelConfigSet\_0); Gpt\_StartTimer(GptConf\_GptChannelConfiguration\_RtcTimer, 512 - 1);
  - The 512-1 value is the timeout value until the flag is set.
- Application code can only basically read the timer value.
- Next section will show how to use the timer as an interrupt.



#### Clean, build, download, debug

Run program and verify RTC increments







#### Introduction

- This project adds to the previous Calypso6M\_MCAL4\_0\_sxosc\_rtc\_irq project.
  - Same Tresos workspace is used (no need to import)
  - Same application folder is used





# Enable Timer (RTC) interrupt





6

## Enable Timer Interrupt (RTC) Example Overview

#### **Configuration**:

- Add GPT channel (done)
- Configure GPT channel for RTC\_0\_CH\_0 and set other parameters for this channel (done)
  - MCAL supports API wakeup only, not RTC wakeup
- Configure (name) notification function (to be executed in ISR between prologue and epilogue)
- Enable the GPT channel in GptHwConfiguration (Only required for MCAL 4.2 and later)
   Code:
- Install interrupt service routine Gpt\_RTC\_0\_Ch\_0\_ISR on vector 225
  - Vector\_vle\_mcal.s file: use name from GPT Integration Manual
- Enable interrupt in initialization task (if OS used then ISR needs to be configured in OS configuration)
- If not done, call Gpt\_StartTimer() using either the timer's ID number or symbolic name
- Write notification function (called by driver ISR) for interrupt [see main.c]



### Summary of MCAL interrupt steps

	Step		Where	Notes		
Tresos	1	Name notification function	Module container. Example: Gpt-GptChannelConfiguration -Channel's Index	Notification function is user code	Î	Prologue
Code	2	Install provided ISR name at IRQ vector	Folder: toolchains File: vector_vle_mcal.s	Get ISR name and vector from module's integration manual	ISR	Notification Function
	3	3 Enable interrupt: priority > 0, core#	File: your initialization code	Write IRQ vector's priority & core # (INTC_PSRx)		Epilogue
	4	Enable notification	File: your initialization code	Sets software flag to make notification function called in ISR		
	5	Add notification function	File: your application code	MCAL driver will clear peripheral flag		



Interrupt Summary: Name Notification for timer example

Gpt module

Tab: GptChannelConfigurationSet Index 0: GptChannelConfigSet\_0 Tab: GptChannelConfiguration

Index 0: RtcTimer

Tab: General (Name: RtcTimeoutNotification)



# Gpt 2- Name notification 2

- Click on <u>red box before</u> <u>GptNotification</u> to enable naming notification
- Enter <u>name</u> for notification function (name will be used in your code): RtcTimeoutNotification
  - Notification function will be called from the ISR.
  - Notification function is for user code in ISR
- Save
- Generate

#### 🕙 \*Mcu (Mcu) 👘 🕙 \*Gpt (Gpt) 🖾 🔪

#### **GptChannelConfiguration**

neral	
GptChannelId (0 -> 4294967295)	0 🗸 🖌
GptHwChannel	RTC_0_CH_0 ▼ 50 ▼
GptChannelMode	
GptChannelTickFrequency (0 -> 160000000)*	1000.0 🕶 🔏 🕶
GptChannelClkSrcRef*	[] /Gpt/Gpt/GptDriverConfiguration/GptClockReferencePoint_0
GptRtcChannelClkSrc	■ RTC_GPT_CLKSRC_SXOSC ▼ 《 ▼
GptStmChannelClkSrc	GPT_STM_SYSTEMCLOCK
GptChannelPrescaler (1 -> 256)	2 🗸 🗸
GptChannelPrescalerAlternate (1 -> 256)	
GptChannelTickValueMax (65535 -> 4294967295)*	i 4294967295
GptFreezeEnable	🚯 📝 🥖 👻 GptEnableWakeup 🙀 🥅 🥖
GptNotification*	RtcTimeoutNotification

#### Enable GPT + ISR channel MCAL4.2 ONLY:

- GptHwConfiguration tab: Check the RTC timer's boxes for:
  - GptIsrEnable
  - GptChannellsUsed
- Save

#### Generate

 <u>Note</u>: screenshot is from MCAL4.2. It is not present in MCAL4.0 since this is done automatically with that version.

#### Gpt

Name 🗁 Gpt

General GptChannelConfiguration GptHwConfiguration GptClockReferencePoint Published Information

#### GptHwConfiguration

3

Index	🗁 Name	GPT Periph	GptIsrEnable	GptChannelIsUsed
34	🗁 GptHwConfiguration_34	PIT_0_CH_8	<b>x</b> 🖂	<b>№</b> □
35	GptHwConfiguration_35	🗟 PIT_0_CH_9	<b>X</b> 🗆	😼 🗆
36	🗁 GptHwConfiguration_36	🗟 PIT_0_CH_10	<b>X</b> 🗆	😼 🗆
37	🗁 GptHwConfiguration_37	🗟 PIT_0_CH_11	x, 🗆	😼 🗆
38	GptHwConfiguration_38	PIT_0_CH_12	x, 🗆	<b>B</b> 🗆
39	🗁 GptHwConfiguration_39	🗟 PIT_0_CH_13	x, 🗆	😼 🗆
40	GptHwConfiguration_40	🗟 PIT_0_CH_14	<b>X</b> 🗆	😼 🗆
41	GptHwConfiguration_41	PIT_0_CH_15	x, 🗆	<b>B</b> 🗆
42	GptHwConfiguration_42	🗟 STM_0_CH_0	x, 🗆	😼 🗆
43	GptHwConfiguration_43	🗟 STM_0_CH_1	<b>X</b> 🗆	😼 🗆
44	🗁 GptHwConfiguration_44	🗟 STM_0_CH_2	<b>3</b> 🖂	😼 🗆
45	GptHwConfiguration_45	🗟 STM_0_CH_3	<b>X</b> 🗆	😼 🗆
46	GptHwConfiguration_46	🗟 STM_1_CH_0	x, 🗆	<b>B</b> 🗆
47	GptHwConfiguration_47	🗟 STM_1_CH_1	x, 🗆	
48	GptHwConfiguration_48	🗟 STM_1_CH_2	x, 🗆	😼 🗆
49	🗁 GptHwConfiguration_49	🗟 STM_1_CH_3	x, 🗆	😼 🗆
50	GptHwConfiguration_50	🗟 STM_2_CH_0	<b>X</b> 🗆	😼 🗆
51	🗁 GptHwConfiguration_51	🗟 STM_2_CH_1	x 🗆	<b>3</b> 🖂
52	GptHwConfiguration_52	STM_2_CH_2	x 🗆	<b>B</b> 🗆
53	GptHwConfiguration_53	STM_2_		<b>№</b> □
54	🗁 GptHwConfiguration_54	RTC_0_CH_0	X	🛛 🗹







#### Get ISR name and interrupt vector number

- See <u>Integration Manual in MCAL's eclipse/plugins/module/doc folder.</u>
- Example from MPC574xG MCAL integration manual: C:\NXP\AUTOSAR\MPC574XG\_MCAL4\_2\_RTM\_1\_0\_0\eclipse\plugins\Gpt\_TS\_T2D35M10I0R0\doc

**Chapter 5 Module requirements** 

ISR Name	Hardware interrupt vector
ISR(Gpt_STM_2_Ch_1_ISR)	45
ISR(Gpt_STM_2_Ch_2_ISR)	46
ISR(Gpt_STM_2_Ch_3_ISR)	47
For RTC	
ISR(Gpt_RTC_0_Ch_0_ISR)	225

#### Table 5-1. GPT ISR's (continued)



#### Install interrupt service routine

 Edit Vector\_vle\_mcal.s file in the application's "toolchains" folder using integration manual's name and interrupt number:

# Gpt related
 .globl Gpt\_PIT\_0\_TIMER\_0\_ISR
 .globl Gpt\_PIT\_0\_TIMER\_1\_ISR
 .globl Gpt\_RTC\_0\_Ch\_0\_ISR

IRQ225:

e\_b Gpt\_RTC\_0\_Ch\_0\_ISR .align ALIGN\_SIZE #interrupt 225



#### Interrupt sequence (shown with RTC)

- RTC interrupt is recognized in hardware
- Execution starts at peripheral's vector (#225 for RTC)
- ISR for vector, Gpt\_RTC\_0\_Ch\_0\_ISR, is executed
- Executes generated prologue
  - If notification is configured, then ISR will call notification function.
    - In this example **RtcTimeoutNotification** is has been configured
    - Notification function will include user application code, such as incrementing counter, writing outputs, etc.
  - Executes generated epilogue



#### Enable interrupt for RTC

- Example- sample\_app\_mcal\_initialization\_task.c
  - Enable core for interrupt and assign priority

#endif

#### **Enable Notification for ISR and Start Timer**

- Add code to start RTC (32KHz divided by 32 here = 1KHz) timer. 2 second timeout.
- Example- main.c (RTC timer is index 2 in Gpt configuration)

void SampleApp\_Int\_Init(void); /\* Local function
prototype\*/

Add before endless loop in main:



#### Add notification function for RTC (executed after timeout)

• File main.c - define variable(s) for notification function:



• Define notification function that will be called in ISR (for example, add to LOCAL FUNCTIONS):

```
FUNC (void, SAMPLE_APP_CODE) RtcTimeoutNotification(void)
{
    GptRtcCounter++;
}
```





#### **Complete project**

- If not done, save and generate code in Tresos
- Copy include and src folders from Tresos to application's cfg folder
- Clean
- Run launch.bat
- If no errors, download and run.
- Verify GptRtcCounter increments



# Reference: NXP AUTOSAR Installation



6



# 1. Get AUTOSAR installation SW to your NXP account






# Get AUTOSAR installation SW from web to your account 1

- Go to nxp.com Click on desired product Example: MPC5748G:
  - 1. Click on PRODUCTS
  - 2. Click on POWER ARCH....
  - 3. Click on MPC5xxx/55xx 32-bit MCUs

\*<u>Note</u> The ISO26262 MCAL version is developed according the SEooC requirements of ISO26262. NXP supplies a FMEA and a Safety Manual for this MCAL. The Safety Manual will state the MCAL's ASIL level.





## Get AUTOSAR installation SW from web to your account 2

Click on MPC57xx

PRODUCTS SOLUTIONS	SUPPORT	ABOUT
Microcontrollers and Processors	s ∽ Power Architec	ture® Processors ~ MPC5xxx/55xx
Microcontrollers and Processors	AUT	OMOTIVE
Arm®-Based Processors and MCUs		
More Processors	32-E	BIT MCUS
Power Architecture® Processors	Bardan	
QorlQ Platforms	Based on F	Power Architecture® Lechn
PowerQUICC Processors	1 164	
MPC5xxx/55xx 32-bit MCUs		
Ultra-Reliable MPC57xx 32-bit Automotive & Industrial Microcontrollers (MCUs)	MPC5xxx with a focu	MCUs offer scalable, higly i us on quality and long-term
Ultra-Reliable MPC56xx 32-bit Automotive and Industrial Microcontrollers (MCUs)		
MPC55xx MCUs		Products
mobileGT <sup>®</sup> (51xx/52xx)	-	
5xx Controllers		
Interested Hand Devenue	MPC57xx	
Identification and Security	Powertrain, B	ody Control, ADAS
Interfaces	Single to m	ulticore Functional Safety comp



## Get AUTOSAR installation SW from web to your account 3

MPC57xx Products

- Scroll down on next page
- Click MPC574xB-C-B

Product	Description
MPC577xK	32-bit MCU for ADAS Applications - PREPRODUCTION
MPC574xB-C-G	Ultra-Reliable MCUs for Automotive & Industrial Control and Gateway
MPC5746R	Automotive & Industrial Engine Management MCU
MDOETAND	Little Deliable MD0574vD M011 for Automative & Industrial Opfaty Applications

 Click on Software & Tools



## Get AUTOSAR installation SW from web to your account 4

1. Scroll down to AUTOSAR and click on desired Download

#### AUTOSAR (3)



#### MPC574xB-C-G Autosar 4.0 MCAL (QM)<sup>(REV 1.0.x)</sup>

Autosar (Classic) v4.0 rev 3 microcontroller abstraction layer drivers for ADC, CAN, DIO, ETH, FLS, FR, GPT, ICU, LIN, MCU, PORT, PWM, SPI, WDG, plus additionally FEE and DMA driver. Tested with Green Hills MULTI, Wind River DIAB. Autosar configuration tool EB tresos Studio included, solely for use with drivers of this package. Conditions of use for this software and other general information about Autosar at www.autosar.org and at Autosar

III ZIP 1 KB SW574XG-MCAL401E

2017-02-14 18:00:00

- MPC574xB-C-G Autosar 4.0 Operating System (QM)<sup>(REV b4.0.88)</sup> Sign in Email Address [nxa12345] Password
  - Password

Download

#### 2. Sign in with name and password

- If you do not have an account, register
- NXP & new employees use NXP ID and NXP password
- (Legacy Freescale employees use core ID and oneIT password)
- 3. Select desired product. Depending on product age, a registration code may be needed to add installation software to your account. Contact local sales office if needed.





# 1b. Get AUTOSAR installation SW from NXP sW marketing to your NXP account





# Get AUTOSAR installation SW from marketing to your account

- Some new version of MCAL, OS are not on the NXP public web site
- Customers can request these newer versions by:
  - Create an account a nxp.com if they do not have one:
    - Go to <u>www.nxp.com</u>
    - Click on ACCOUNT in upper right corner
    - Click on REGISTER and fill in the fields
  - Provide their name(s), project, requested software (version and chip) to the local sales office
  - The sales representative will forward the request to NXP software marketing.
  - -When approved, a registration code will be provided to customer(s). A code is good for one PC.





# 2. Download MCAL / OS installation from your NXP account



L STEVE - HENGLISH - H

- Sign in to your account at nxp.com
- Click on drop-down arrow next to your name, then click on "MY ACCOUNT"



PRC	DUCTS	SOLUTIONS	SUPPORT	ABOUT	N	ALL ~	Search
	My Account				М.		

#### Welcome, Steve Mihalik

You logged in at 01-09-2018 07:23:10 MST

The most up-to-date technical documents, select Documentation and Tools Updates below. The tool is updated daily and is searchable and sortable by type, revision date and product.

- Click "Software Licensing and Support"
- NOTE: customers usually need a unique registration code to download a software product
- PROFILE
- Change My Password
- Orders
  - Subscription Center
- Document, inpland Tools Updates

#### **?** SUPPORT

- View all Communities
- View all Support Methods
- View Existing Issues
  - Ticket begins with 1-XXX or 2-XXX

#### SECURE APPLICATIONS

- Cross Check Part Finder With Pricing
- Cross Check Competitor Cross Reference
- eCommerce
- Compass Extranet
- MCU Programming Center
- Moderated Downloads
- ROM Programming
- Software Licensing and Support
- My LPCXpresso activations
- NXP Training

 Click on desired category (the first time you will have only one category)

- Example:

Automotive SW – Autosar MCAL

#### PRODUCTS APPLICATIONS SUPPORT

NXP > Software & Support > Product List

Software & Support Product List Product Search Order History Recent Product Releases Recent Updates

#### **Product List**

Welcome to the premier delivery, update, and manage your rapidly changing software from a

To access an item, select a product below.

Licensing

License Lists Offline Activation

Automotive SW - Autosar MCA Automotive SW - Autosar MCAL / ISO26262 Automotive SW - Autosar OS ALLER AND ONL FREEDTOO



PRODUCTS

APPLICATIONS

SUPPORT AE

NXP > Software & Support > Product Information : Automotive SW - Autosar MCAL

- Click in software you want to dow
  - Example:

Automotive SW – Autosar MCAL

Note: Register button is for custor

S	oftware & Support	Product Information
	Product List	
	Product Search	Automotive SW - Autosar MCAL
	Order History	
	Recent Product Releases	
ſ	Recent Updates	Your choice contains a suite of products. Please To register a New Product please click on the but
L	icensing	Degistor
	License Lists	Register
	Offline Activation	Automotive SW - Autosar MCAL
F	AQ	Automotive Sw - Elektrodit Tresos Studio / Autosa
	CC	MPANY PUBLIC   118



PRODUCTS	APPLICATIONS	SUPPORT	ABOUT

NXP > Software & Support > Product Information : Automotive SW - Autosar MCAL

#### Software & Support **Product Information** Product List Product Search Automotive SW - Autosar MCAL Order History Recent Product Releases To register a New Product please click on the button belo Recent Updates Register Licensing License Lists Previous Offline Activation Current FAO Version Description Download Help 1.0.4 SW574XG-MCAL401-RTMC\_SR-1.0.4 Table of Contents 1.0.0 SW32K14-MCAL421-RTMC-1.0.0 FAQs 0.4.0 SW32G17-MCAL422-EAR-0.4.0 102 SWEETYK MCALAD1 DTMC 10.2

#### Click on desired version. Examples – MCAL 4.0, MCAL 4.3:

#### **Product Information**

#### Automotive SW - Autosar MCAL

To register a New Product please click on the button below

Register

Current	Previous
Version	- Description
1.0.0_P2	SW574XG-MCAL431-RTMC_1.0.0_P2
1.0.0_P1	SW574XG-MCAL431-RTMC_1.0.0_P1
1.0.0	SW574XG-MCAL431-RTMC-1.0.0
105	SW574XG_MCAL401_RTMC_SR_1.0.5
	COMPANY PUBLIC 119



 Click on I Agree to SW terms and conditions

#### NP

#### PRODUCTS APPLICATIONS SUPPORT ABOUT

NXP > Software & Support > Software Terms and Conditions

Software & Support

Product List Product Search Order History Recent Product Releases Recent Updates

#### Software Terms and Cond

#### SW574XG-MCAL401-RTMC SR-1.0.4

Please read the following agreement and click "I AGREE" a

Licensing

License Lists Offline Activation NXP Automotive Software License Agreement v1.6

(System Agreement ID 21147)

FAQ

Download Help Table of Contents FAQs

IMPORTANT. Read the following NXP Software the "I Accept" button at the end of this page, you may then install the software.

#### NXP SOFTWARE LICENSE AGREEMENT

This is a legal agreement between you (either as

I Agree Cancel COMPANY PUBLIC 120



- Check boxes for software to download
  - Examples: MCAL4.0, MCAL 4.3
- Click Download Selected Files
- <u>Note</u>: some versions have "hot fixes" to enhance a version. Hot fixes are cumulative – only need to install the last one after installing the released SW. See hotfix's release notes to see if there is any impact to your application.

PRODUCTS			Files	License Keys Notes
PRODUCTS	APPLICATIONS SUPPORT	Sh	ow Al	Il Files 😑
NXP > Software & Support >	Automotive SW - Autosar MCAL > SW574XG-MCAL40	1-RTMC_SR	+	File Description
Software & Support	Product Download		+	MPC574XG_MCAL4_2_RTM_1_0_0_Report.xlsx
Product List	i loudot Bomiloud	Jan	+	MPC574XG_MCAL4_3_RTM_1_0_0.exe
Product Search Order History	SW574XG-MCAL401-RTMC_SR	-1.0.4	+	MPC574XG_MCAL4_3_RTM_1_0_0_QualityPackage.zip
Recent Product Releases	Files License Keys Notes		+	MPC574XG_MCAL4_3_RTM_1_0_0_ReleaseNotes.pdf
Recent Updates	Show All Files		++	MPC574XG_MCAL4_3_RTM_1_0_0_Sample_App.exe MPC574XG_MCAL4_3_RTM_1_0_0_Sample_App_ReleaseNotes.pdf
License Lists Offline Activation	H     File Description       H     MPC574XG_MCAL4.0_RTM_1.0.4_Quarter	alityPackage.	+	MPC574XG_MCAL4_3_RTM_HF1_1_0_0.exe
	HPC574XG_MCAL40_RTM_104Report	.html	+	MPC574XG_MCAL4_3_RTM_HF1_1_0_0_ReleaseNotes.txt
FAQ Download Help	MPC574XG_MCAL4_0_RTM_1_0_4.ex	e 🗌	+	MPC574XG_MCAL4_3_RTM_HF2_1_0_0.exe
Table of Contents	MPC574XG_MCAL4_0_RTM_1_0_4_R	eleaseNotes	+	MPC574XG_MCAL4_3_RTM_HF2_1_0_0_ReleaseNotes.txt
FAQs	Download Selected Files		ownio	oad Selected Files
		CC	MPA	ANY PUBLIC   121

SW574XG-MCAL431-RTMC-1.0.0

 Select folder (create if needed) to download installation software and click OK.

This folder can be anywhere, e.g., when you later run the install software, you can choose a path for the actual software package.

- Download progress is shown
- Close window when download completes.





### Download AUTOSAR OS if needed

- If you also received a license code for the OS, follow the same steps to download the OS
- NOTE: You must install TRESOS before MCAL or OS!!
- You must find the Tresos version needed for your MCAL or OS. See release notes for MCAL or OS. Search for Tresos.



## 3. Download, Install / renew Tresos



6

## **Tresos Versions**

- The Tresos version used for testing an MCAL version is put in your NXP Account with your requested MCAL version.
- To determine or verify what Tresos version an MCAL used, search for "Tresos" in MCAL's or OS's Release notes. (Normally higher number versions will work fine.)
  - 2.4 Modules Configuration

Modules configuration were developed and tested using the Tresos Configuration Tool version "*EB tresos Studio* 14.2.1 b140128-1223"

• **INSTALLATION TIP**: When installing Tresos include the version number in the Tresos folder name. This prevents confusion when another Tresos version is installed for a different MCAL version. Example:



EB\_Client\_License\_Administrator

tresos14 2 1

tresos21\_0\_0\_G\_safety

## Tresos Licensing: License File vs Activation Code

- Tresos version requirements:
  - Tresos versions before 17.0.0 require Tresos license files for older MCALs
  - Tresos versions 17.0.0 & after require Tresos activation codes for newer MCALs

- Examples:	<b>Tresos Version</b>	Requirement	MCAL Version
	14.2.1	License File	MPC574xG MCAL 4.0
	21.0	Activation Code	MPC574xG MCAL 4.2
	23.0	Activation Code	S32K14x MCAL 4.2
	24.0	Activation Code	MPC574xG MCAL 4.3 S32K14x MCAL 4.3

- Tresos license codes & activation codes are good for one calendar quarter (3 months)
- If you have a valid license in your account, a new license file or activation code is automatically added to your account by the end of the quarter





3A. Download, Install / renewTresos with activation code(versions 17 & after)



# Install Tresos – Versions 17.0.0 and after (1 of 5)

- Go to Product Download for Tresos (example v21.0.0):
  - Sign in to your account at nxp.com
  - Click the following: Software Licensing and Support
     → Autosar SW Autosar MCAL
     → Autosar SW Elektrobit Tracco Studio / Autosar
    - → Autosar SW Elektrobit Tresos Studio / Autosar Configuration Tool
    - $\rightarrow$  AUTOSAR Tresos Studio 21.0.0
    - $\rightarrow$  I agree
- Select all files and download to any folder. Be sure to use "Download Selected Files" which uses the Download Manager or else files will be compressed in the wrong format.

#### **Product Download**

#### AUTOSAR Tresos Studio 21.0.0

Ple	ease	use the following activation code to start your evaluation
12	/31/2	2017)
Sho	w Al	I Files =
_		
V	+	File Description The S
V	+	1.1_EB_tresos_installation_guide.pdf
1	+	Documentation_Documentation_EBtresosStudio.uip
1	+	EBtresosStudio_EBtresosStudio.uip
1	+	EBtresosStudio_StudioVariantPlugin.uip
1	+	EBtresosStudio_WibuKeyRuntime.uip
1	+	EB_Client_License_Administrator_1_2_4_Setup.exe
	+	setup.exe

COMPANY PUBLIC | 128



## Install Tresos – Versions 17.0.0 and after (2 of 5)

- Note the <u>activation code</u> will be needed in subsequent steps
  - Codes are updated every 3 months.
  - See "Files" tab of Product Download in your account.
  - Copy and paste Tresos activation code to some temporary location for use later.





Install Tresos – Versions 17.0.0 <u>and after</u> (3 of 5)

- Run setup.exe from the downloaded folder
- Choose unique folder (Tip: include Tresos version # in the name)
- Click Install

#### Name

1.1\_EB\_tresos\_installation\_guide.pdf
 Documentation\_Documentation\_EBtresosStudio.uip
 EB\_Client\_License\_Administrator\_1\_2\_4\_Setup.exe
 EBtresosStudio\_EBtresosStudio.uip
 EBtresosStudio\_StudioVariantPlugin.uip
 EBtresosStudio\_WibuKeyRuntime.uip
 setup.exe



## Install Tresos – Versions 17.0.0 and after (4 of 5)

- If EB Client License Administrator is not already on your PC, install it.
  - Look for the program in the Windows program menu
  - To install, start the EB Client Administrator setup program from your downloaded folder.
- Start EB Client Administrator program from Start menu
- If necessary, click Next to get thru dialog boxes until you get to FlexLM License information
  - Click Use License Activation Codes
  - Click Next

#### Name

- 1.1\_EB\_tresos\_installation\_guide.pdf
- Documentation\_Documentation\_EBtresosStudio.uip
- EB\_Client\_License\_Administrator\_1\_2\_4\_Setup.exe
- BtresosStudio\_EBtresosStudio.uip
- EBtresosStudio\_StudioVariantPlugin.uip
- EBtresosStudio\_WibuKeyRuntime.uip
- 🚦 setup.exe





## Install Tresos – Versions 17.0.0 and after (5 of 5)

- You will be prompted for one of the following dialog boxes:
  - Click Install or
  - Enter Tresos activation code (stored earlier; see slide 2 of 5)
  - Click Activate (You must be on line to activate

Tresos is now activated

	Product	Activation Code	Fulfillmon	Tructed Status	Evniro Dato	Maintenance	Activate
	NONE	-	-	-	-	permanent	-
	Offline Activation	ess Artivation	Activation Code Please select a	code from the list	Mumbe	Onlin	ne Activation
s	Offline Activation Create Activation Request Proce	ess Activation	Activation Code Please select a or enter a new AC5C-6C30-2	code from the list code: 789-6217	License	onlin 25:	ne Activation Activate Upgrade

### Tresos Activation Code Renewal 1 (For versions 17.0.0 and after)

- Every 3 months the Tresos activation codes expire
- Customers with an active MCAL license (such as DISM or evaluation), will automatically get a new activation code in their account.
- Obtaining new Tresos activation code (for Tresos 17.0.0 or later):
  - 1. Go to Tresos in your nxp account:
  - Sign in to your account at nxp.com
  - Click the following: Software Licensing and Support
    - → Autosar SW Autosar MCAL
    - → Autosar SW Elektrobit Tresos Studio / Autosar Configuration Tool
  - 2. Select the Tresos version for your MCAL or OS. Example:

#### **Product Information**

### Automotive SW - Elektrobit Tresos S

To register a New Product please click on the buttor

Current	Previous
Version	Description
24.0.1	AUTOSAR Tresos Studio 24.0.1
24.0.0	AUTOSAR Tresos Studio 24.0.0
23.0.0	AUTOSAR Tresos Studio 23.0.0
21.0.0	AUTOSAR Tresos Studio 21.0.0



**Tresos Activation Code Renewal 2** (For versions 17.0.0 and after) 3. Agree to SW terms and conditions. 4. Copy activation code Product Download AUTOSAR Tresos Studio 21.0.0 Files License Keys Notes Please use the following activation code to start your evaluation: C195-344A-C

09/30/2018)

#### **Software Terms and Conditions**

#### AUTOSAR Tresos Studio 21.0.0

Please read the following agreement and click "I AGREE" at the bottom be:

**1.2 'AGREEMENT'** means the QUOTATION as accepted by CUSTOMER and the annexes thereto, together with these EB T&C.

1.3 'BACKGROUND IP' means any software, hardware, documentation, materials, data, technology, information, know-how and INTELLECTUAL PROPERTY RIGHTS, in whatever form, that have been created or developed prior to or independently of the PROJECT governed by this AGREEMENT.

1.4 'BUSINESS DAY' means Monday to Friday from 9.00 a.m. to 5.00p.m. (CET), except German (Bavarian) public holidays.

COMPANY PUBLIC | 134

Cancel

Aaree

Tresos Activation Code Renewal 3 (For versions 17.0.0 <u>and after)</u>

5. From the Windows start menu, start EB Client License Administrator







Tresos Activation Code Renewal 4 (For versions 17.0.0 and after)

- 6. In the dialog box:
  - Enter (paste) the new Tresos activation code.
    (<u>Note</u>: you must be on line to activate.)
  - Click Activate

Tresos is now activated

NONE	-	-	-	-	permanent	-
fline Activation		Activation Code	8		Onlin	ne Activation
Create Activation Request Proce	ss Activation	Please select a or enter a new	a code from the list v code:	Licens	es:	Activate
Create Return Request	5	0030-2	2709-0217			Upgrade



3B. Download, Install / Renew Tresos with License File (versions before 17)



## Install Tresos – Versions <u>before</u> 17.0.0 (1 of 4)

- Go to Product Download for Tresos:
  - Sign in to your account at nxp.com
  - Click the following: Software Licensing and Support
     → Autosar SW Autosar MCAL
     → Autosar SW Elektrobit Tresos Studio / Autosar Configuration Tool
     → AUTOSAR Tresos Studio 14.2.1
     → I agree
- Select all files and download to any folder. Be sure to use "Download Selected Files" which uses the Download Manager or else files will be compressed in the wrong format.
  - Versions before 17.0.0: also download license (versions 17.0.0 and later use activation codes)

#### NP

#### **Product Download**

#### AUTOSAR Tresos Studio 14.2.1



## Install Tresos – Versions <u>before</u> 17.0.0 (2 of 4)

Documentation.uip

• Unzip the downloaded Tresos file

•

STUDIO\_14.2.1\_2014-01-28.zip Documentation\_Documentation\_EBtresosStudio.uip EB tresos\_installation\_guide.pdf Studio\_new\_and\_noteworthy.pdf EBtresosStudio EBtresosStudio.uip Studio\_release\_notes.pdf EBtresosStudio\_StudioVariantPlugin.uip • Run setup.exe 🚼 setup.exe tresos\_Freescale\_Customer\_2017-12-31.lic STUDIO\_14.2.1\_2014-01-28.zip Studio\_new\_an Options 🕀 🔽 Documentation Studio\_release ⊕ 🔽 EBtresosStudio Create Link on Desktc tresos Freescal Create Program Group Choose unique Choose Folder ... folder C:\EB\tresos14\_2\_1\ Status Overall Package Click Install Install Exit

EB\_tresos\_installation\_guide.pdf

## Install Tresos – Versions before 17.0.0 (3 of 4)

- Start Tresos
- First time: a workspace needs to be created.
  - Click Yes.
- Close Welcome screen
- After several seconds, the License Information window appears
  - Click Licenses..

• Click "+"





## Install Tresos – Versions <u>before</u> 17.0.0 (4 of 4)

- Navigate to where you downloaded the Tresos license file (probably with other Tresos installation files from your account
- Select license file
- Click Open
- Click OK

🙏 Tresos		
📕 Tresos 14.2.1	Name	
1 STODIO_1121_	tresos_Freescale_Customer_2017-12-	31.lic
L Tresos 14.2.1 cop		
🔥 STUDIO_14.2.0_2(		
🐌 Licensing - pricing		
📙 Old		
📙 Slides		
📙 Benchmarks		
📙 Blocks + my code 🕚	III	
File name:	tresos_Freescale_Customer_2017-12-31.liv 🔻	*.lic Open
Installing Licenses		<b>×</b>
Successfully installed	licenses.	
		ОК

COMPANY PUBLIC

141

## License File Renewal 1 – Tresos versions <u>before</u> 17.0.0

- Every 3 months the Tresos license file expires
- Customers with an active MCAL license (such as DISM or evaluation), will automatically get a new license file in their account.
- Obtaining new Tresos license:
  - Sign in to your account at nxp.com
  - Click the following: Software Licensing and Support
     → Autosar SW Autosar MCAL
    - → Autosar SW Elektrobit Tresos Studio / Autosar Configuration Tool

- Download the most recent license to a file

Product Download

AUTOSAR Tresos Studio 14.2.1

Fil	es	License Keys Notes		
Show All Files				
	+	File Description		
✓	+	Evaluation License		
<b>~</b>	+	EB_tresos_installation_guide.pdf		
<b>~</b>	+	STUDIO_14.2.1_2014-01-28.zip		
<b>~</b>	+	Studio_new_and_noteworthy.pdf		
<b>~</b>	+	Studio_release_notes.pdf		
	Fil Show	Files         Show All         • <tr< th=""></tr<>		

**Download Selected Files** 



### License File Renewal 2 – Tresos versions before 17.0.0

- Start Tresos
- Go to Help Licenses
- Select the downloaded license.








• Go to

https://sourceforge.net/projects/getgnu win32/files/

 <u>Note</u> – screenshots are done with Windows 7. Windows 10 may differ.

Click on getgnuwin32



Click on the recent version









- Click on version to download
- Wait for download to complete

ð) 🗉	https://sou	irceforg	e.net/pr	ojects/	/getgnu	uwin32,	/file 🖌	ρ.	Ç	📑 Gnu	Win32
dit View	Favorites	Tools	Help								
SailPoint											
Auverusemen	ı										
Hama / Bri	waa / Rusinaa	o 8 Entorn	vrieo / Office	a/Bueiner	ee / Could	/in32 / Ei	lee				



Summary Files Reviews Support Wiki Tickets • News Discussion Donate

Looking for the latest version? Download GetGnuWin32-0.6.3.exe

#### Home / getgnuwin32 / 0.6.30

Name +	Modified +	Size +	Downloads / Week ¢	
<b>↑</b>	Parent	t folder		
source	2016-08-06		ο 🗌	
openssl-1.0.2h.zip	2016-08-06	4.3 MB	7	ĺ
openssl-1.0.2h.sha	2016-08-06	62 Bytes	2	i
HOWTO_Proxy_Support.txt	2013-04-18	1.1 kB	10 📃 🕚	į
README	2011-09-07	175 Bytes	11 🔔 🛛	i
HOWTO_Add_GnuWin32_To_Syste	2011-05-29	61.6 kB	19 📃 🛛	i
Release_Notes_0.6.3_amended.txt	2009-11-16	1.7 kB	6	i
GetGnuWin32-0.6.3.exe	2009-11-16	3.4 MB	1,017 🛕 (	i
GetGnuWin32-0.6.3.sh Click to down	nload GetGnu\	Win32-0.6.3.exe	3	i
Totals: 9 Items		7.8 MB	1.075	



 Go to your download file and double clicken on it to start installation

Click Accept

🖭 Open Share with • New folder anize 🔻 Users Name 😚 GetGnuWin32-0.6.3.exe Momentum GNU GENERAL PUBLIC LICENSE Version 2, June 1991 Copyright (C) 1989, 1991 Free Software Foundation, Inc. 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA Everyone is permitted to copy and distribute verbatim copies of this license document, but c Preamble The licenses for most software are designed to take away your freedom to share and chang intended to guarantee your freedom to share and change free software--to make sure the su applies to most of the Free Software Foundation's software and to any other program whose Foundation software is covered by the GNU Library General Public License instead.) You ca When we speak of free software, we are referring to freedom, not price. Our General Public freedom to distribute copies of free software (and charge for this service if you wish), that yo change the software or use pieces of it in new free programs; and that you know you can do To protect your rights, we need to make restrictions that forbid anyone to deny you these right translate to certain responsibilities for you if you distribute copies of the software, or if you m For example, if you distribute copies of such a program, whether gratis or for a fee, you mus Decline Accept **COMPANY PUBLIC** 148

▶ Computer ▶ Primary (C:) ▶ Users ▶ r17052 ▶ Downloads

• If desired, change GetGnuWin subfolder location. (Steve's default folder is shown)

- Click Install
- A subfolder is created called GetGnuWin32



Cancel



Follow

 instructions
 to complete
 installation
 as shown
 on the
 following
 slides





• Change directory to the subfolder created in your download folder



ated GnuWin32	2 Download Version 0.6.3	X
	Maintaining a GnuWin32 package list	*
R J	Please select a folder in which to place the GetGnuWin32 subfolder. You may want to read more about this tool in the following lines.	
	As of this release GetGnuWin32 no longer attempts to parse sourceforge's website project pages. Instead, a digitally signed update containing a master list of projects and hashes is downloaded and verified. This program and its dependencies are automatically updated using that method as well.	ш
	Here are instructions to get started right away: After installation open a command window, switch to the GetGnuWin32 directory and run download.bat. If download.bat completes successfully you will be able to run the installer. For example:	
	C:\Users\Internet\Desktop>cd getgnuwin32 C:\Users\Internet\Desktop\GetGnuWin32>download	
	C:\Users\Internet\Desktop\GetGnuWin32>install C:\gnuwin32	Ŧ
progress	Destination folder       C:\Users\r17052\Downloads      Browse	

Cancel

Install



### Run "download"



W Automated GnuWin32 Download Version 0.6.3



Installatio

	Maintaining a GnuWin32 package list	
Ŷ	Please select a folder in which to place the GetGnuWin32 subfolder. You may want to read morthis tool in the following lines.	e about
	As of this release GetGnuWin32 no longer attempts to parse sourceforge's website project page Instead, a digitally signed update containing a master list of projects and hashes is downloaded verified. This program and its dependencies are automatically updated using that method as wel	s. and 1.
	Here are instructions to get started right away: After installation open a command window, switch to the GetGnuWin32 directory and run down If download.bat completes successfully you will be able to run the installer.	nload.bat.
	For example: C:\Lisers\Internet\Deslton\cd.gatanuwin32	
	C:\Users\Internet\Desktop\GetGnuWin32>download	
	C:\Users\Internet\Desktop\GetGnuWin32>install C:\gnuwin32	
on progress	Destination folder	
-	C:\Users\r17052\Downloads	Browse

Cancel

Install

COMPANY PUBLIC 152



 Run install command with proper path (default shown is fine)



Installation p

eu onuvinoz		
	Maintaining a GnuWin32 package list	*
S.	Please select a folder in which to place the GetGnuWin32 subfolder. You may want to read more about this tool in the following lines.	
	As of this release GetGnuWin32 no longer attempts to parse sourceforge's website project pages. Instead, a digitally signed update containing a master list of projects and hashes is downloaded and verified. This program and its dependencies are automatically updated using that method as well.	Ш
	Here are instructions to get started right away: After installation open a command window, switch to the GetGnuWin32 directory and run download.bat. If download.bat completes successfully you will be able to run the installer. For example:	
	C:\Users\Internet\Desktop>cd getgnuwin32 C:\Users\Internet\Desktop\GetGnuWin32>download	
	C:\Users\Internet\Desktop\GetGnuWin32>install C:\gnuwin32	-
rogress	Destination folder	
	C:\Users\r17052\Downloads ▼ Browse.	

Cancel

Install

- During installation you may get a message that utilities are outdated, and given the opportunity to update.
  - IMPORTANT: Hit return for every prompt -anything else may cancel installation
- The make utilities by default will be in:
  - C:\gnuwin32\bin

Administrator: Command Prompt - install c:\gnuwin32	
Would you like more information? [yes]	
▶	
UnZip 6.00; unzip.exe (2009-04-20) (bugfixes, security fixes) ^ Suggested replacement for GnuWin32 UnZip 5.51 of May 2004	
OpenSSL 1.0.2h; all binary files (2016-05-03) (bugfixes, security f ^ Suggested replacement for GnuWin32 OpenSSL 0.9.8h of May 2008	ixes)
Wget 1.12.1-dev; wget.exe (2010-06-23) (bugfixes, security fixes) >> No international support, and english help text only. >> Built from wget mainline 013c8e2f5997 2010-03-04 ^^ Suggested replacement for GnuWin32 Wget 1.11.4 of Jun 2008	
Bundle of CA Root Certificates; cacert.pem (2016–04–29) >> Extracted from Mozilla Certificate Authorities list. ^^ Suggested replacement for <unknown></unknown>	
Sort 7.6 testbuild; sort-7.6.exe (2009-09-11) (bugfixes) >> Properly supports LC_ALL environment variable. Test build. ^^ Suggested supplement to GnuWin32 Sort 5.3.0 of April 2005	
Would you like to install these utilities? [yes]	









## Install MCAL or OS Software 1

- If you plan to use MCAL, just install MCAL.
  - If you also plan to use OS, then install OS also.
- · Double click on the installation SW that you downloaded.
  - Example organization of download folders
- <u>Note</u>: some versions have an installation for MCAL and an installation for the MCAL sample application. Both need to be installed.



#### Name

- MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0\_ReleaseNotes
- MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0\_Sample\_App\_ReleaseNotes

MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0

MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0\_Sample\_App



## Install MCAL or OS

## Software 3

 A license file is needed. Go back to Product Download Page and click on License Keys tab.

(If logged off, go to nxp.com & sign in ACCOUNT, then click, e.g.: Software Support and Licensing  $\rightarrow$ Automotive SW – Autosar MCAL $\rightarrow$ Automotive SW – Autosar MCAL $\rightarrow$ SW574xG-MCAL401-RTMC\_SR 1.0.4  $\rightarrow$  I agree)

Click on License Keys tab

NXP MPC574XG AUTOSAR 4_0 MCAL RTM 1_0_4 Setup				
License check NXP MPC574XG AUTOSAR 4_0 MCAL license	NP			
	NP			
	PRODUCTS	APPLICATIONS	SUPPORT	ŀ
Please select a license file.	NXP > Software & Support > /	Automotive SW - Autosar MC/	AL > SW574XG-MCAI	L <b>401</b> -
	Software & Support Product List	Product [	Download	ł
NXP	Product Search Order History	SW574XG-MCA	L401-RTMC_S	;R-1
S Back	Next > Recent Product Releases	License Ke	ys Notes	
	Licensing	Show All Files	U	
	License Lists	+ File Description	'n	
	Offline Activation	+ MPC574XG_N	1CAL4.0_RTM_1.0.4_0	Qualit
	FAQ	<ul> <li></li></ul>	1CAL40_RTM_104Rep 1CAL4_0_RTM_1_0_4	oort.ht
	Table of Contents	✓ + MPC574XG_N	1CAL4_0_RTM_1_0_4	Rele
	FAQS			

**Download Selected Files** 

## Install MCAL or OS Software 4

- If more than one license is shown, find the one that shows an expiration date that is in the future by checking the displayed Expiration Date after generating. Example:
  - Click on one license check box
  - Click on Generate Generate
  - View expiration date (licenses cannot be generated after expiration date!)



#### License Information

#### SW574XG-MCAL431-RTMC-1.0.0

#### Generate Select All

Item Des	cription	MPC574xB-C-G Au
Order Nur	mber	STEVE-MIHALIK-EVA
Purchase	Order Number	
Total Num	ber of Licenses:	1
License A	pplicable to Product(s):	
Version	Description	
1.0.0	SW574XG-MCAL431-RTM0	C-1.0.0 (View EULA)
	1 Available	

Item Desc	cription	MPC574xB-C-G Au
Order Nur	nber	SW574XG-MCAL431E
Purchase	Order Number	
Total Num	ber of Licenses:	2
License A	pplicable to Product(s):	
Version	Description	
1.0.0	SW574XG-MCAL431-RTMC	-1.0.0 (View EULA)
	1 Available	



## Install MCAL or OS Software 5

Click "Save All"

- Save generated license to a file.
   Suggestions:
  - Provide a common license folder. Example
     C:/NXP/AUTOSAR/licenses
  - Choose a name with identification and .dat extension. Example: license\_MPC574xG\_MCAL4\_3.dat

#### **View Licenses**

Below are the licenses you selected to view.

License Overview Print Friendly

Save All

License Applicable to Product(s): <u>Version</u> 1.0.0 <u>Description</u> SW574XG-MCAL431-RTMC-1.0.0 License Quantity: 1

Host ID: ANY Generated By: Steve Mihalik on Aug 16, 2018



## Install MCAL or OS Software 5 License check Go back to the MCAL installation program and click on the

navigate button to locate your license file







## Install MCAL or OS Software 6

NXF MPCS74XG AUTOSAR 4,0 MCAL RTM 1,0,4 Setup	- E \$3		
License check NXP MPC374XG AUTOSAR 4_0 MCAL license	NO	<ul> <li>Click Next</li> </ul>	
			NP NXP MPC574XG AUTOSAR 4_0 MCAL RTM 1_0_4 Setup
Planse select a license file.		<ul> <li>Click Next to</li> </ul>	Choose Install Location Choose the folder in which to install NXP MPC574XG AUTOSAR 4_0 MCAL RTM 1_0_4.
NXP MPC574XG AUTOSAR 4.0 MCAL RTM 1.0.4 Setup	nt > Cancel	install selected components	Setup will install NXP MPC574XG AUTOSAR 4_0 MCAL RTM 1_0_4 in the following folder. To install in a different folder, click Browse and select another folder. Click Next to continue.
Choose Components Choose which features of NXP MPCS74XG AUTOSAR 4_0 MCAL RTM 1_0.	4 you NP		Destination Folder
went to install. Check the components you want to install and uncheck the components y install. Click Next to continue.	ou don't want to	Chose	C:\NXP\AUTOSAR\MPC574XG_MCAL4_0_RTM_1_0_4 Browse
Select components to install:		destination folder (default shown) and click Next	Space required: 209.1MB Space available: 221.2GB NXP
tion < BackNext >	Cancel		



## MCAL Installation only – specify path 7a

- EB Tresos install directory: Click to location where Tresos is installed
- Select proper Tresos directory (If you have multiple"Tresos" folders, select the right one!)
- Click OK
- Click Install
- When done, click Finish



## OS Installation only – specify paths 7b

- Browse to and specify required paths. Example:
- GHS 201416 is recommended (it was validated with this version)
- Cygwin is required if using OS Sample Application from OS installation
- Tresos path is mandatory
- Click Install
- Click Finish when done

Wind River (DIAB) Toolchain Path	
Concert tille MUNITY To al desig Date	
C:\ghs\comp_201416	
Cygwin Bin Directory	
C:\cygwin\bin	
EB Tresos Studio	
C:\EB\tresos14_2_1	

## AUTOSAR OS

• If the OS is to be installed, repeat prior installation steps for OS









# 6A. Sample Application Installation& Configuration MPC574xG MCAL4.0



## MCAL 4.0: Edit sample application launch.bat 1

 Edit the launch.bat file in MCAL's sample application file to provide proper paths for your PC.

 The launch.bat location will be in the sample application folder



## MCAL 4.0: Edit sample application launch.bat 2

::TRESOS

```
SET TRESOS_DIR=C:\EB\tresos14_2_1
```

::GHS (recommend comp\_201416 since MCAL and OS ver 4.0 were tested with it) SET GHS\_DIR=C:\ghs\comp\_201714

::DIAB

```
::SET DIAB_DIR=C:/Tools/WindRiver/diab/5.9.4.8
```

::Path to the plugins folder

SET

PLUGINS\_DIR=C:\NXP\AUTOSAR\MPC574XG\_MCAL4\_0\_RTM\_1\_0\_4\eclipse\plugins SET MAKE\_DIR=C:\gnuwin32

::SSC is required when OS is used

::SSC can still be defined if OS is not installed or not defined at all

SET SSC\_ROOT=C:\NXP\AUTOSAR\MPC574xG\_AUTOSAR\_OS\_4\_0\_92\_RTM\_1\_0\_3



## Example launch.bat changes for NXP S32 Design Studio (gcc like)

- Change MAKE\_DIR path:
  - SET MAKE\_DIR=c:/NXP/S32DS\_ARM\_v2018.R1/utils/msys32/usr
- Change compiler path:
  - SET LINARO\_DIR=C:\NXP\S32DS\_ARM\_v2018.R1\Cross\_Tools\gcc-6.3-arm32-eabi
- Invoke launch.bat with a non-default tool chain. Example: using TOOLchain=linaro. Either:
  - Enter from DOS command prompt launch.bat TOOLCHAIN=linaro
  - Or create new file with a name like launch\_linaro.bat which includes one line: launch.bat TOOLCHAIN=linaro



## MCAL 4.0: Edit sample application launch.bat 3

• **TIP:** Add a **pause** command at the very end of the launch.bat file:

ECHO. pause GOTO end :end

bause

- Now when you can use launch.bat by double clicking on it from a Windows browser. The batch file executes but after completion it does not automatically immediately exit back to windows. The pause command allows you to see if the make file worked or had errors.
- Note: GHS is the default compiler. If you use a different compiler you must invoke launch.bat a compiler option. Example for WindRiver compiler: launch.bat TOOLCHAIN=diab





# 6b. Sample Application Installation& Configuration MPC574xG MCAL4.3



## MCAL 4.3: Separate Sample Application Installation

 $\sim$ 

 $\sim$ 

- MCAL 4.3 downloaded software has a separate sample application installation, repeat prior installation steps for it
  - Example of separate installation:

- MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0\_ReleaseNotes
   MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0\_Sample\_App\_ReleaseNotes
   MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0
   MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0\_Sample\_App
   Date content

- Example of completed installation of MCAL and sample application.
  - Note Tresos workspaces for different chip versions



 $^{\sim}$ 



## MCAL 4.3: MPC574xG Sample Application Initial Structure





## MCAL 4.3: Edit sample application launch.bat 1

The sample application includes a batch file to build the application.

Iaunch.bat It must be modified for your paths

- Open launch.bat in the build folder
- Modify path names for your PC (see example on next slide)



## MCAL 4.3: Edit sample application launch.bat 2

Example launch.bat with modified path names and using Green Hills compiler:

::TRESOS

**SET TRESOS\_DIR=**C:\EB\tresos24\_0\_0

::GHS

**SET GHS\_DIR=**C:\ghs\comp\_201754

::DIAB

::SET DIAB\_DIR=C:/Tools/WindRiver/diab/5.9.4.8

::SET LINARO\_DIR

**SET PLUGINS\_DIR=**C:\NXP\AUTOSAR\MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0\eclipse\plugins **SET MAKE\_DIR=**C:\gnuwin32

::SSC is required when OS is used

::SSC can still be defined if OS is not installed or not defined at all

SET SSC\_ROOT=C:\NXP\AUTOSAR\MPC574xG\_AUTOSAR\_OS\_4\_0\_92\_RTM\_1\_0\_3

**SET TRESOS\_WORKSPACE\_DIR**= C:\EB\tresos24\_0\_0\workspace\**MPC574XG\_MCAL4\_3\_RTM\_1\_0\_0\_Sample\_Ap**\output

<u>Note</u>: this folder name will change for each Tresos project



### Example changes when using NXP S32 Design Studio (gcc like)

- Modify make\linaro\build\_cfg.mak file by adding the last LDOPT line (in red):
- LDOPT := -mcpu=cortex-m4 \ -msoft-float \ -mthumb \ -e \_start \ -nostartfiles -static -lc -lm -lgcc -lnosys \ -nostartfiles **-static -lc -lm -lgcc -lnosys** \ -sysroot=\$(LINARO\_DIR)/arm-none-eabi/newlib
- Modify launch.bat:
  - Change MAKE\_DIR path: SET MAKE\_DIR=c:/NXP/S32DS\_ARM\_v2018.R1/utils/msys32/usr
  - Change compiler path: SET LINARO\_DIR=C:\NXP\S32DS\_ARM\_v2018.R1\Cross\_Tools\gcc-6.3-arm32-eabi
- Invoke launch.bat using MODE=USER TOOLchain=linaro. Either:
  - Enter from DOS command prompt launch.bat MODE=USER TOOLCHAIN=linaro
  - Or create new file with a name like launch\_linaro.bat which includes one line: launch.bat MODE=USER TOOLCHAIN=linaro



## MCAL 4.3: Edit sample application launch.bat 3

TIP: Add a pause command at the very end of the launch.bat file:

- Now when you can use launch.bat by double clicking on it from a Windows browser. The batch file executes but after completion it does not automatically immediately exit back to windows. The pause command allows you to see if the make file worked or had errors.
- Note: GHS is the default compiler. If you use a different compiler you must invoke launch.bat a compiler option. Example for WindRiver compiler: launch.bat TOOLCHAIN=diab



:end

bause









## Compiler for MCAL, OS

- NXP's MCAL and OS products are tested using multiple compilers.
  - See the release notes for versions.
- Examples in this presentation are based on MPC574xG MCAL & OS version 4.0.3 using Green Hills Software compiler version 201416.
  - -Later versions have not been known to be a problem.
- Obtain and install the compiler if you have not done so.


## OS installation only: Java

- If using Autosar OS, a Java version 1.6 or higher is required for the Autosar OS system generator
  - Usually it is already installed in windows
- To determine if Java is installed and the version, go to the DOS command prompt and enter: java –version
  - Example:

Administrator: Command Prompt	- • ×
	<b>^</b>
C:\Users\r17052>java -version	
java version "1.6.0_29"	
Java(TM) SE Runtime Environment (build 1.6.0_29-b11)	
Java Hotspot(IM) 64-Bit Server VM (build 20.4-b02, mixed mode)	
C:\Users\r17052>	
	· ·

- If you do not have version 1.6 or higher, it can be obtained at <a href="https://java.com/en/download/">https://java.com/en/download/</a>
  - Caution: sometimes installing a new Java version can affect other programs! Suggest not installing it if AUTOSAR OS is not needed.





## SECURE CONNECTIONS FOR A SMARTER WORLD

www.nxp.com

NXP, the NXP logo, and NXP secure connections for a smarter world are trademarks of NXP B.V. All other product or service names are the property of their respective owners. © 2018 NXP B.V.