Electrification – Functional Safety Backbone Solutions to Attach with S32

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Segment Manager Functional Safety SBC Safety Power Management Product Line

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Agenda

- Multi-PMIC Solutions
- Powertrain & Vehicle Dynamics: Solutions
- Safety Proven
- Portfolio Scalability
- Electrification Robustness
- System Solutions

Next Generation FS-SBCs



Global Megatrends NXP to Lead This Industry Transformation

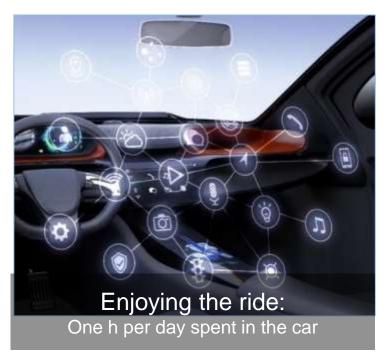
Autonomy

Electrification

Saving lives: 90% of accidents caused by human error



Connectivity





Domain-Based Architecture

Secure Gateways & Networks

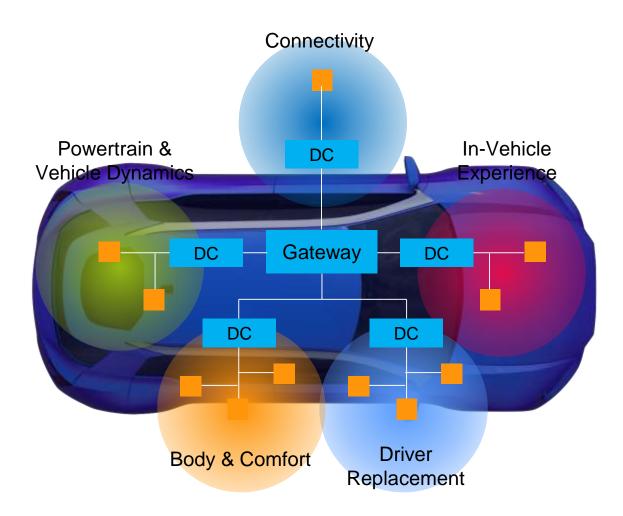
Connectivity

Driver Replacement

Powertrain & Vehicle Dynamics

Body & Comfort

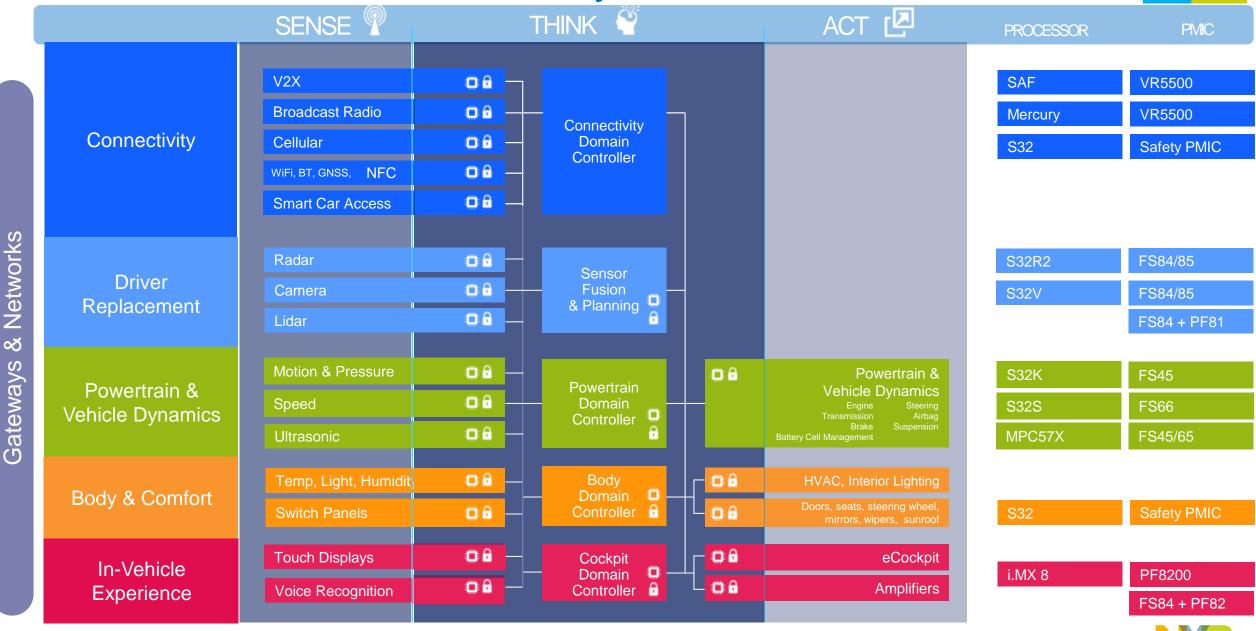
In-Vehicle Experience





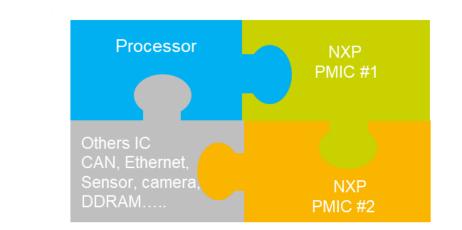
One Processor = One Safety PMIC

NXP NXP Ocessor PMIC



BENEFITS: PMIC DESIGNED & VALIDATED WITH PROCESSOR COMMON REFERENCE DESIGN SOFTWARE

Multi-PMIC Solutions for System Power and Safety

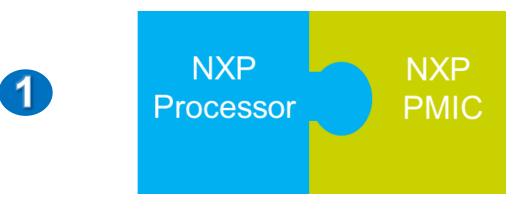


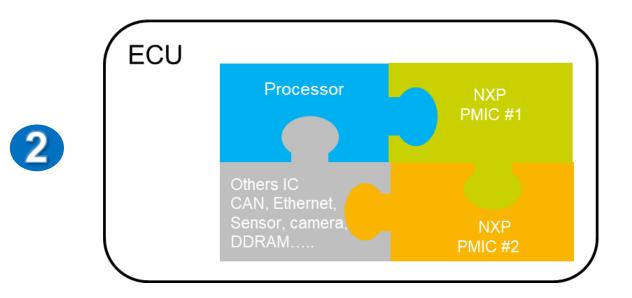
NXP's next generation power management: Simplicity of safety power management IC family





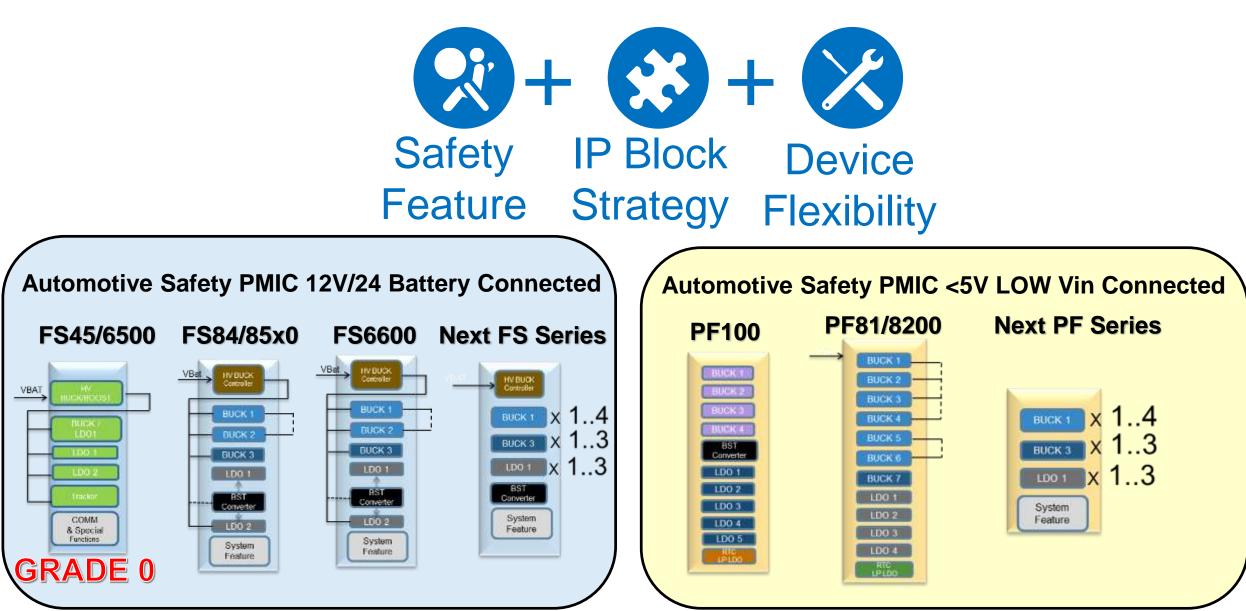
Automotive Safety Power Management Solutions Strategy



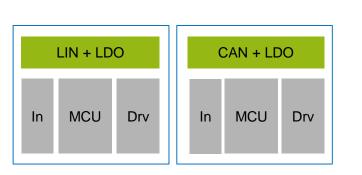




NXP Safety PMIC's DNA



System Basis Chip Integrates Power and System Level Features



Mini-SBC

PERIPHERAL MODULES DOOR, SEAT, CLIMATE CONTROL, SUNROOF,...



MCU

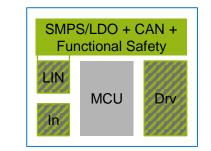
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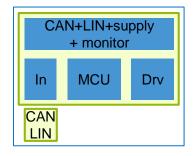
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GATEWAYS & BCM

Safety SBC



ELECTRIFICATION ADAS BMS, DCDC, RADAR, CAMERA, INVERTER, TCU SENSORS FUSION Application Specific SBC



MECATRONICS BRAKING

Simplifying the System

Improving Space, Quality & Overall Cost Leverage NXP in-vehicle networking expertise Safe & Scalable SBC Solutions

System Power Solution

System Differentation with High Performance MCUs Attach Strategy and Functional Safety





Powertrain & Vehicle Dynamics

PSENSE

Powertrain & Vehicle Dynamics	\$	Motion & Pressure	
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Powertrain Domain Controller

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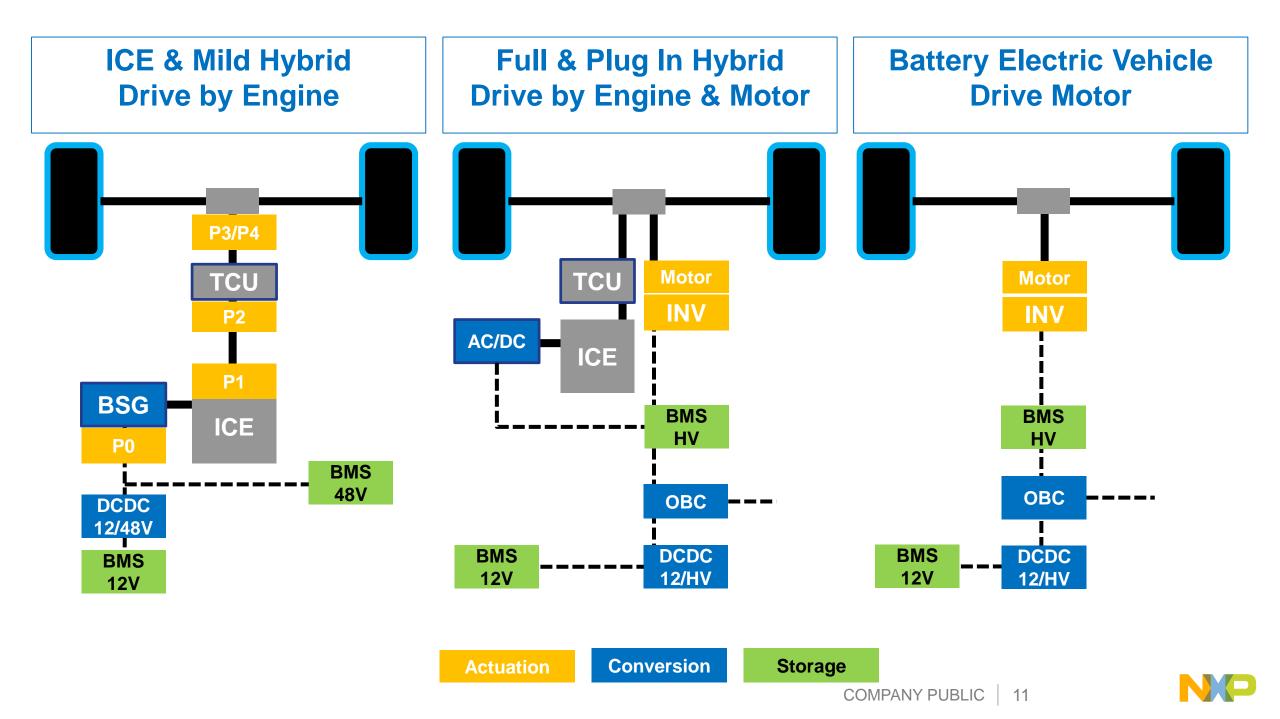
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Powertrain & Vehicle Dynamics

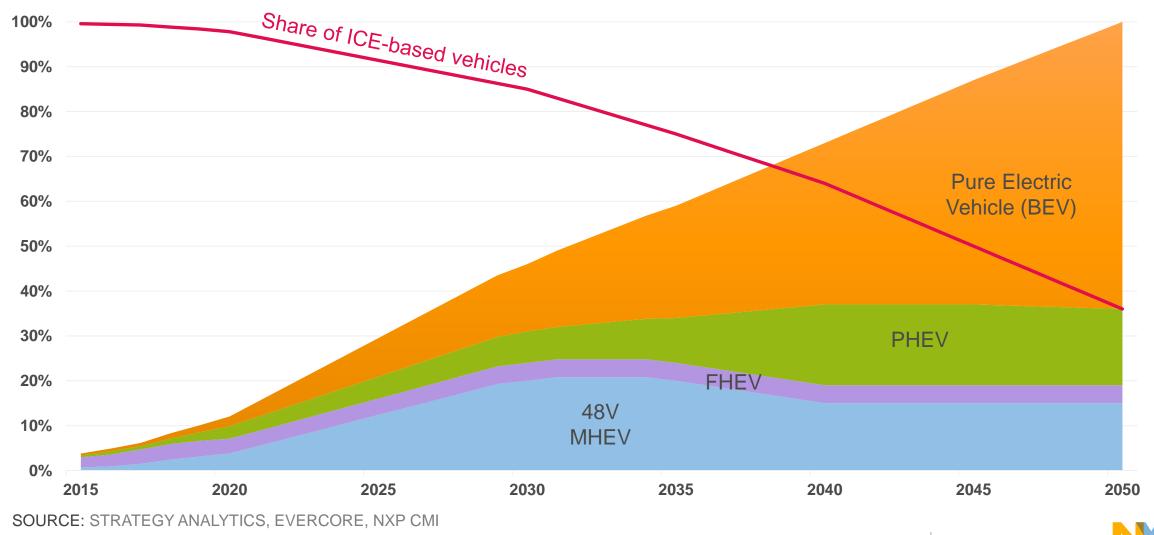
Engine Steering Transmission Airbag Brake Suspension Battery Cell Management

Vehicle Electrification: Diversity Of Approaches

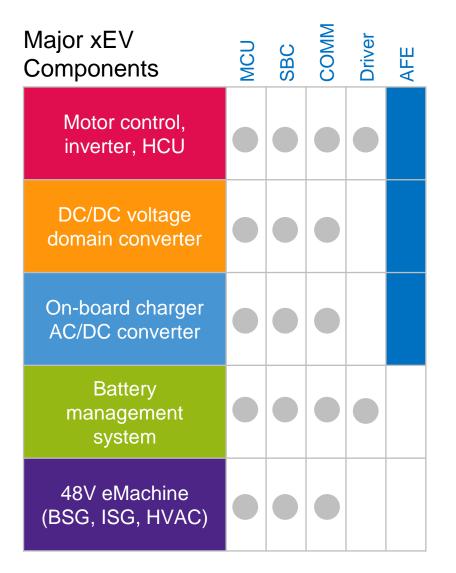
Common Name	Combustion Engine (ICE)	Mild Hybrid (M-HEV)	Full Hybrid (F-HEV)	Plug-in Hybrid (P-HEV)	Range Extended EV (RE-BEV)	Pure Electric Vehicle (BEV)
Example	Ford Mustang	Honda Insight	Toyota Prius	FCA Pacifica	BMW i.3	Nissan Leaf
Combustion Engine	$\bullet \bullet \bullet \bullet$			$\bullet \bullet \bullet$	• •	
Battery System	 [[⁺ 12V] [⁺ 48V]	 [+] [+] LV [HV]			
Mains Charging	-	-	-			
Electric Traction	-	[] 10 – 20 kW	15 – 60 kW	40 – 80 kW	40 – 80 kW	> 80 kW

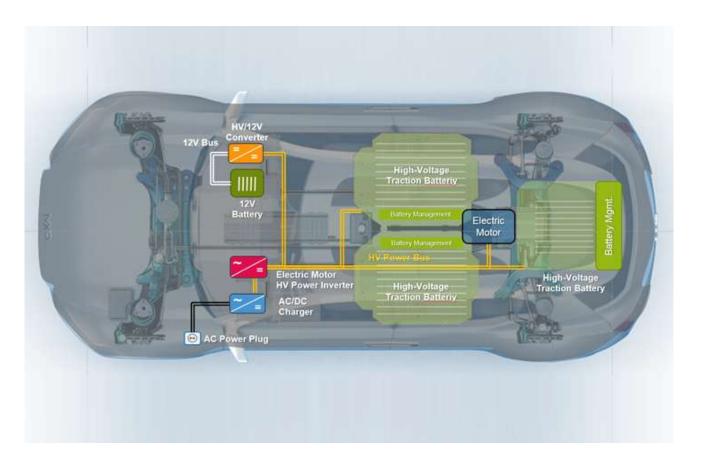


xEV Long Term Market Forecast



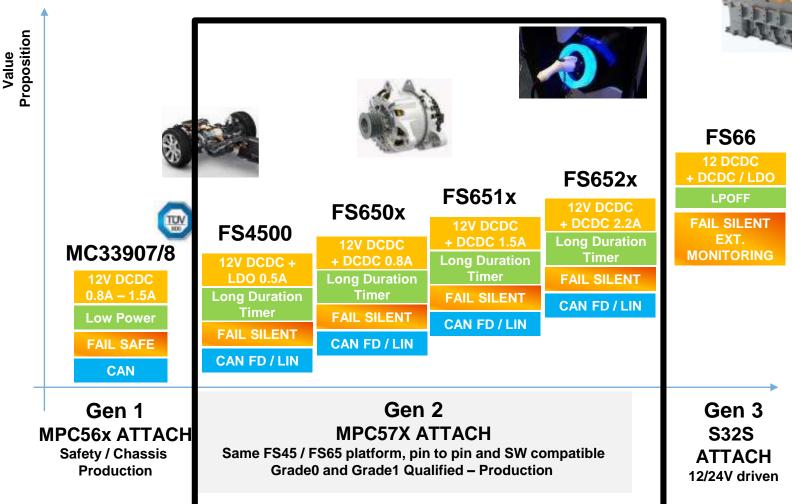
Efficient Powertrain and Energy Management







Energy Storage – Conversion – Actuation Electrification & General Purpose Safety Power Management





Secured & Safe System Solutions

- ISO26262 architecture (TUV SUD proven)
- Functional robustness (non ISO pulse, EMC, HTOL)
- Security (SM transition and Power Gate)

High Efficient Solutions

- Target 12 V, 24 V, 48V (application note)
- DCDC & LDO architecture (Vpre + Vcore)
- Ultra low power modes (low Iq, long dur. timer)

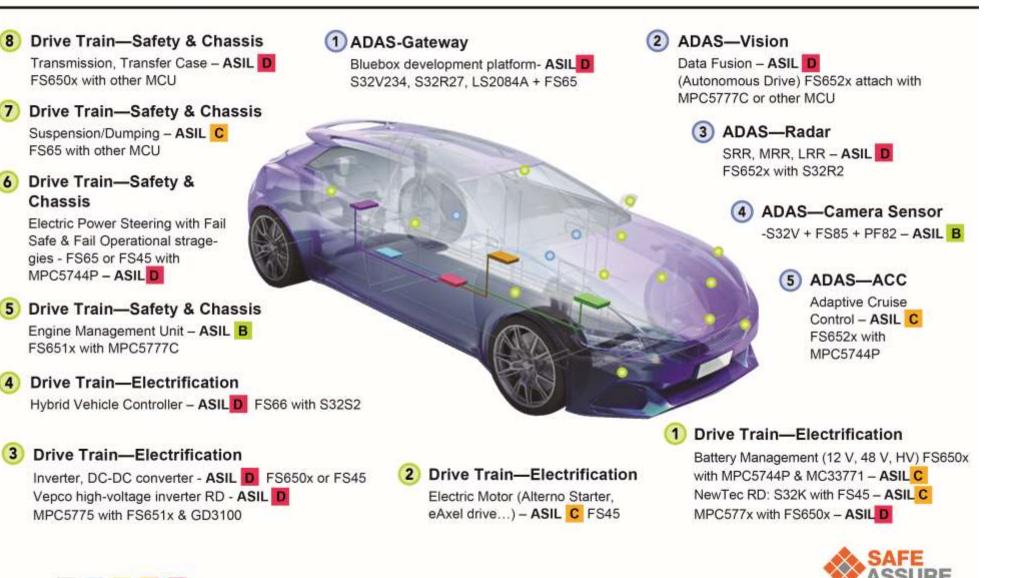
Safety Simplified Solutions

- ISO26262 ready documentation
- System validation test (eFAST)
- Global ecosystem (incl HW & SW)



NXP® POWER SBC APPLICATION EXAMPLES

ASIL



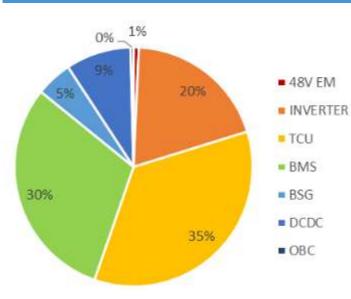
Electrification FSBC NXP Designs: Where & Why?

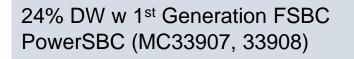
> 60 customers, 16 Applications

55% DW in Energy Actuation in Inverter, TCU

30% DW in Energy Storage in BMS

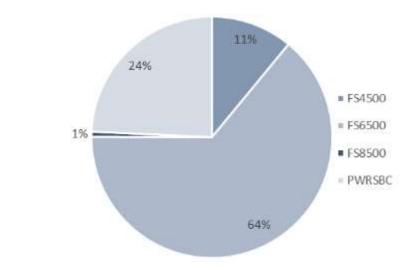
14% in Energy Conversion in DCDC and BSG





75% DW w 2^{nd} Generation FSBC FS45 and FS65

First DW with FS85 and FS66 on 24V BMS and Inverter



FSBC Driving factors

1. SAFETY PROVEN

- MCU agnostic
- Self independent Monitoring
- Fail Silent (3 generations)

2. FAMILY SCALABILITY

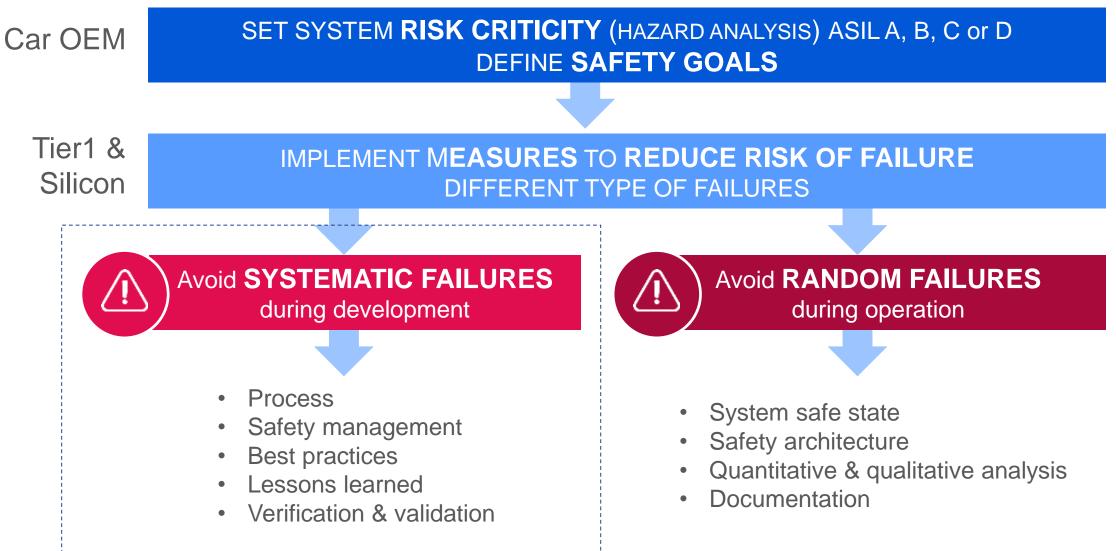
- Pay what is needed
- Pin to pin & SW scalable
- Power & System Segmentation

3. SYSTEM BASE SOLUTIONS

- Low Power Strategy (LDT)
- Trackers & 1% Accuracy
- Grade 0 capability (Robustness)

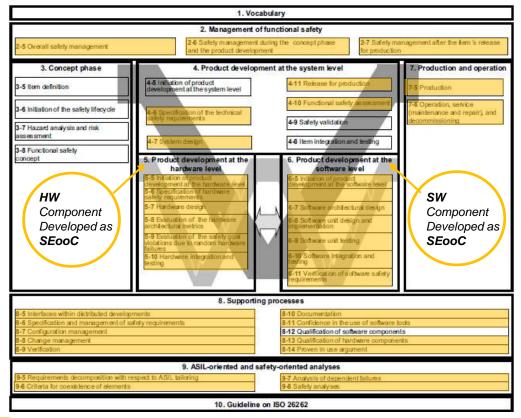


Reduce Risk: Track and Understand Failures





Functional Safety - Vmodel & Responsibility



Applicable to Component developed as SEooC

Reference ISO 26262-10:2012

NXP focuses on Safety Element Out Of Context (SeooC)

OEM

- Item definition
- Hazard analysis and risk assessment
- Safety Goals
- Functional Safety Concept

Safety Manual ⁴ Safety Analysis

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Tier 1

- Technical Safety Concept
- Technical Safety Architecture
- ASIL Classification of Functions
- HW / SW design



Safety Requirements DIA

Tier 2 Supplier - NXP

• HW / SW "products"

Product Safety Mechanisms (implemented in offering, described in Safety Manual, quantified/qualified by Safety Analysis)

Development Process & Methods



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Y

NXP Auto BCaM7 Process Fully Compliant with ISO 26262: 2011

IPI PROJECT LI	FECYCLE			
- QM or ASILx			 Safety analysis (FTA, DFA, FMEDA) Confirmation review Safety Analysis Functional Safety assessment 2 	 Safety assessment 3 Confirmation review Safety case
	- Impact Analysis	- Safety concept - Safety reqs. - Safety archi.	 Safety plan DIA (if needed) Safety Assessment plan SW tool criteria Eval. Verification Plan (inc. safety) Confirmation review Safety Plan Confirmation review SW tools Functional Safety assessment 1 	CERTIFICATE No. door in the constant and the constant of the c
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Automative Defentions

Automative Translation Table

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Automative BCaW 7.8 Expert Training

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NXP ISO 26262 INNOVATIONS

Lessons Learned and Continuous Improvement

Product Requirement Mgt

- OEM workshops
- Lessons learned
- System Safety Goal Translation
- Fail Operational analysis and solutions

Product Definition

- Doors (SoC & IP)
- Standardized Fail Safe State Machine
- Common chassis safety platform
- Safety Behavioral Model

Product Architectures



- Fail Safe (qualified, certified)
- Fail Silent (qualified, certified)
- Fault Tolerant Systems

- C

Customer Support

- Documentation (FMEDA, SM)
- Reference Design & AN
- SW Production ready
- System Solutions (RADAR, ...)

System Validation

- eFAST : OEM Non ISO PulseHW Fault Injection Test
- FS65 Safety Behavioral Model
- FIT verification through Model

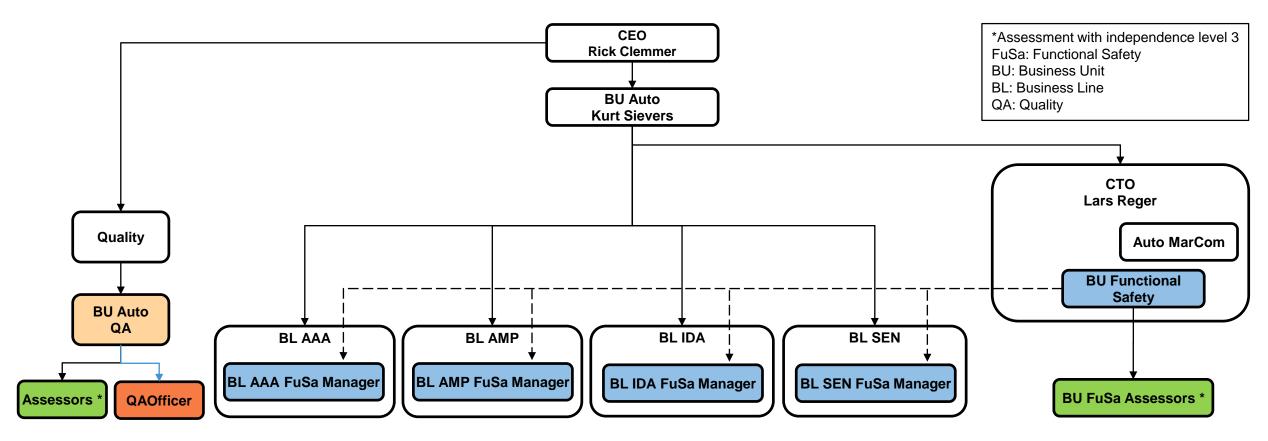
Verification

- Automated Traceability Matrix (NPI360)
- Virtual Test
- Fault Injection Simulation
 - available
 - planned





Automotive Functional Safety – Organization Setup





SafeAssure Community Customer support for Functional Safety





SafeAssure Community

Public Space for knowledge distribution and industry-wide news <u>here</u>

SafeAssure NDA

Private NDA space for customer to access safety documentation here

Support

Safety Expert Group composed of Safety Managers and Architects, Field and Application Engineers

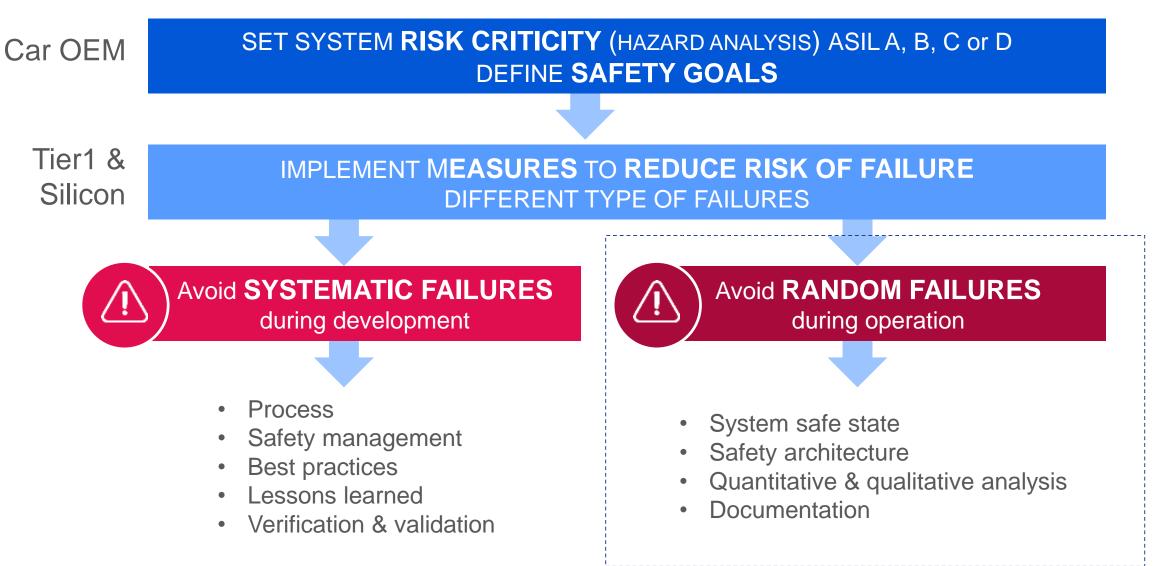


Self Sufficient

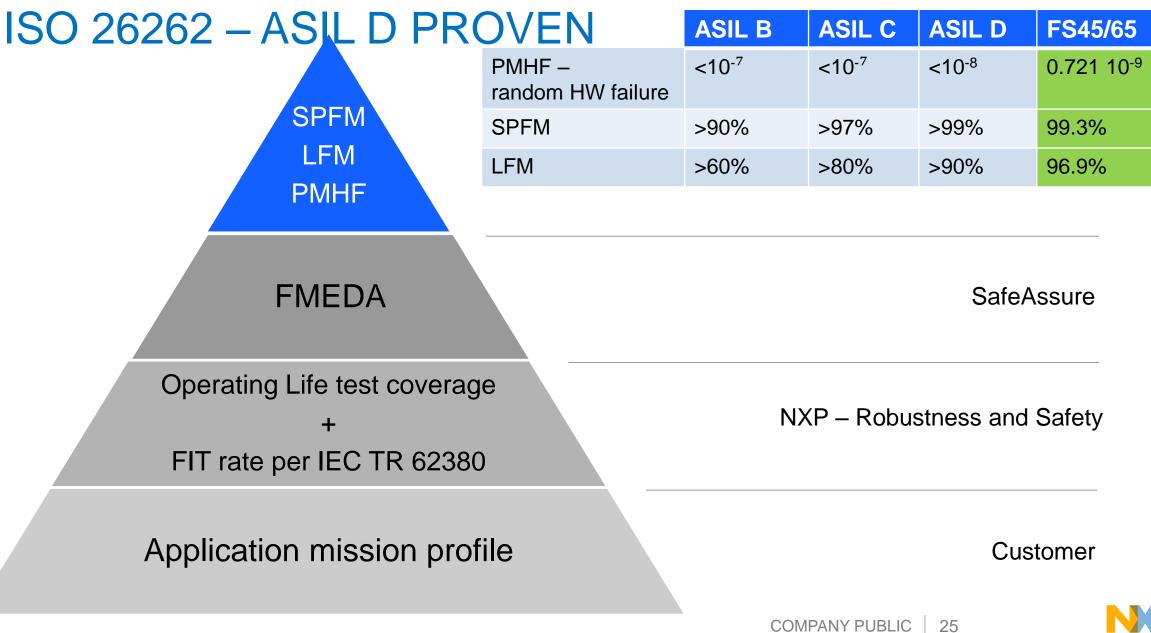
Community users find answers to their questions an safety documentation requests



Reduce Risk: Track & Understand Failures



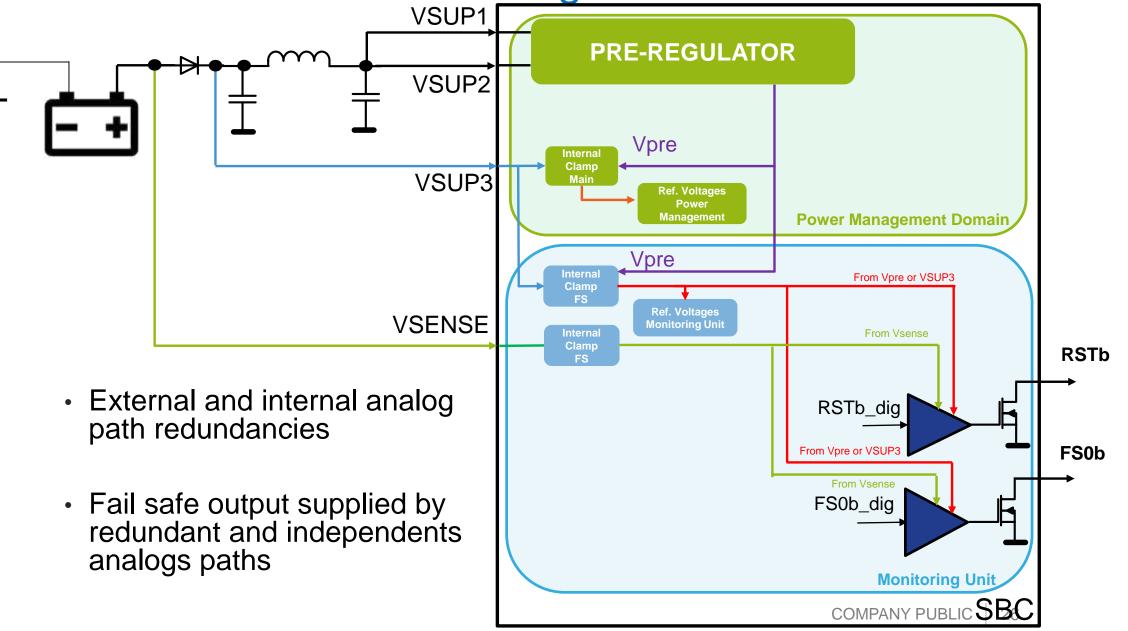






Power Management Domain Monitoring Unit

External and Internal Analog Paths



MCU + FSBC Fit for ASIL D Backbone



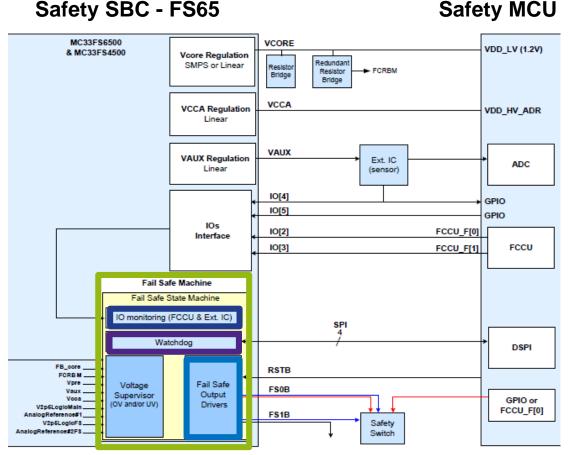
HW & SW added values for fail silent requirements

Independent Fail Safe State Machine

- Physical & Electrical independance to fit for ASILD
- Power Management Monitoring Unit (UV / OV)
- Analog & Digital Built In Self Test to minimize Latent Faults
- Own Reference & Supply to Reduce Common Cause Failure

Advanced Watchdog

- Challenger
- Replace external MCU Monitoring



All safety mechanism reaction are < FTTI < 10ms FTTI = Faul Tolerant Time Interval

HW Redundancy

Vcore external Monitoring

MCU Monitoring

- FCCU : Fault Collection Control Unit
- Monitor Dual Core Lock Step Modes MCUs

RSTb – Fail Silent Mode

Configurable RSTb activation giving more system availability

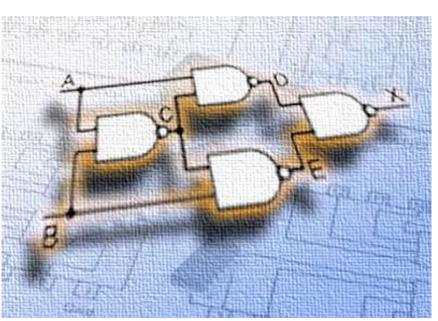
Fail Safe Pin (FS0b) :

- Redundant System Fail Safe enabler
- Second Fail Safe pin to assert safety path with configurable delay after failure



Digital Architecture Of Monitoring Unit

Several mechanisms are implemented to avoid a bad interpretation of a bit flip (SEU):



- Triplication of safety registers with majority voter (e.g. WD period duration)
- Design structure to avoid erratic activation of safety outputs (e.g. FS0b)
- ECC for fuse
- Detection and correction of 1-bit flip (SPI registers of Monitoring Unit)
- Detection of 2-bits flips (SPI registers of Monitoring Unit)
- LBIST to reduce Latent Fault

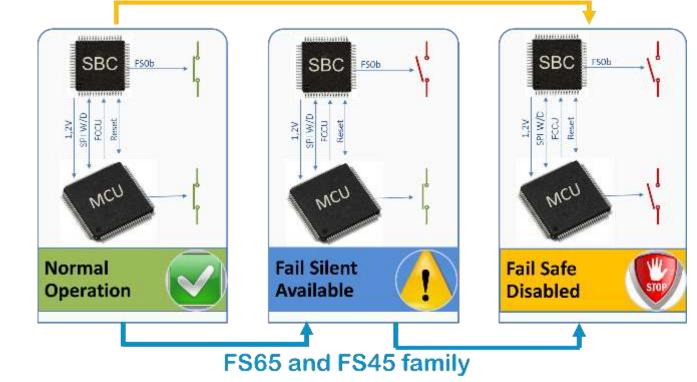


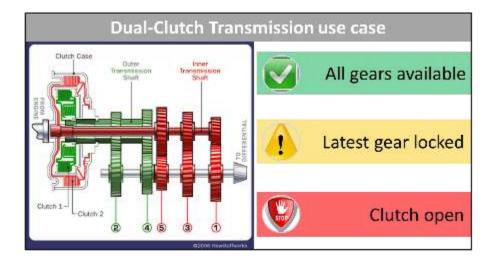


Dependability: Safety vs Availability

Qualitative Analysis – Enabling Fail Silent Operations

MC33907 & MC33908





- 1. CONFIGURABLE RESET at SAFE STATE activation to enable MCU Diagnostic.
- CONFIGURABLE SAFE STATE, independently for each failure with 2nd Fail Safe output.
- 3. HIGH AVAILABILITY : No MCU shutdown in case of multiple failures.
- 4. SMART DEGRADED MODE : Application is failing with safe and available operation



Safety SBC Documentation

SafeAssure – Safety Manual

- Integration of the SBC in a functional safety relevant application
- HW and SW guidelines for the system integrator
- Assumptions of use

SafeAssure – Dynamic FMEDA

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- ASIL metrics (SPF, LF, PMHF)
- FIT RATES
- System Assumptions
- Safety functions and safety mechanisms

SafeAssure – Fault Injection Test report

- Linked to FMEDA
- Validation and verification of the good implementation of all the Safety Mechanisms
- FTTI verification

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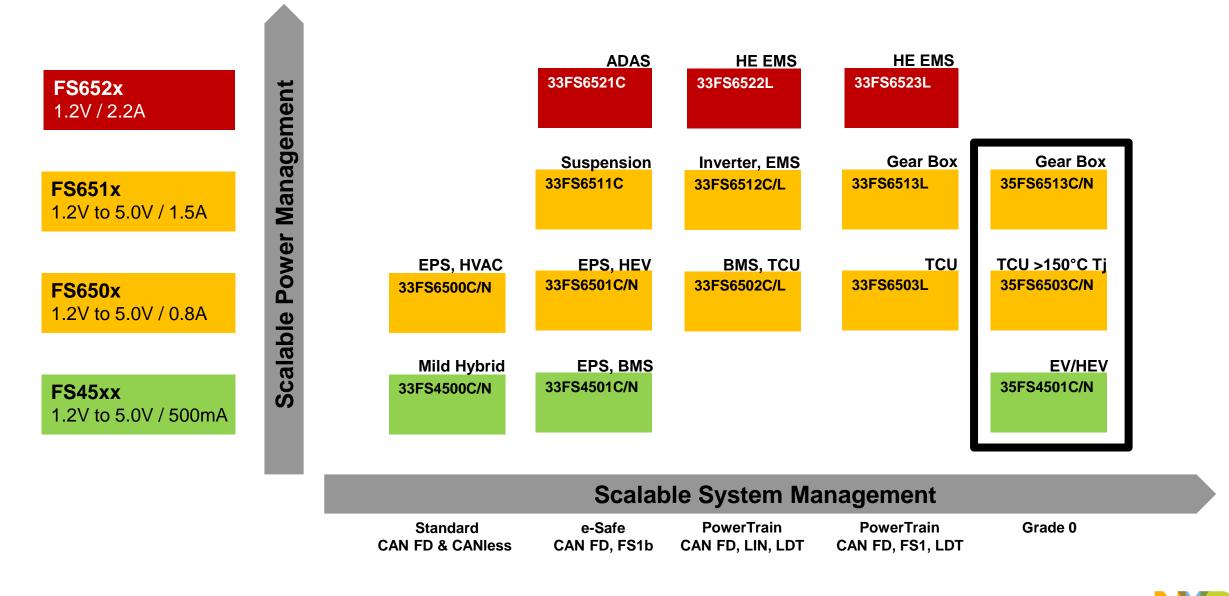








FS65/45 Family – SCALABLE Functional Safety SBC Solutions



FS65x: ASIL D Safety SBC

MPC57xx Attach Solution used in ASIL D systems (TCU, Inverter, EPS)

Power Management Solution

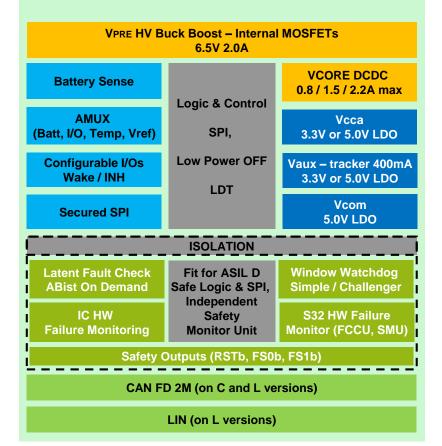
- Input supply up to 40V DC
- HV BUCK/BOOST, 6.5V, 2.0A DC, 440kHz Asynch. With integrated MOSFETs
- VCORE : DCDC with adjustable Vcore 1.0V to 5.0V
 - Part Number selection for Max Current : 0.8A (FS650x) or 1.5A (FS651x) or 2.2A (FS652x)
- VCCA LDO 3.3V or 5V, 100mA / 1% or 300mA 3%
- VAUX TRACKER 3.3V or 5V, 400mA, with 10mV offset
- VCOM LDO 5V, 100mA
- LOW POWER OFF MODE, with Wake up by I/Os, CAN/LIN, and 30µA Low Quiescent current

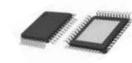
Safety & System Features

- Fail Silent Configurable State Machine with Independent Safety Monitoring Unit
- Control via 32 bits SPI (including CRC)
- Low Power OFF Mode with Low Quiescent current : 30µA
 - Wake up via dedicated pins, CAN, LIN
 - Long Duration Timer (LDT) feature counter from few secs up to 6 months
- AMUX: Battery, Internal Safety critical voltages, Precise Vref and Temperature
- CAN FD 2M PHY and LIN
- Several Part Numbers pin to pin compatible with optional FS1b, LDT, CAN, LIN



FS65xx Functional Block Diagram





Samples : Now PPAP : Now Package : LQFP48eP

COMPANY PUBLIC

FS450x: ASIL D Safety SBC

S32Kx Attach Solution used in ASIL C/D systems (BMS with Newtec)



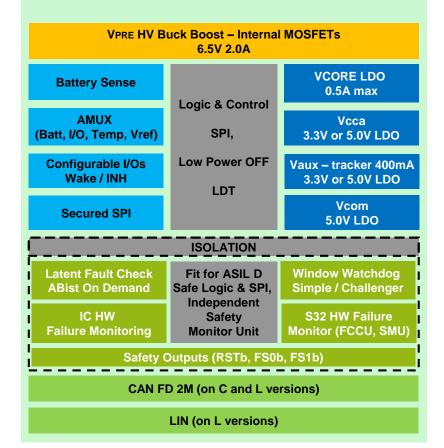
Power Management Solution

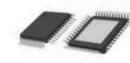
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- VCORE : LDO adjustable 1.0V to 5.0V, 0.5A max
- VCCA LDO 3.3V or 5V, 100mA / 1% or 300mA 3%
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- Low Power OFF Mode with Low Quiescent current : 30µA
 - Wake up via dedicated pins, CAN, LIN
 - Long Duration Timer (LDT) feature counter from few secs up to 6 months
- AMUX: Battery, Internal Safety critical voltages, Precise Vref and Temperature
- CAN FD 2M PHY and LIN
- Several Part Numbers pin to pin compatible with optional FS1b, LDT, CAN, LIN

FS450x Functional Block Diagram





Samples : Now PPAP : Now Package : LQFP48eP

COMPANY PUBLIC 34

Attaching FS45/65 to NXP Safety MCU FROM AN5238

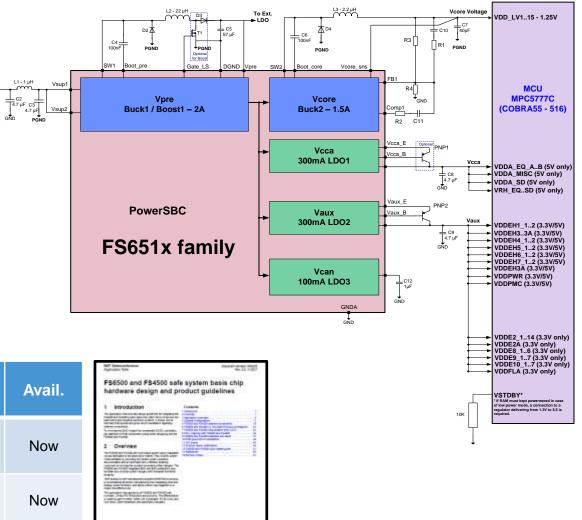
FS45 & FS65 Power Management Values

- ✓ **Scalability** : Pin Compatible Solutions
- ✓ Power Flexibility
 - ✓ FS450x : 0.5A Vcore (LDO)
 - ✓ FS650X : 0.8A Vcore (DCDC)
 - ✓ FS651X : 1.5A Vcore (DCDC)
 - ✓ FS652X : 2.2A Vcore (DCDC)

✓ Availability :

- ✓ Configurable Buck / Buck Boost Vpre
- ✓ Min Vsupply : 2.7V LV124 compliant
- ✓ Max Vsupply : 36V

Auto MCU Part Number	Applications	ASIL level	Safety SBC Attach	Reference Design	Avail.	F96500 and F94500 safe system basis chip hardware design and product guidelines
MPC5744P	HEV, Motor ctrl EPS	D	FS650X	SAFETYKIT MICROSYS	Now	And the strateging of the stra
MPC5775E	Inverter	C/D	FS651X	ASIL D INVERTER On going	Now	 Hereit Hereit
S32K1	Safety General Purpose	B to C	FS450x	BMS ASIL C NEWTEC	Now	AN5238 N





Attaching FS45/65 to AURIX Safety MCU FROM AN5238

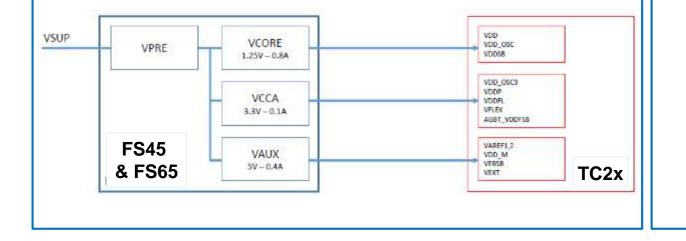


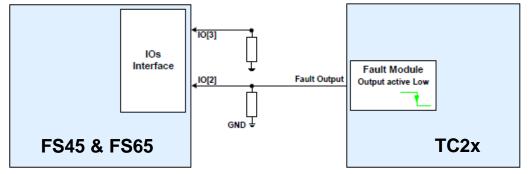
Power Management

- FS6500 can power Aurix MCU. Below power tree proposal described in AN5238 is not the only one, and can be adjusted depending on application need.
- Proven solution in production at multiple Tier 1 & Disty.

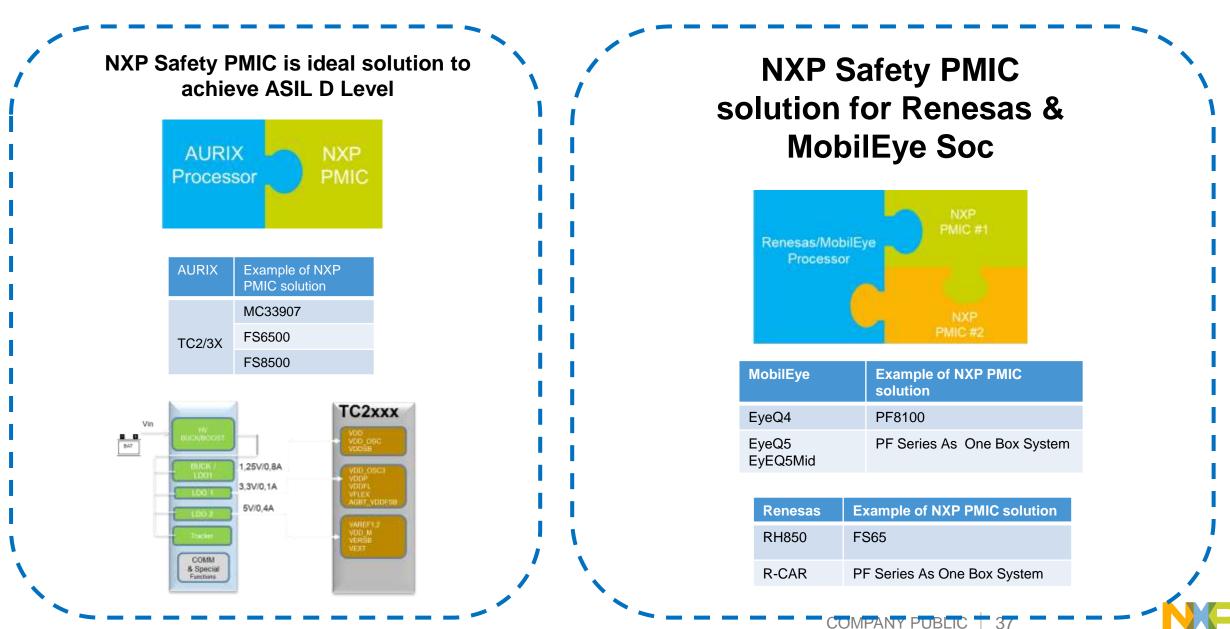
Safety Monitoring

 FS6500 can monitor AURIX Safety Monitoring Unit (SMU) output thru IO_2:3 safety input pins with below connections described in the Safety Manual.



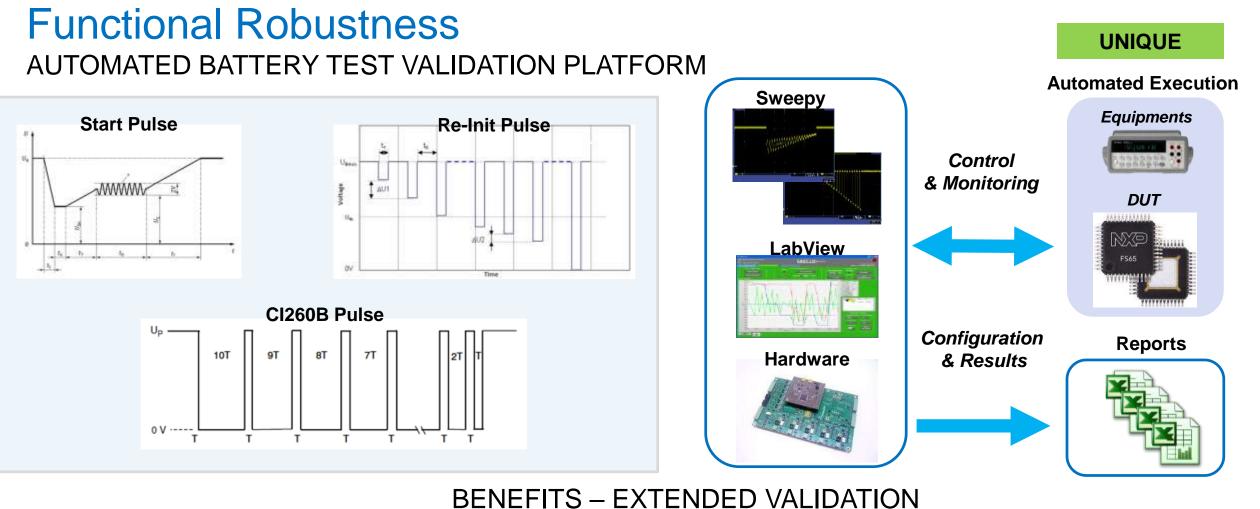


NXP Safety PMIC Solution Attached to NON NXP Processor



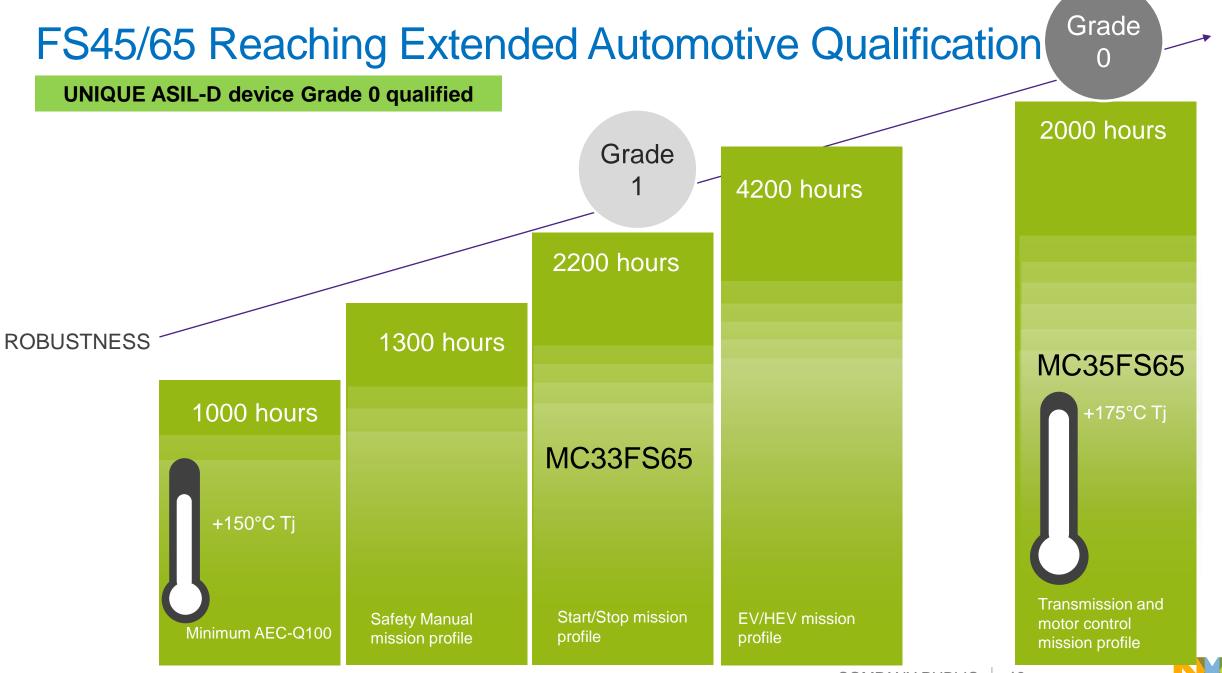






Car OEMs PULSES - Database INCREASE USE CASE Coverage - Break the limit. (x10) ACCELERATE VALIDATION – Reduce validation by 4 ISO26262 COMPLIANT - Full Traceability MULTIPURPOSE - Non-ISO, Funct. Validation









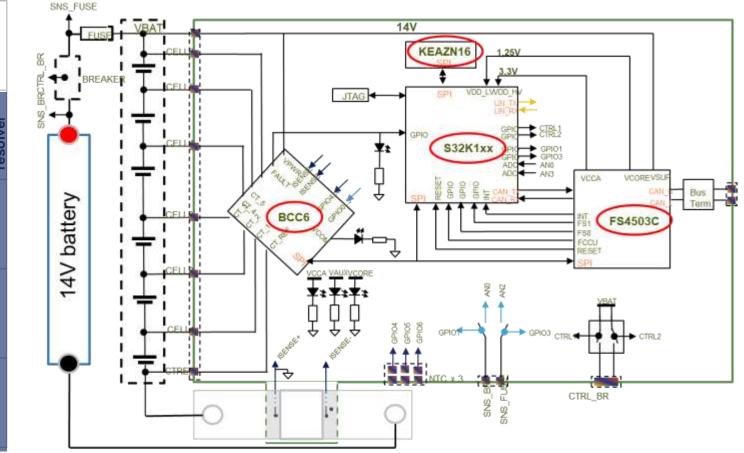
Mastering xEV Power: ePowertrain Solutions



System-Entwicklung und Beratung

Major xEV Components	MCU	SBC	COMM	Driver	AFE
Battery management system	S32K	FS45	TJA104x	eSwitch	BCC6x
Motor control, inverter, HCU	5775E	FS65	TJA104x	GD3100	Software resolver
48V eMachine (BSG, ISG, HVAC)	S32Kx	FS45 UJA116x	TJA14xx		
DC/DC voltage domain converter	S32K+		TJA104x		
On-board charger AC/DC converter	S32K+		TJA104x		

Low Cost ASIL C Battery Management System



FS45/65 – System Solutions for Electrification

Enabling ASIL D Fail Silent Operations, Simplify Design and Reduce System cost

FIT FOR ASIL-D APPLICATIONS

- SPM>90%, LT>99%, PHMF<10-8
- Analog & Digital Safety Mechanisms to fit for ASILD
- All safety mechanism reaction are < FTTI < 10ms

LONG DURATION TIMER

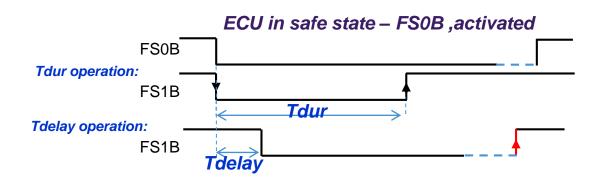
- Internal SBC Counter from few sec to 6 months
- Active in Run and Low Power Operations
- Measure time during Parking Mode (Low Power)
- Measure time during BMS Operation (Run)
- Optional Cyclic Wake Up (every week)

FAIL SILENT MODE



- OPTIONAL RESET at SAFE STATE to enable MCU Diag
- CONFIGURABLE SAFE STATE, independently for each failure
- SYSTEM AVAILABILITY : No MCU shutdown after multiple failures
- SMART DEGRADED MODE : safe and available operation

REDUNDANT FAIL SAFE PIN (FS0B & FS1B)



- T duration OPERATION : Inhibit CAN during *Tdur* duration
- T delay OPERATION : Safe delay of BMS load desactivation

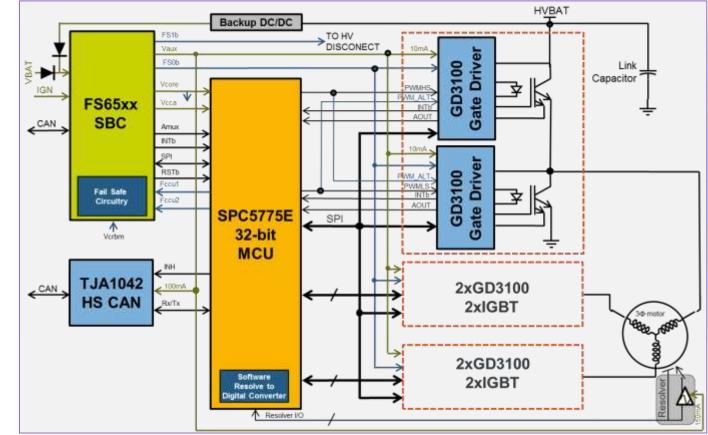


Mastering xEV Power: ePowertrain Solutions



Major xEV Components	MCU	SBC	COMM	Driver	AFE
Battery management system	5775B	FS65	TJA104x	eSwitch	BCC77x
Motor control, inverter, HCU	5775E	FS65	TJA104x	GD3100	Software resolver
48V eMachine (BSG, ISG, HVAC)	S32Kx	FS45 UJA116x	TJA14xx		
DC/DC voltage domain converter	S32K+		TJA104x		
On-board charger AC/DC converter	S32K+		TJA104x		

Traction Motor Power Inverter Solution





VEPCO PIM Support and Engineering Services

Power Inverter Platform (PIM)

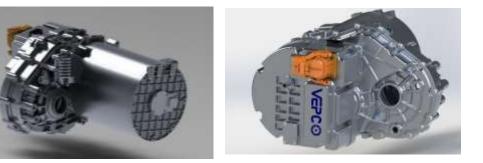
- Apply NXP hardware, software and toolchains to vehicle motor control
- Setup and training on the use of PIM
- Introduction on safety concept of the PIM

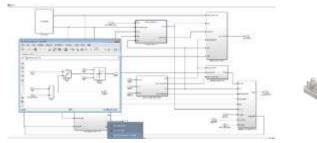
Customization and advanced support:

- Advance PIM design principle to different vehicle architecture and topology needs
- Participate customer's packaging design and integration analysis
- Premium support over design of complete eDrivetrain system
- Facilitate customer ISO 26262 **functional safety** related activities in system design, development, validation and integration cycle
- Work with agencies on design and development process certification

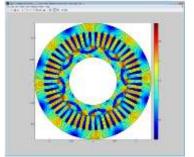
Value-added services:

- Calibration and tools integration
- Application level integration with advanced auto code generation tools
- Engineering software and **tools**
- Dyno and Hardware-in-the-loop testing





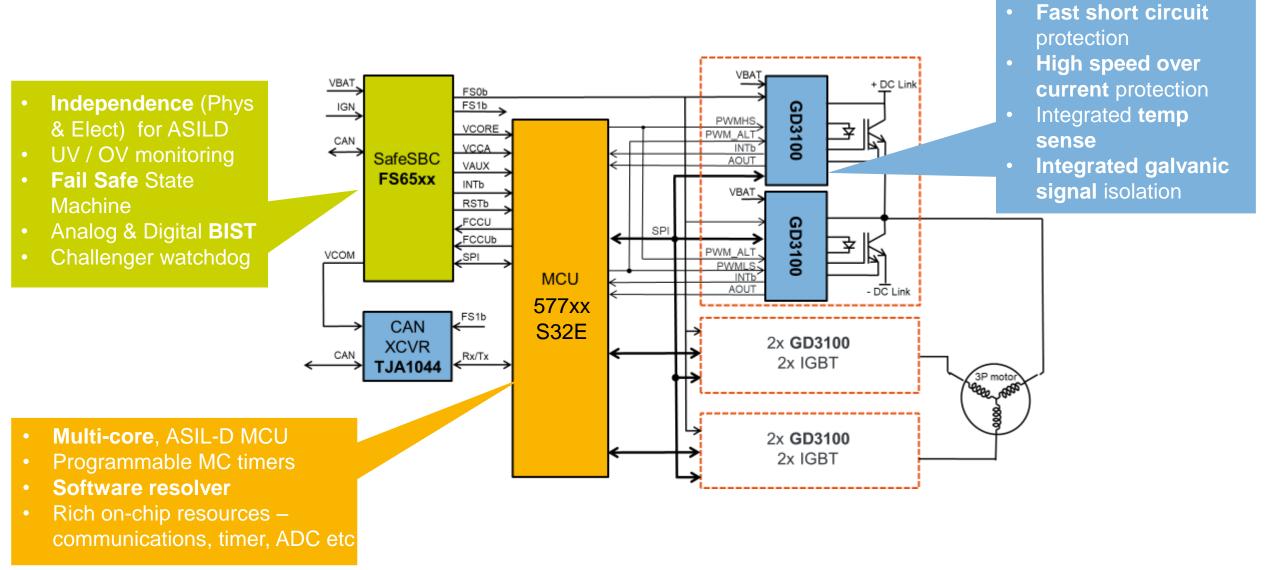








Example of Solution : Inverter/Traction Motor Control System

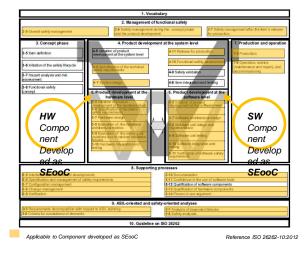


NXP Offer Standard Offer and New Offer

ASIL D components



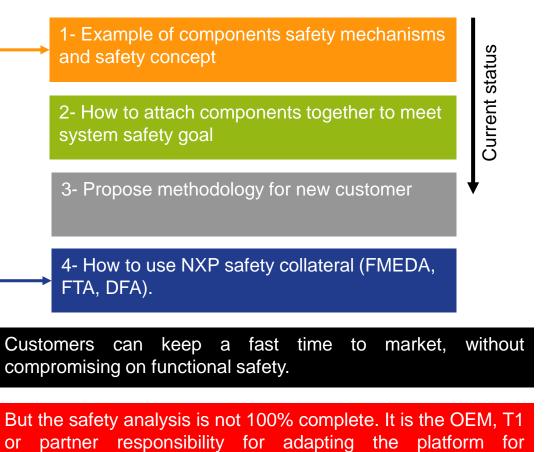
NXP ASIL-D components are developed accordingly to the ISO26262 standard SEooC



FS6500	MPC5775E	GD3100	
Customer Documents	Customer Documents	Customer Documents	
Data Sheet	-► Data Sheet	Data Sheet	
► Reference Manual	Reference Manual	Reference Manual	
·► Safety Manual	Safety Manual	► Safety Manual	1
FMEDA, FTA,	FMEDA, FTA,	FMEDA, FTA,	
DFA	DFA	DFA	

System safety enablement

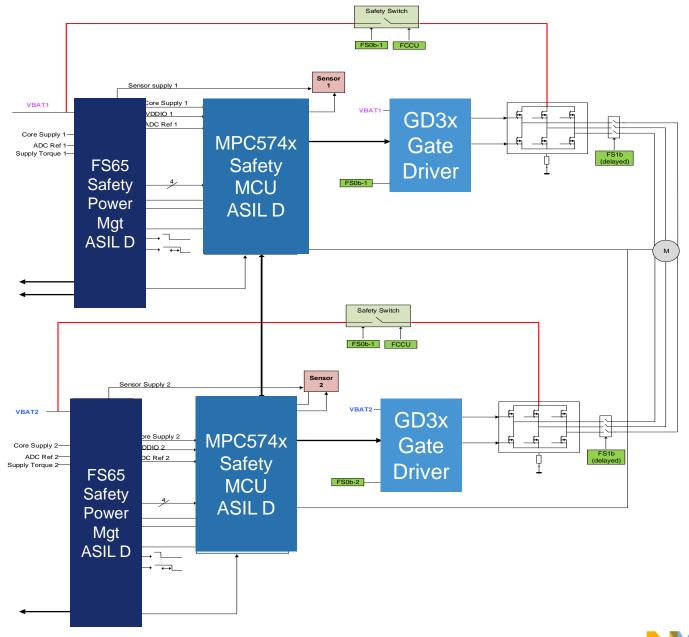
production.





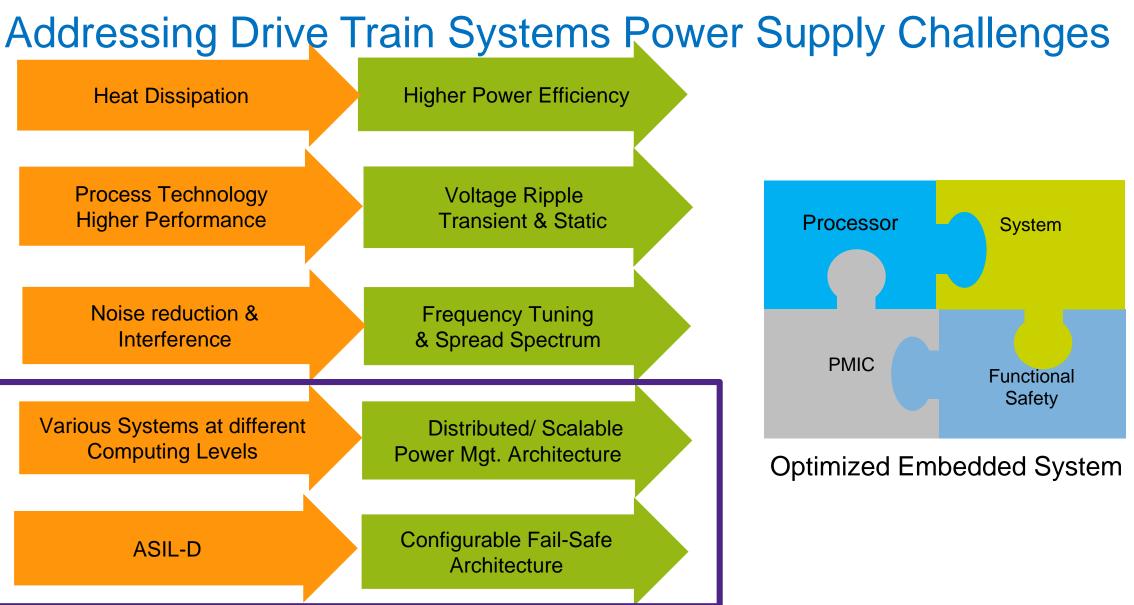
Motor Control Fail Operational Architecture

- SAE standards require new fail operational architecture
- Guarantees full/ degraded operation of a function
- Full safety analysis needed to eliminate common cause failures







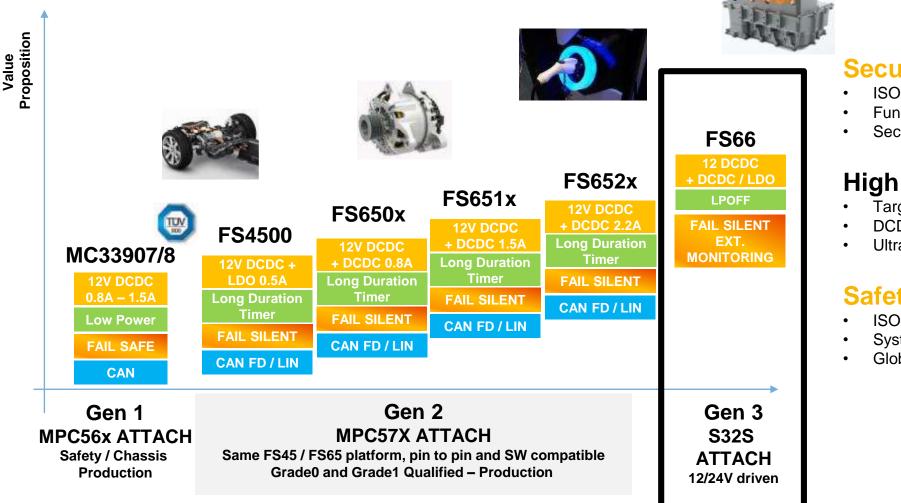




ENERGY STORAGE – CONVERSION – ACTUATION



Electrification & General Purpose Safety Power Management



Secured & Safe System Solutions

- ISO26262 architecture (TUV SUD proven)
- Functional robustness (non ISO pulse, EMC, HTOL)
- Security (SM transition and Power Gate)

High Efficient Solutions

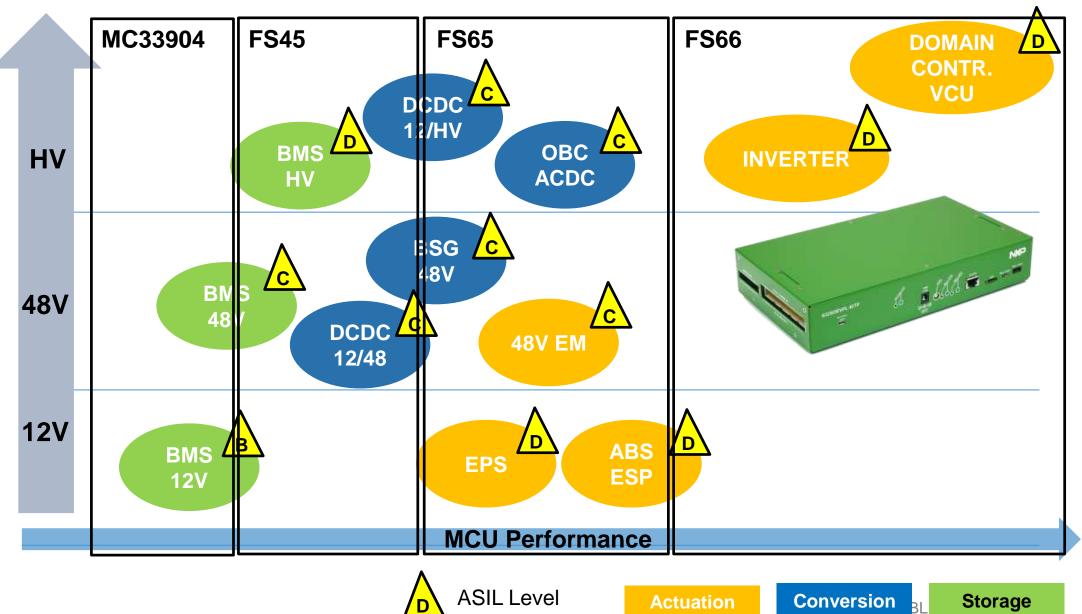
- Target 12 V, 24 V, 48V (application note)
- DCDC & LDO architecture (Vpre + Vcore)
- Ultra low power modes (low Iq, long dur. timer)

Safety Simplified Solutions

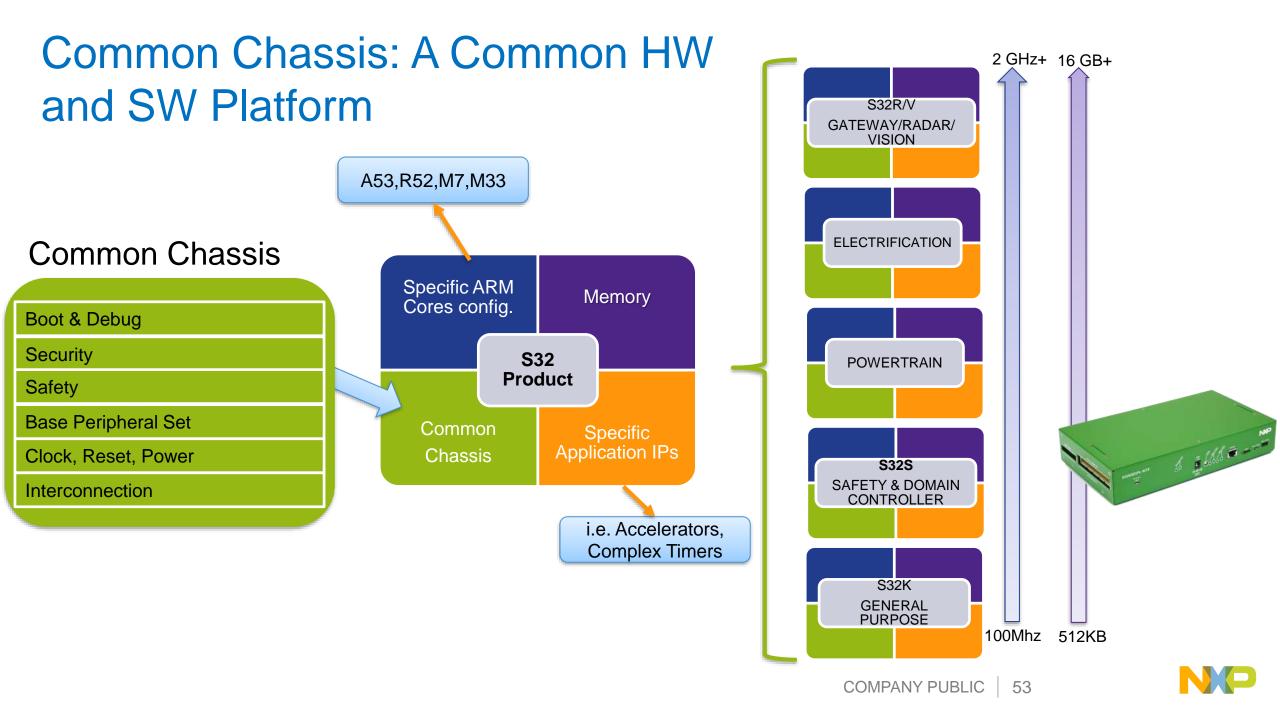
- ISO26262 ready documentation
- System validation test (eFAST)
- Global ecosystem (incl HW & SW)

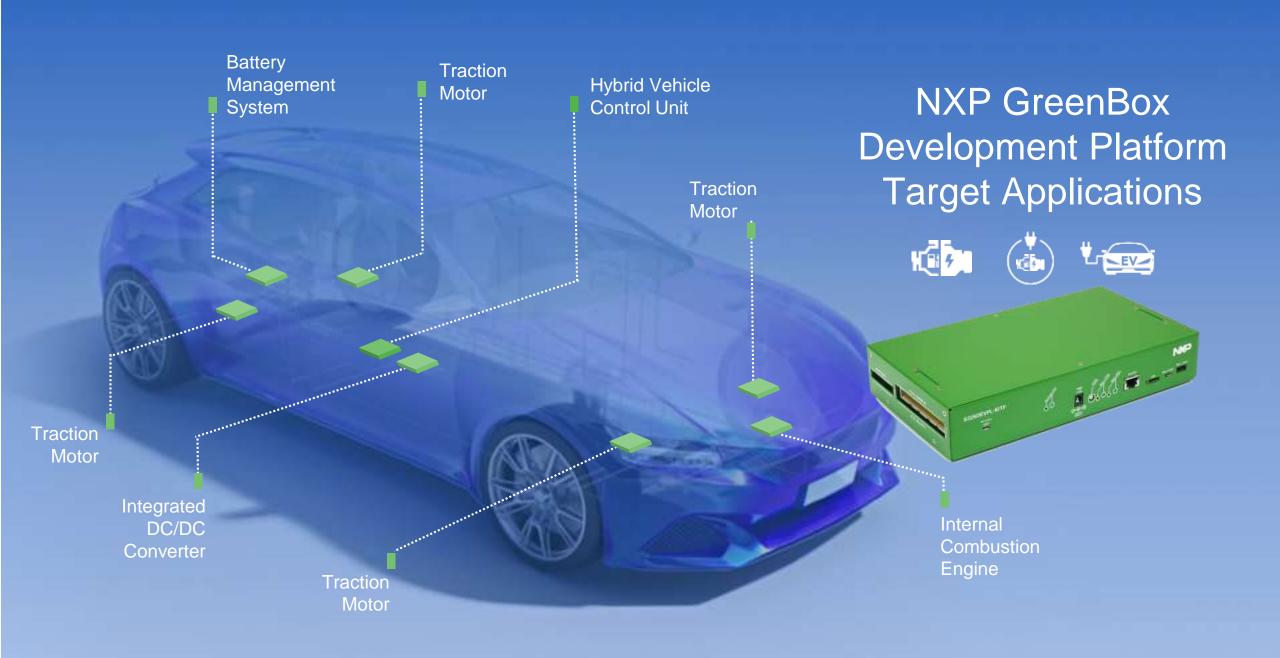


Electrification Market Mapping





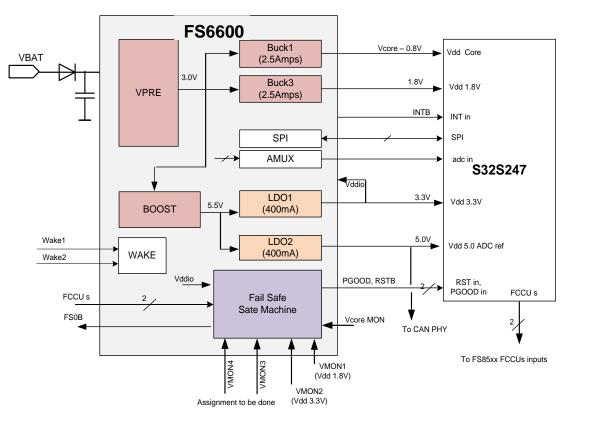






S32S2 + FS6600 – Value Proposition

FS8530 Derivative for Drivetrain Market



- NXP system solution : integrated Safety Power Management solutions to support High Performance applications requiring S32Sx MCUs.
- Simplify your ISO 26262 development : designed to support up to ASIL-D level with combined architecture and documentation
- Fault tolerant system : Innovative Fail Silent and Fault Recovery Modes
- High-Performance energy usage : Synchronous DC-DC Converter Architecture with High Efficiency and Lower Power Dissipation.
- **BOM optimized :** Programmable Switching frequency up to 3MHz, for cost and space savings
- Configurable High Precision Monitoring
- Accelerate your development : Ready to use platform combining HW, SW and Documentation.
- Platform available end 2018



FS6600 – S32S2 Attach ASIL-D SBC

FS85 Derivative for Drive Train Domain Controller

Power Management

- Input supply up to 60V 12V and 24V systems
- HVBUCK , adjustable 3.3V to 5V, scalable output current up to 10A
 - Synchronous Buck, 300kHz to 2.5MHz, ext. MOS
- BUCK1+2, multiphase, adjustable 0.8V to 1.8V, up to 5A DC max
 - Synchronous Buck, up to 3MHz, int. MOS
- BUCK3, adjustable 1.0V to 3.3V, up to 2.5A DC max
 - Synchronous Buck, up to 3MHz, int. MOS
- BOOST 5V to 5.74V, up to 800mA DC- 1.5A peak, int. MOS
- LDO1/2, configurable 1.1V to 5V, up to 400mA
- Synchronization signal for dual device operation / Power GOOD output

System Features

- Independent Safety Monitoring Unit
- Control via 32 bits SPI (including CRC).
- Low Power Mode : <10µA in LPOFF, wake up via dedicated pins
- AMUX: Battery, Internal Safety critical voltages, Precise Vref and Temperature
- Emulation and Programming capability offered in Engineering mode only : Voltage, frequency, phase shift, PW sequencing.
- EMC optimization : Spread Spectrum, Vpre Slew Rate control



Reference silicon available now FS66 Samples : Now

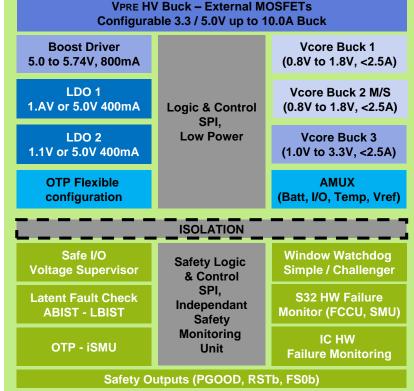
FS66 PPAP : Q1 2019

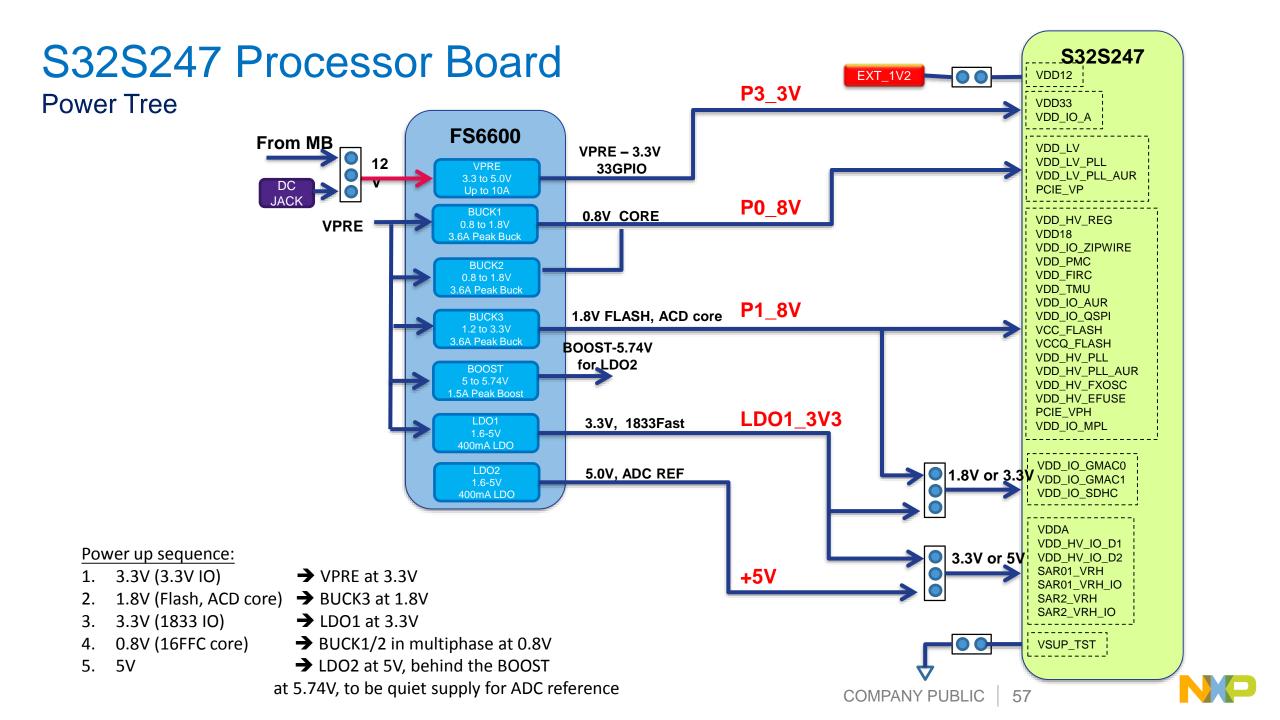
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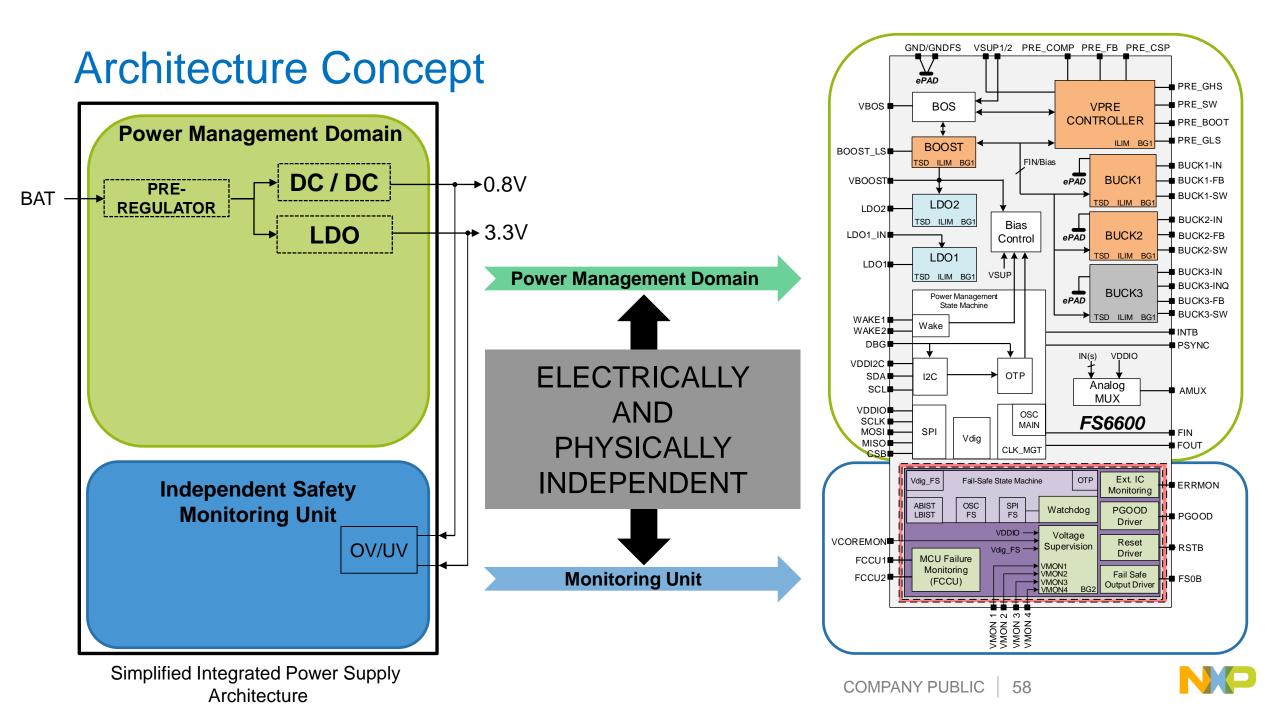




FS6600 Functional Block Diagram

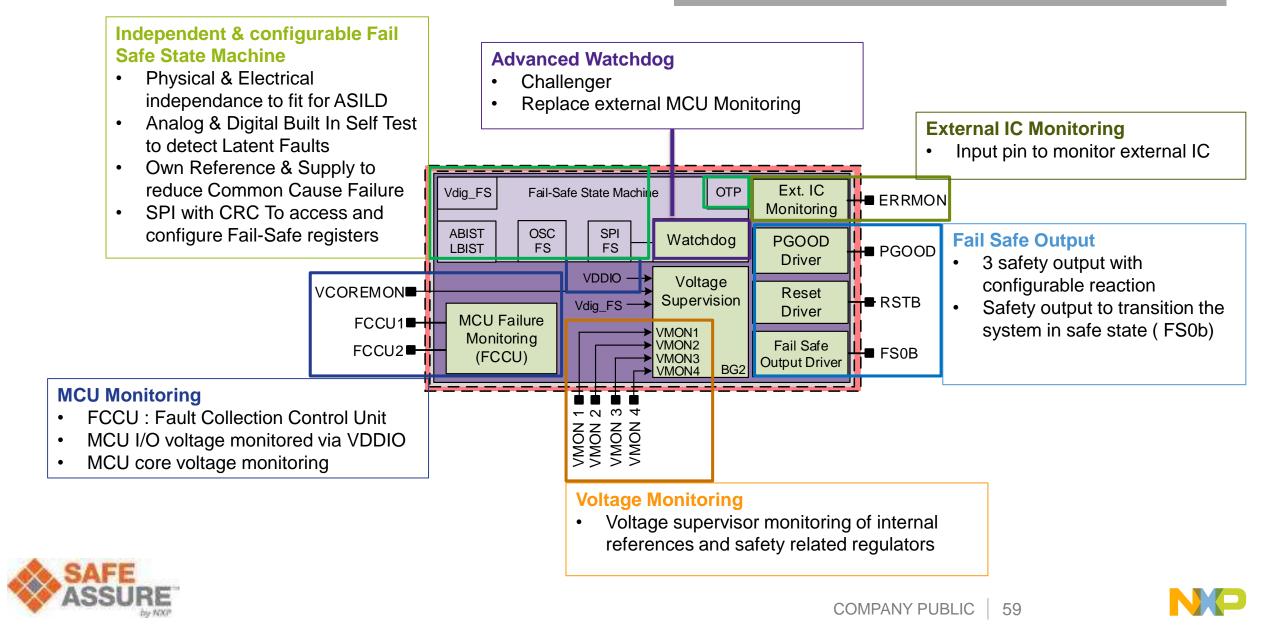




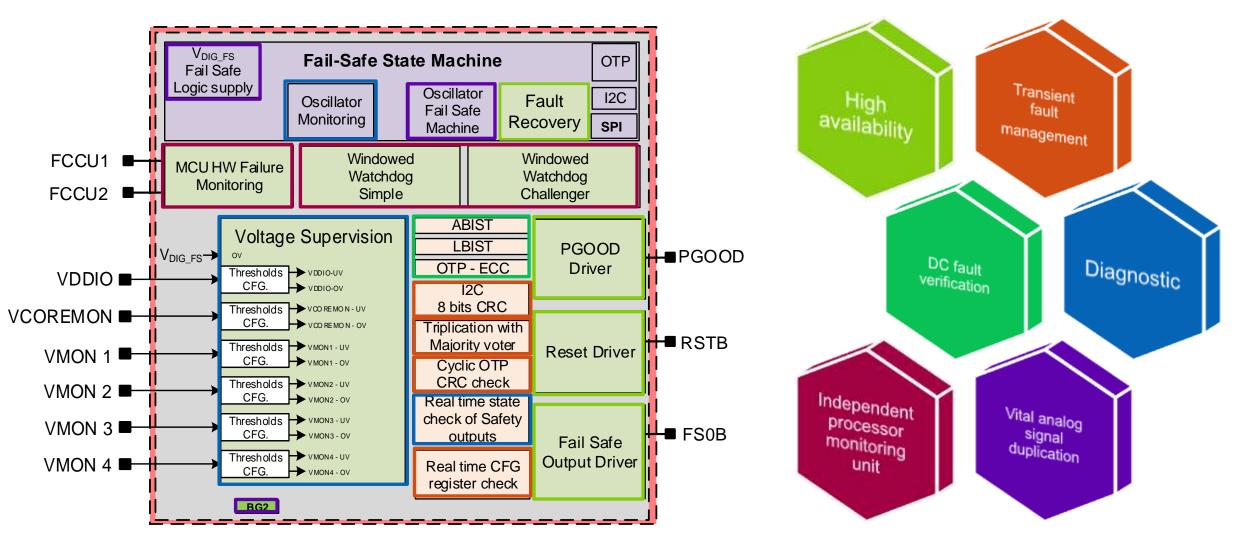


FS66 Architecture Concept

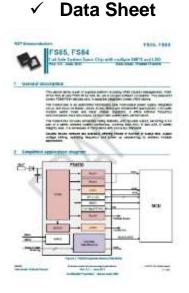
All safety mechanism reaction are < FTTI < 10ms FTTI = Faul Tolerant Time Interval



Independent Safety Monitoring Unit



SBC – Documentation, Tools & Application Support

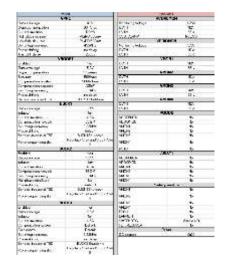




✓ Thermal Tool

Apres 10 Mol 11 Aprel 12 Mol 20 Mol 2

✓ OTP Programming Tool



✓ Safety Manual

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FS85/FS84	Functional Saloty Manual	
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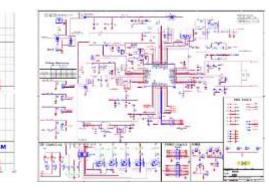
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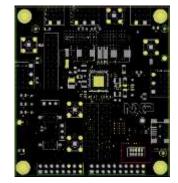


✓ Models - SIMPLIS

Phase Mergin : PM = 67°C Gets mergin : GM = 19dE ✓ PCB Schematic

✓ PCB Layout Recommendations









NXP Safety Backbone Solutions for Drivetrain Electrification

MCU	Attach	Reference	48V eMachine MPC574x + FS651x	VCU – Domain Control S32S2x + FS66x	BMS - Battery Controller 12V S32K1 + FS45 HV MPC577x+FS65
S32S24x	FS6600	Green Box		8us 12/48V DC/DC 48V Bus	
S32K1x	FS450x	Yes	Combustion Engine	Product 48V eCat Motor	High-Voltage Traction Bettery
MPC574x	FS650x	Yes	Hybrid Gaintrol Unit AC Power Plug	AC/DC Charger HV Power Bus	
MPC577x	FS651x	Yes	AC/DC converter MPC574x + FS650x	Motor control, inverter, HCU MPC577x+FS65	DC/DC Converter MPC574x + FS650x

Safety System Basis Chip



- FOCUS ASIL B/D SYSTEMS TO POWER SAFETY MCU
- SCALABLE AND SAFE PORTFOLIO FOR DRIVETRAIN ELECTRIFICATION AND ADAS MARKETS

	Safety SBCs		Pow LDO/DC		Functional Safety Architecture	Syster Feature	
SBC	Package	Voltage	Current	MCU Attach	Safety Features	System Features	SOP
FS650x FS45xx	LQFP48EP	DC/DC 6.5V DC/DC 0.9-5V or LDO 0.9-5V LDO 3.3/5V	2.0A 0.8/1.5/2.2A or 0.5A 100/300mA	MPC574x MPC577x S32K1 Other safety MCUs	ASIL D. Fail Silent. FS0 /FS1 safe delay.FCCU Watchdog challenger	Vcore DC/DC FS65 or LDO FS45. Grade 1 & 0. LDTimer. 30µA low power mode. Tracker. CAN/LIN/no PHY options	Released
FS85xx FS84xx FS6600	QFN56EP	DC/DC 3.3/5.0V DC/DC 0.8/1.8V DC/DC 1.0/3.3V LDO 1.1/5.0V	10A (ext.) 2.5/5.0A 2.5A 2x 400mA	S32R2 Radar S32V2 Vision S32S2 Safety Other safety MCUs	ASIL D: FS85, FS66. ASIL B: FS84. Fail Silent. FS0. W/D challenger. FCCU	12 & 24V systems. Frequency synch. Config Power Up/Down. Static Voltage Scaling. SPI/I ² C. External PMIC synchronization	Q2 2019





Summary

- Automotive Market moving to electrification after 2020
- NXP has a global potfolio of solutions to support Electrification market (MCU, FSBC, PHY, GD, BCC)
- FSBC has strong market presence in Electrification
 - Energy Conversion : DCDC 48/12, DCDC HV/12, BSG
 - Energy Storage : BMS12V, BMS48V, BMSHV
 - Energy Actuation : HV Inverter, 48V EM, TCU
- Main reasons of market acceptance are
 - Safety concept to help achieving ASIL D and Robustness
 - Portfolio Scalability and System Solution
- Next Generation MCU + FSBC provide
 - Computing, power and safety scalability offer
 - Safety innovations
 - Simplified and ready to use ecosystem

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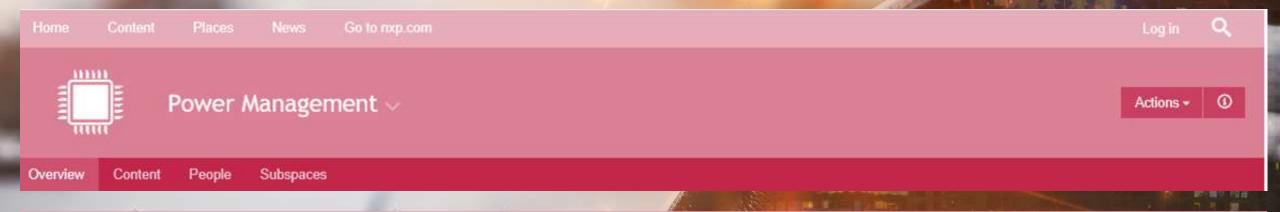
Other Sessions of Interest

WEDNESDAY

Session Code	Title	Presenter	CoPresenters	Duration	Room	Start Time
AMF-AUT-T3352	Automotive—Safety Power Management Solutions for Car Electrification and ADAS	David Lopez	Vincent Lagardelle	2	Galilee (Main Floor)	9:00 AM
AMF-AUT-T3402	Attach PMIC Offerings for NXP's Next-Generation Gateway Processors	Vincent Lagardelle	David Lopez	1	Galilee (Main Floor)	11:00 AM



Join our Community



Public community for NXP power management solutions, it addresses:

- Solutions to power i.MX, MPC, S32, LS and LX processors
- Support for Functional safety related items
- EMC considerations in power management

https://community.nxp.com/community/Power-Management?tid=community







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