

Signals Overview

Note that individual chapters of this document provide details for each signal, describing each signal's behavior when the signal is asserted or negated and when the signal is an input or an output.

The following table provides a summary of the signals grouped by function. Note that the direction of signals applies for the primary signal function listed in the left-most column of the table for that row (and does not apply for the alternate function or reset configuration input signals).

Table 3-1. T2080 Signals Reference by Functional Block

| Name | Description | Alternate Function(s) | Number of Signals | I/O |
|--|------------------------------|-----------------------|-------------------|-----|
| DDR Memory Interface see DDR Controller for more details | | | | |
| D1_MAPAR_ERR_B | Address Parity Error | | 1 | I |
| D1_MAPAR_OUT | Address Parity Out | - | 1 | O |
| D1_MA[0:15] | Address | - | 16 | O |
| D1_MBA[0:2] | Bank Select | - | 3 | O |
| D1_MCAS_B | Column Address Strobe | - | 1 | O |
| D1_MCKE[0:3] | Clock Enable | - | 4 | O |
| D1_MCK[0] | Clock | - | 1 | O |
| D1_MCK[1:3] | Clock | - | 3 | O |
| D1_MCK[0:3]_B | Clock Complement | - | 4 | O |
| D1_MCS[0:3]_B | Chip Select | - | 4 | O |
| D1_MDIC[0:1] | Driver Impedance Calibration | - | 2 | I/O |
| D1_MDM[0:8] | Data Mask | - | 9 | O |
| D1_MDQS[0:8] | Data Strobe | - | 9 | I/O |
| D1_MDQS[0:8]_B | Data Strobe | - | 9 | I/O |
| D1_MDQ[0:63] | Data | - | 64 | I/O |
| D1_MECC[0:7] | Error Correcting Code | - | 8 | I/O |
| D1_MODT[0:2] | On Die Termination | - | 3 | O |
| D1_MODT[3] | On Die Termination | - | 1 | O |
| D1_MRAS_B | Row Address Strobe | - | 1 | O |
| D1_MWE_B | Write Enable | - | 1 | O |
| Integrated Flash Controller see IFC Controller for more details | | | | |
| IFC_AD[0] | IFC Address / Data | cfg_gpinput[0] | 1 | I/O |
| IFC_AD[1:2] | IFC Address / Data | cfg_gpinput[1:2] | 2 | I/O |
| IFC_AD[3:7] | IFC Address / Data | cfg_gpinput[3:7] | 5 | I/O |
| IFC_AD[8:15] | IFC Address / Data | cfg_rcw_src[0:7] | 8 | I/O |
| IFC_AVD | IFC Address Valid | | 1 | I/O |
| IFC_A[16] | IFC Address | | 1 | I/O |
| IFC_A[17] | IFC Address | | 1 | I/O |

Table continues on the next page...