



	When no use
SPI_MISO SPI_MOSI	2~10K pull-high to 1.8V
SPI_CLK	2~10K pull-down to GND

i SDHC_CD_B and SDHC_WP_B pins were added to T4240 for rev2 silicon. They are not functional on rev 1.x silicon.

i Keep UT_SPICLK and UT_SDHCCLK traces short (<2 cm)

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		Title T4240 SPI / SDHC Port	
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