PEX RC Mode ATMU Configuration Guideline

- The RC's Type 1 Header's Mem_Base & Mem_Limit Register Setup:
    - The translation address range of all the outbound windows (except OW0) must be enclosed by the address range defined in Mem_Base & Mem_Limit
    - The base address range of all RC's inbound windows must be configured as outside of the Mem_Base & Mem_Limit range