


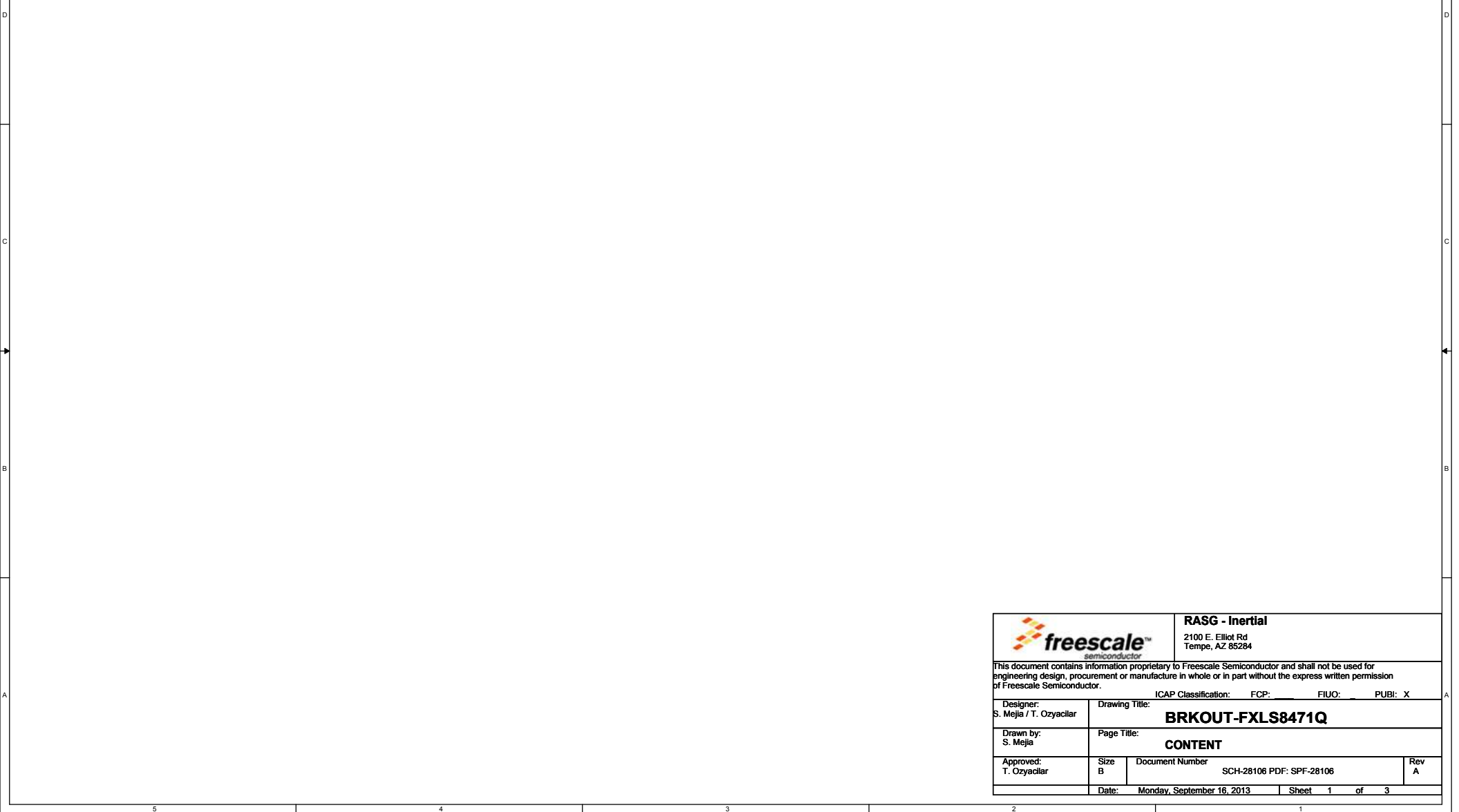
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3	FXLS8471Q

Revisions

Rev	Description	Date
A	Original Release	09-13-13

		RASG - Inertial 2100 E. Elliot Rd Tempe, AZ 85284	
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Designer: S. Mejia / T. Ozyacliar		Drawing Title: BRKOUT-FXLS8471Q	
Drawn by: S. Mejia		Page Title: CONTENT	
Approved: T. Ozyacliar		Size B	Document Number SCH-28106 PDF: SPF-28106
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1. Unless Otherwise Specified:

All resistors are in ohms, most are 1%, 1/10 Watt. Otherwise are 5%, 1/8 Watt.
All capacitors are in uF, some are 10% or 20%
All voltages are DC
All polarized capacitors are tantalum


2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

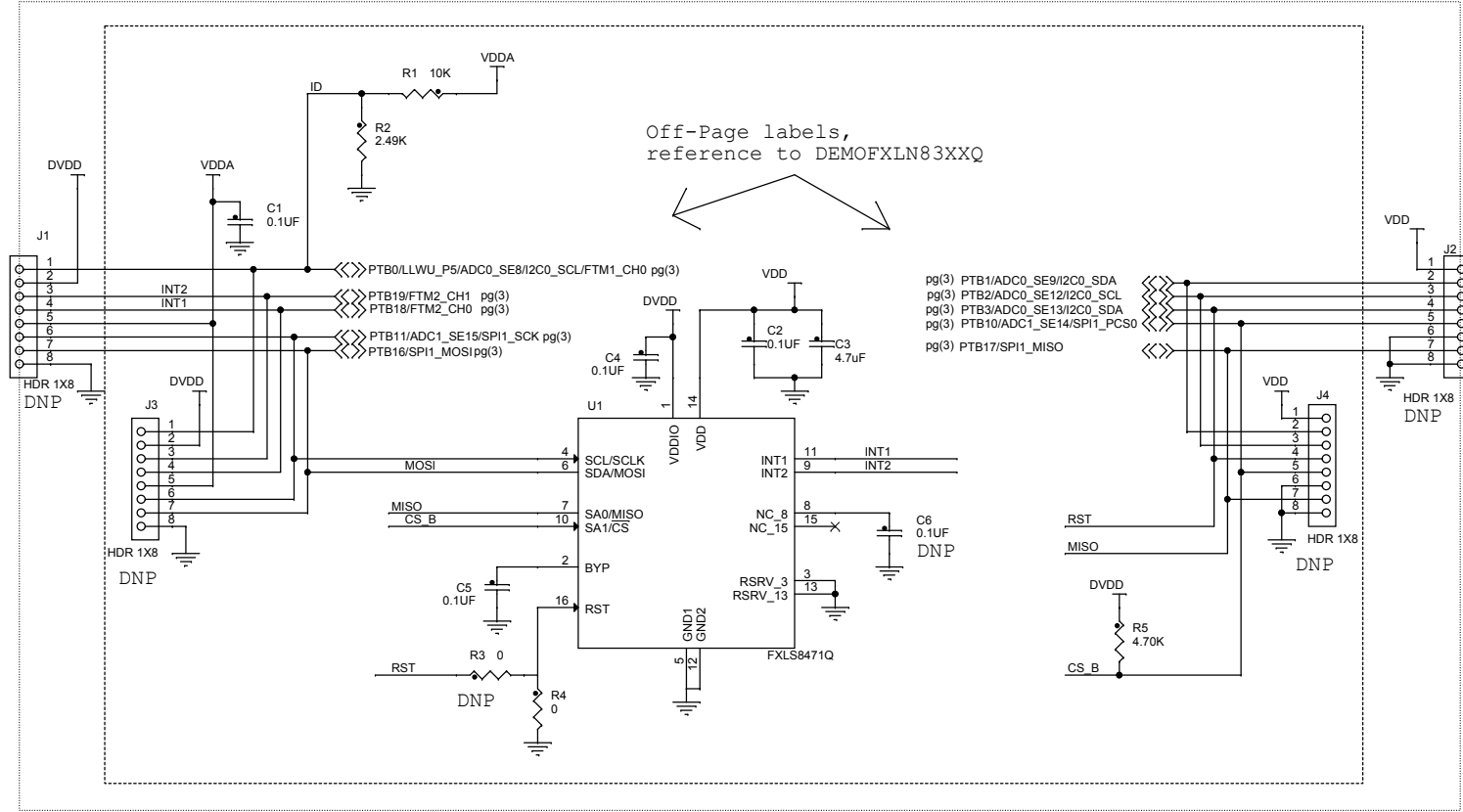
4. Special signal usage:

_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

			
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EDGE OF BOARD



NOTES:

PCB outline cuts through the center lines of J1 and J2


J3 and J4 Can be used to connect to DEMOFXLN83XXQ

DVDD = 1V8 MCU Digital Supply LDO

VDD = 2V5 Analog Sensor Supply

VDDA = 1V8 MCU ANALOG SUPPLY LDO

Capacitor C6 (space holder on pcb)

		
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