



Low ESR capacitors must be connected to CREG and CREGA pins for best performance. A grounded land area with solder mask should be placed under the package for improved shielding of the device from external effects. If a land area is not provided, no signals should be routed beneath the package.

VPP pin should be tied directly to VSS

TITLE: MMA690x Sensor Board	
Document Number:	REV:
Date: 5/27/2014 2:30:31 PM	Sheet: 1/1