

s32g2xx\_memory\_regions\_py\_patch  
Erstellt: 9/22/2023 9:13:28 AM

Modus: Unterschiede, Im Kontext

Linke Datei: D:\Projects\...\tools\S32DS\_35\S32DS\tools\S32Debugger\Debugger\scripts\s32g2xx\s32g2xx\_memory\_regions\_org.py

Rechte Datei: D:\Projects\...\tools\S32DS\_35\S32DS\tools\S32Debugger\Debugger\scripts\s32g2xx\s32g2xx\_memory\_regions.py

44	m7_tcm1 = Range(0x20180000, 0x00010000, 'ram') #M7-1 TCM Backdoor (64k)	=	44	m7_tcm1 = Range(0x20180000, 0x00010000, 'ram') #M7-1 TCM Backdoor (64k)
45	m7_tcm2 = Range(0x20200000, 0x00010000, 'ram') #M7-2 TCM Backdoor (64k)		45	m7_tcm2 = Range(0x20200000, 0x00010000, 'ram') #M7-2 TCM Backdoor (64k)
46			46	
47	sb_ram = Range(0x24000000, 0x00008000, 'ram') #Standby RAM (32k)		47	sb_ram = Range(0x24000000, 0x00008000, 'ram') #Standby RAM (32k)
48	v_ram = Range(0x38000000, 0x01400000, 'ram') #Virtual Code Ram(20M)		48	v_ram = Range(0x38000000, 0x01400000, 'ram') #Virtual Code Ram(20M)
49			49	
		<>	50	#phg_0 = Range(0x40000000, 0x00100000, 'nocache') #Peripheral Group 0 (1M)
			51	# NXP Community forum fix
50	phg_0 = Range(0x40000000, 0x00100000, 'nocache') #Peripheral Group 0 (1M)		52	phg_0 = Range(0x4001C000, 0x000E4000, 'nocache') #Peripheral Group 0 (1M)
51	phg_1 = Range(0x40100000, 0x00100000, 'nocache') #Peripheral Group 1 (1M)	=	53	phg_1 = Range(0x40100000, 0x00100000, 'nocache') #Peripheral Group 1 (1M)
52	phg_2 = Range(0x40200000, 0x00100000, 'nocache') #Peripheral Group 2 (1M)		54	phg_2 = Range(0x40200000, 0x00100000, 'nocache') #Peripheral Group 2 (1M)
53	phg_3 = Range(0x40300000, 0x00100000, 'nocache') #Peripheral Group 3 (1M)		55	phg_3 = Range(0x40300000, 0x00100000, 'nocache') #Peripheral Group 3 (1M)
54			56	
55	qspi_rx= Range(0x41000000, 0x00800000, 'nocache') #QuadSPI Rx buffer (8M)		57	qspi_rx= Range(0x41000000, 0x00800000, 'nocache') #QuadSPI Rx buffer (8M)
56			58	