

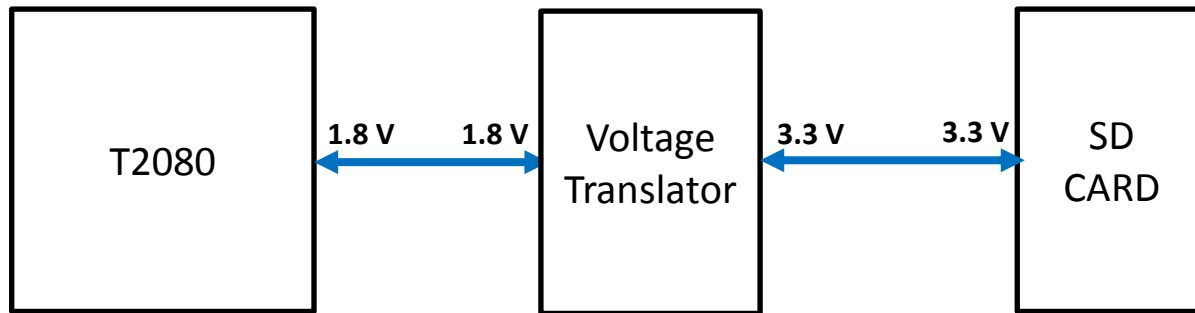


# Supported MMC/eMMC Modes

Mode	1 bit Support		4 bit Support		8 bit support	
	T2080	eMMC (4.5)	T2080	eMMC (4.5)	T2080	eMMC (4.5)
DS	Yes	Yes	Yes	Yes	Yes	Yes
HS	Yes	Yes	Yes	Yes	Yes	Yes
HS200	No	No	Yes	Yes	Yes	Yes
DDR	No	No	Yes	Yes	No	Yes



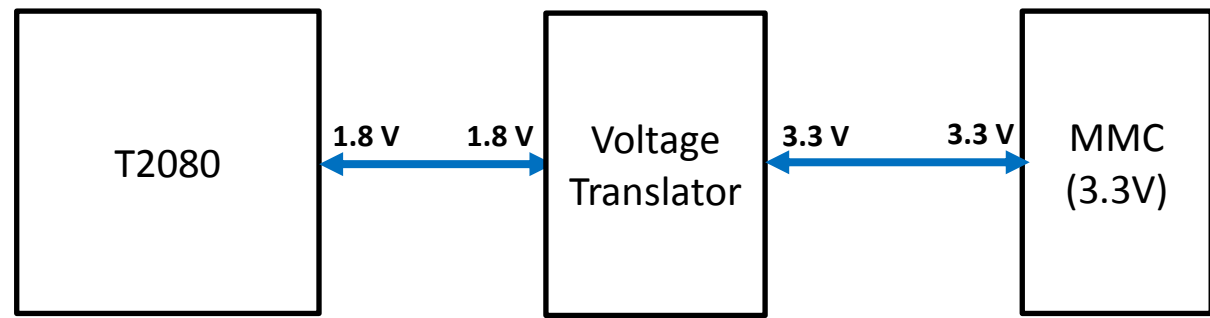
# SD Card Connections for T2080 (DS and HS Modes)



◀▶ **CMD, DAT[0], DAT[1:3], CLK, CD\_B, WP**

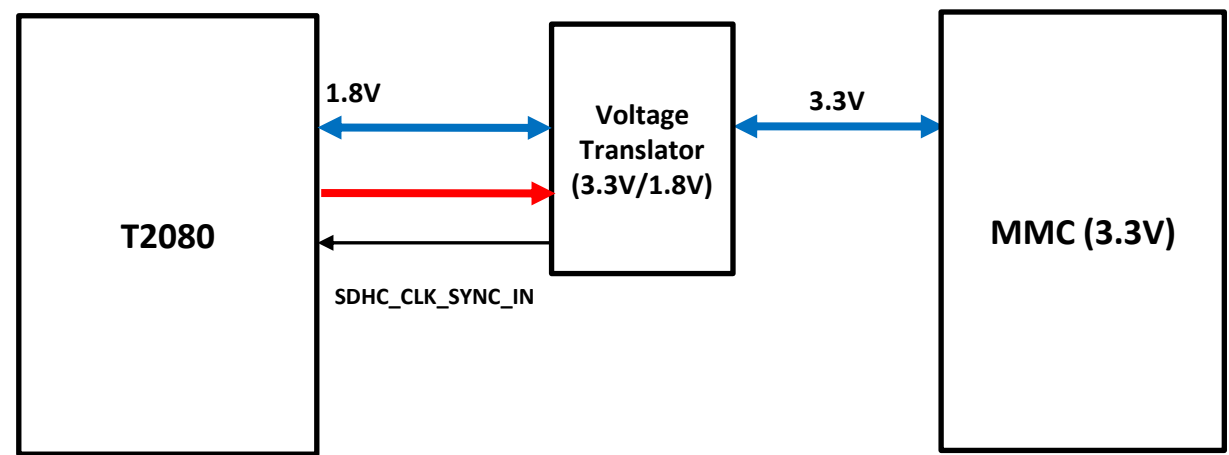
- Other signals should be left NC
- SYNC\_OUT should be pulled-down with a weak resistor or the pin should be configured for alternate functionality

# MMC Card Connections for T2080 (DS, HS, HS200 Modes)



- CMD, DAT[0], DAT[1:7], CLK, CD**
- Other signals should be left NC
  - SYNC\_OUT should be pulled-down with a weak resistor or the pin should be configured for alternate functionality
  - Voltage translator is not needed for 1.8V MMC.

# MMC (3.3V) Connections for T2080 (DDR Mode)

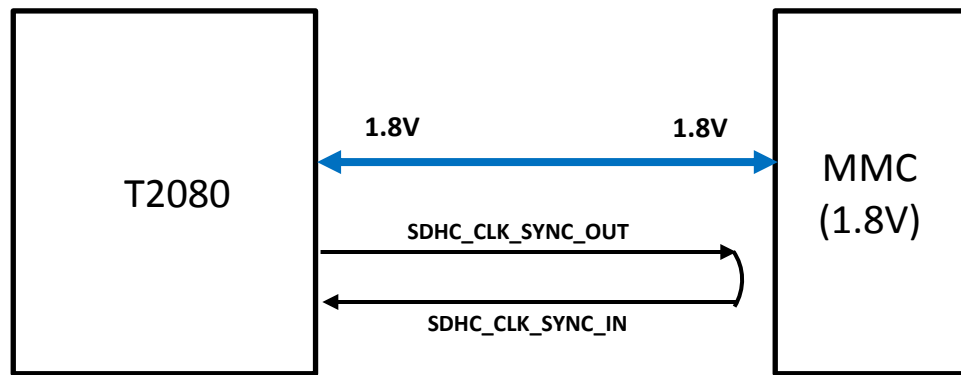


	CMD, DAT[0], DAT[1:3], CLK, CD
	DIR

- In DDR mode all the input signals are sampled with respect to SYNC\_IN

- Other signals should be left NC
- SYNC\_OUT should be pulled-down with a weak resistor or the pin should be configured for alternate functionality
- Voltage translator is not needed for 1.8V MMC.

# MMC (1.8V) Connections for T2080 (DDR Mode)



- ↔ **CMD, DAT[0], DAT[1:7], CLK, CD**
- Other signals should be left NC
- In DDR mode all the input signals are sampled wrt SYNC\_IN