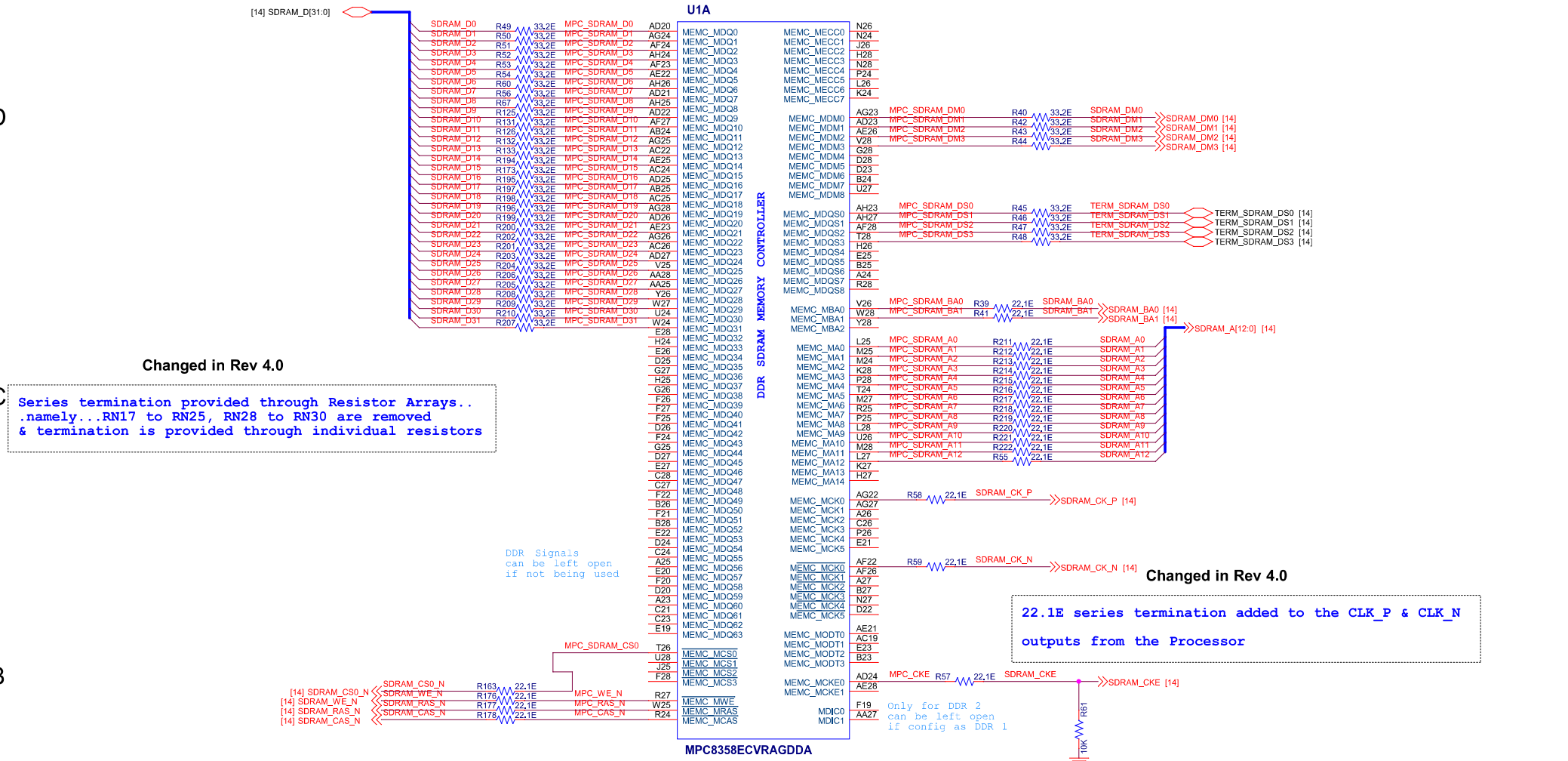
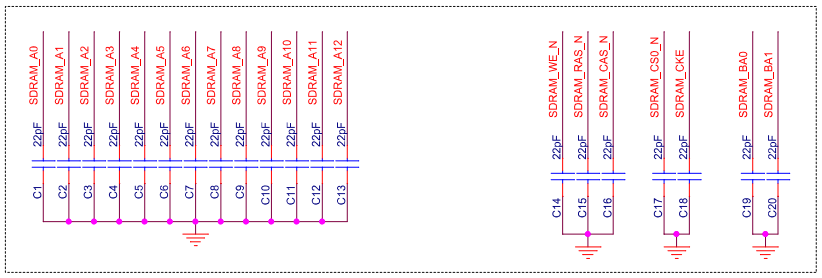
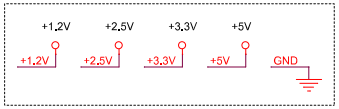






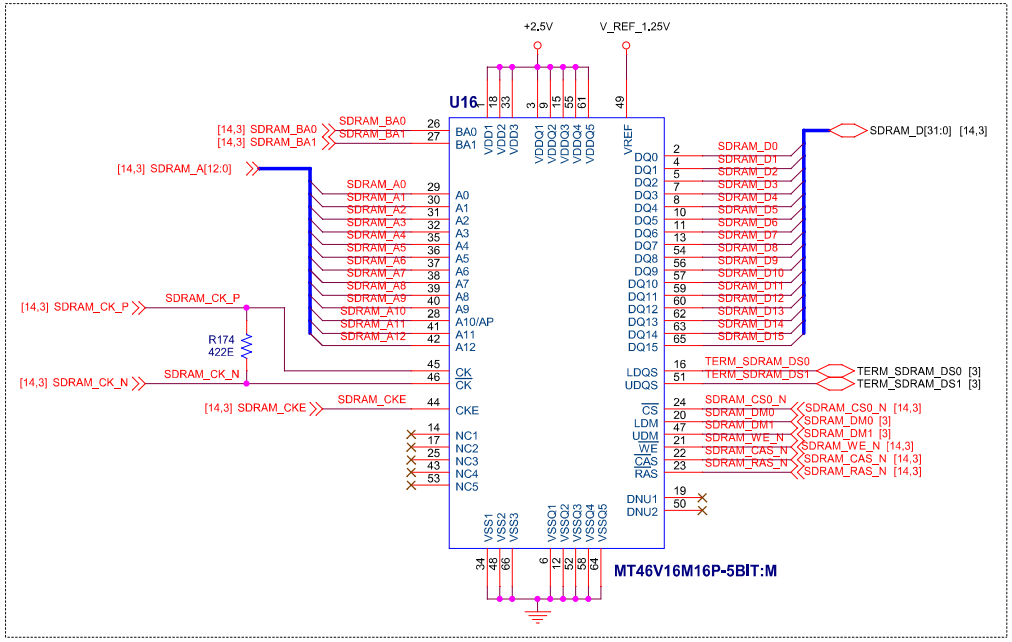
Designed By			CSKB, ATS
Checked By			KPL, ATS
Verified By			CSS, ATS
			AM, ADE
Approved By			SKS, ADE
			JKK, CEMILAC
	SIGN	DATE	NAME

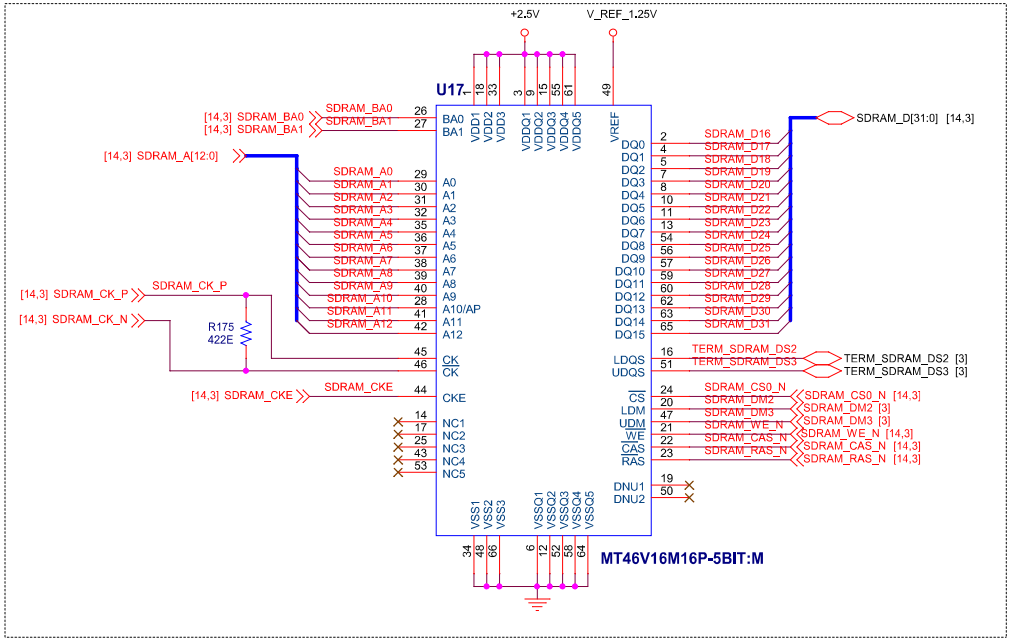
AutoTEC

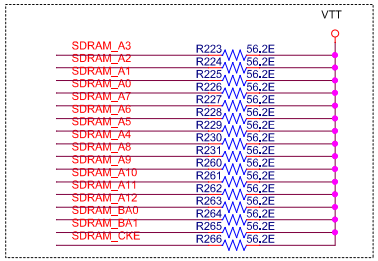
Title: RII - AED PROCESSOR CARD_MPC8358 DDR MEMORY

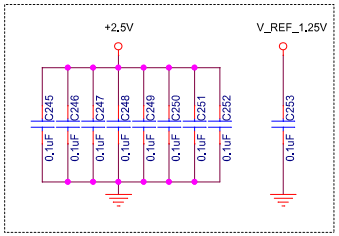
Size	Document Number	Rev
A3	ATS-AED-PROC SCH-001	4.0

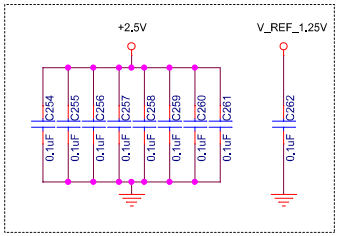
Date: Wednesday, June 17, 2020 Sheet 3 of 44

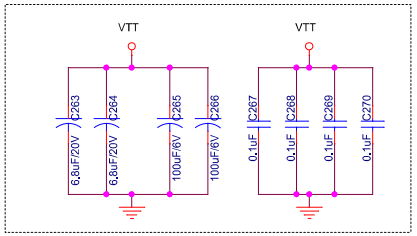


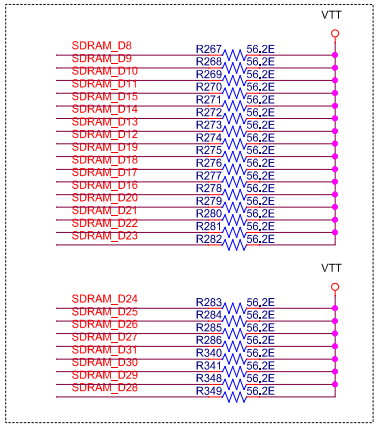






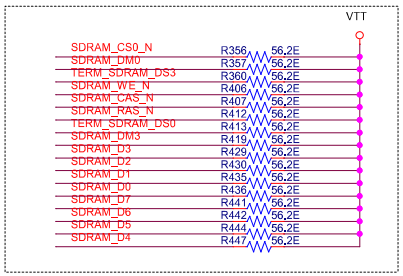


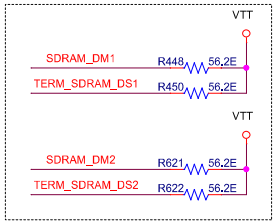


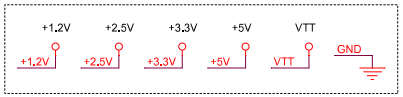


Changed in Rev 4.0

Series termination provided through Resistor Arrays..
.namely...RN31 to RN44 are removed
& termination is provided through individual resistors







Designed By			CSKB, ATS
Checked By			KPL, ATS
Verified By			CSS, ATS
			AM, ADE
Approved By			SKS, ADE
			JKK, CEMILAC
	SIGN	DATE	NAME

AutoTEC

Title: RII - AED PROCESSOR CARD_SDRAM INTERFACE

Size	Document Number	Rev
A3	ATS-AED-PROC SCH-001	4.0

Date: Wednesday, June 17, 2020 Sheet 14 of 44