

```

// Set PTHRU_ON_OFF Enable
TxBuf[0] = 0xFE; // session reg
TxBuf[1] = 0x00;
TxBuf[2] = 0x40; // MASK
TxBuf[3] = 0x40; // REGDAT(PTHRU_ON_OFF 1b enabled)
SubWriteI2C((uint8_t *)&TxBuf,4);
if((gvI2CNackFlg==imON)|| (gvI2CEndFlg!= imON)){
    retstat |= 0x02;
}

```

refer fig1

```

// for Read Verify
TxBuf[0] = 0xFE; // session reg
TxBuf[1] = 0x00;
SubWriteI2C((uint8_t *)&TxBuf,2);
if((gvI2CNackFlg==imON)|| (gvI2CEndFlg!= imON)){
    retstat |= 0x02;
}

```

refer fig2

```

SubReadI2C((uint8_t *)&NfcReadSessionData,8);
if((gvI2CNackFlg==imON)|| (gvI2CEndFlg!= imON)){
    retstat |= 0x04;
}

```

refer fig3

(x)= NfcSessionBytes	unknown	identifier not found: Nfc...	
▼ NfcReadSessionData	unsigned char[8]	[1 '\x01', 255 '\xff', 255 '\x...	0x001C10
(x)= [0]	unsigned char	1 '\x01'	0x001C10
(x)= [1]	unsigned char	255 '\xff'	0x001C11
(x)= [2]	unsigned char	255 '\xff'	0x001C12
(x)= [3]	unsigned char	255 '\xff'	0x001C13
(x)= [4]	unsigned char	255 '\xff'	0x001C14
(x)= [5]	unsigned char	255 '\xff'	0x001C15
(x)= [6]	unsigned char	255 '\xff'	0x001C16
(x)= [7]	unsigned char	255 '\xff'	0x001C17

Expect 0x40 (PTHRU_ON_OFF 1b :enable)

fig1 Write I2C FE 00 40 40 without NAK

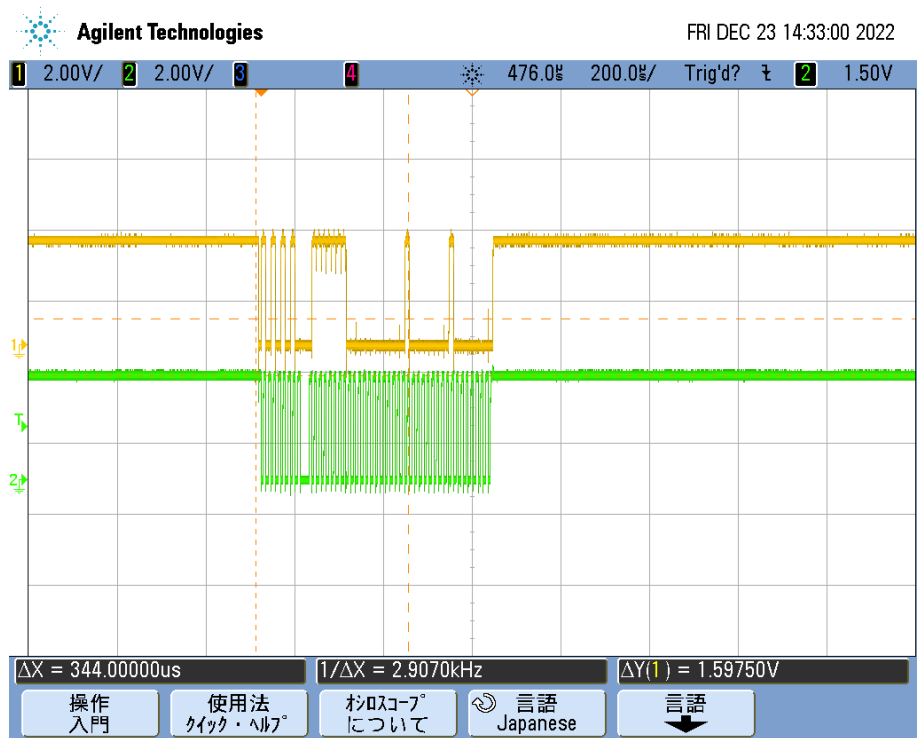


fig2 Write MEMA®A for verify Write I2C FE 00

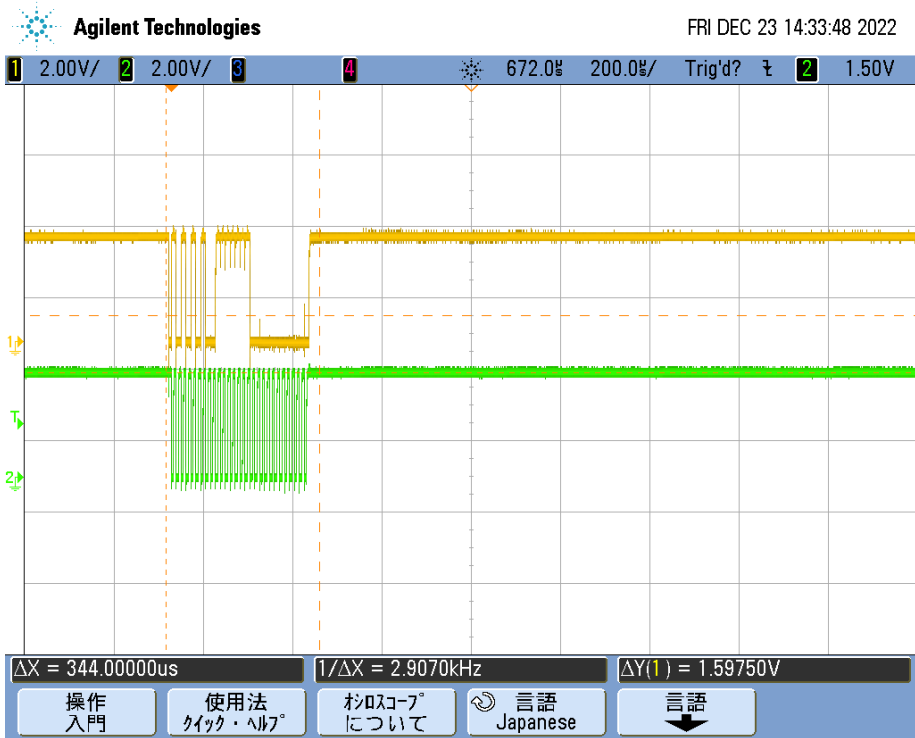


fig3 Read from I2C 01h FFh FFh FFh.....

could not written REGDAT(PTHRU_ON_OFF 1b enabled , why?

