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Revisions

Rev	Description	Date	Approved
X1	Initial Design Release	21-Oct-15	Antonio Quiroz
X2	Add Sensor	11-Nov-15	Antonio Quiroz
X3	Updated to MKW24D512	27-Nov-15	Antonio Quiroz

X-KW2X-OSN-RD

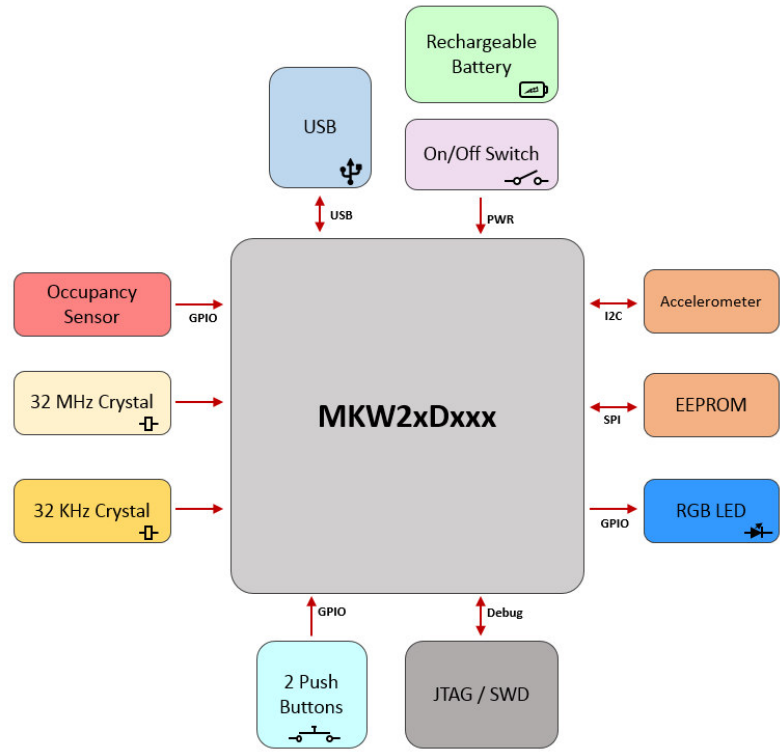
Occupancy Sensor Node

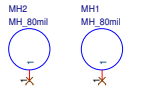
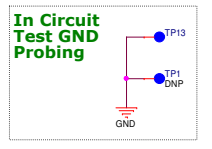
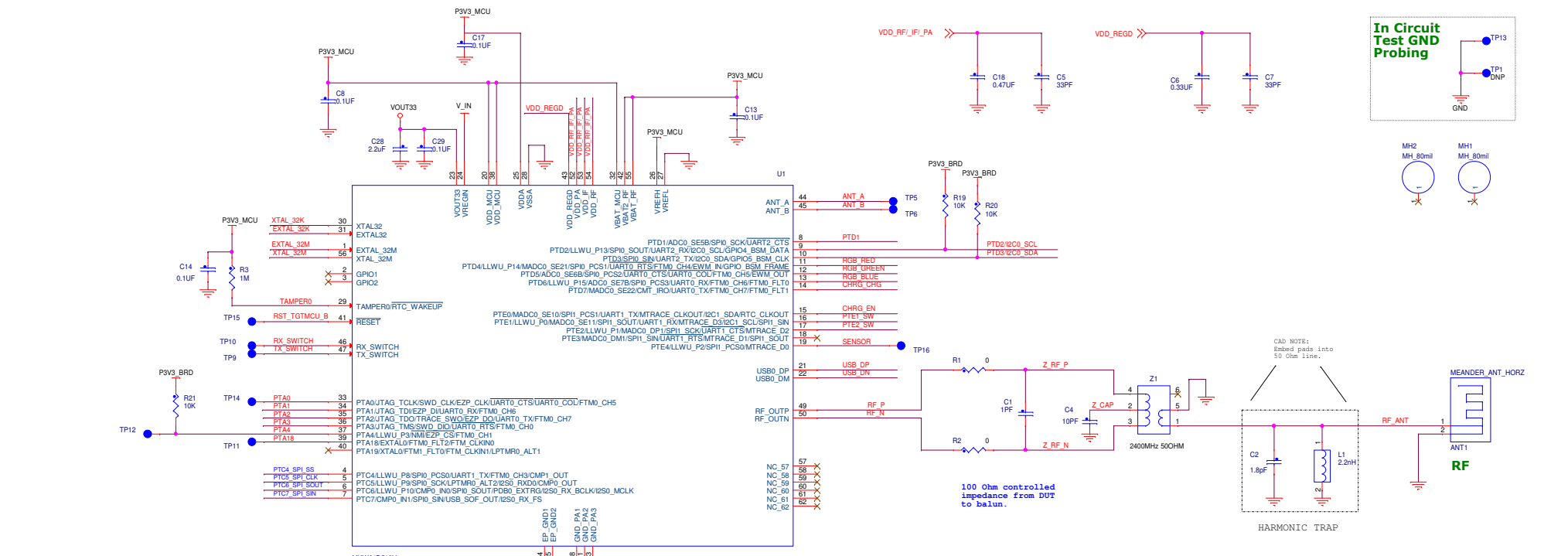
		Microcontroller Product Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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ICAP Classification: FCP: FUC: X PUB:			
Designer: Antonio Quiroz	Drawing Title: X-KW2X-OSN-RD		
Drawn by: Antonio Quiroz	Page Title: TITLE PAGE		
Approved: Juan Carlos Pacheco	Size C	Document Number SCH-xxxxx PDF: SPF-xxxxx	Rev X3
Date: Friday, November 27, 2015	Sheet 1	of 3	

1. Interrupted lines coded with the same letter or letter combinations are electrically connected.

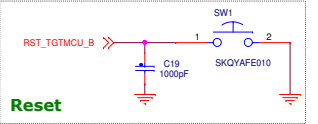
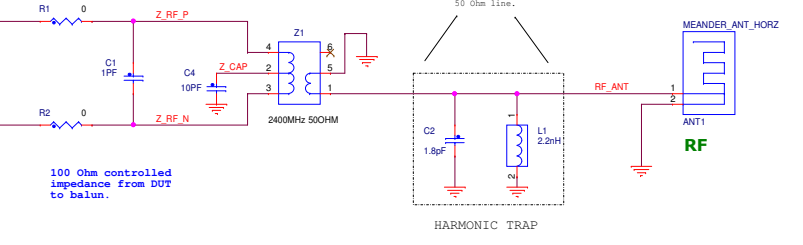
2. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals

3. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

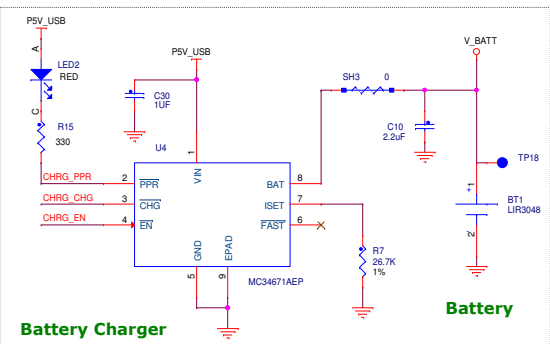




CAD NOTE:
Embed pads into
50 Ohm line.

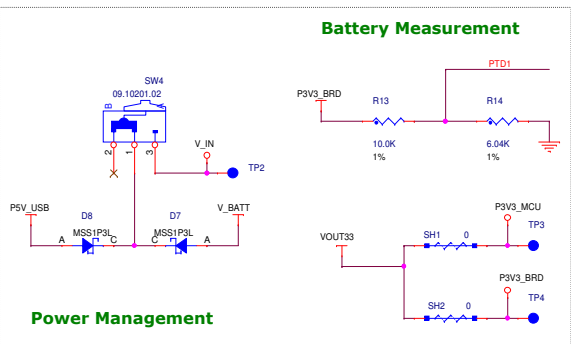


Reset



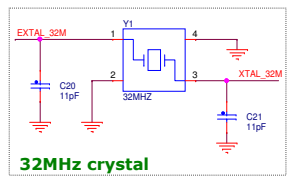
Battery Charger

Battery

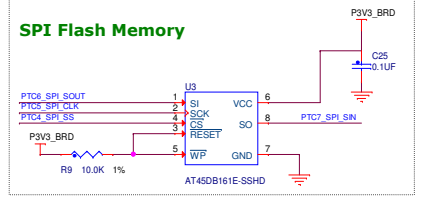


Battery Measurement

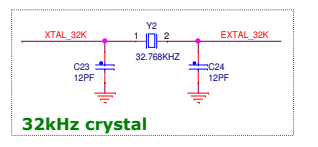
Power Management



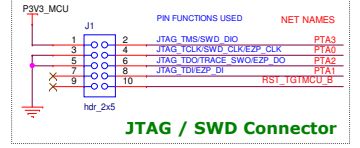
32MHz crystal



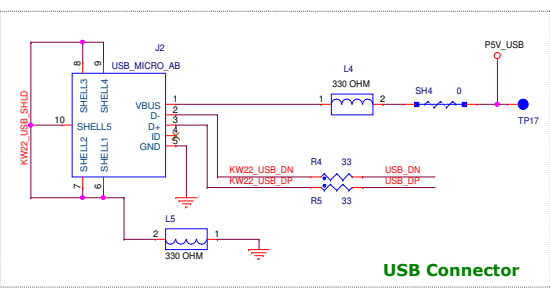
SPI Flash Memory



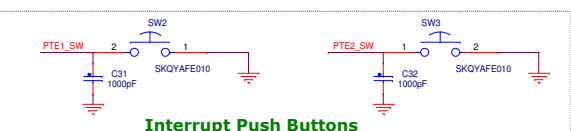
32kHz crystal



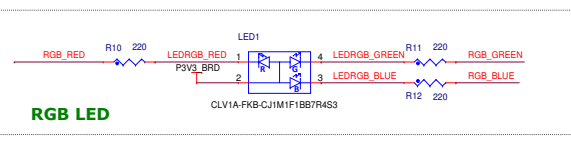
JTAG / SWD Connector



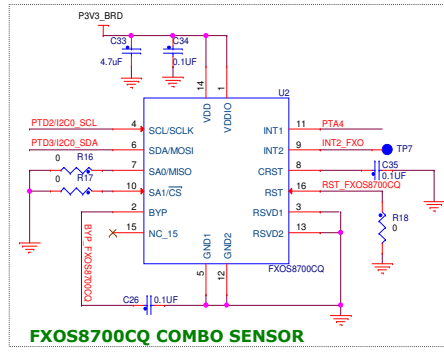
USB Connector



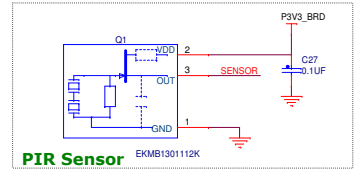
Interrupt Push Buttons



RGB LED



FXOS8700CQ COMBO SENSOR



PIR Sensor

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