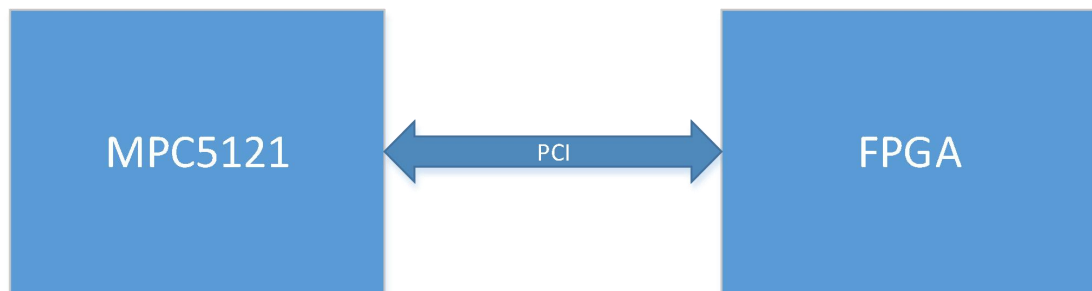


I have a confused about the pci of MPC5121.

we used the mpc5121 in the project , and product 6 PCS board for debug ,the function of PCI is ok ,only one board have the program about pci.

The hardward block diagram is as follows.

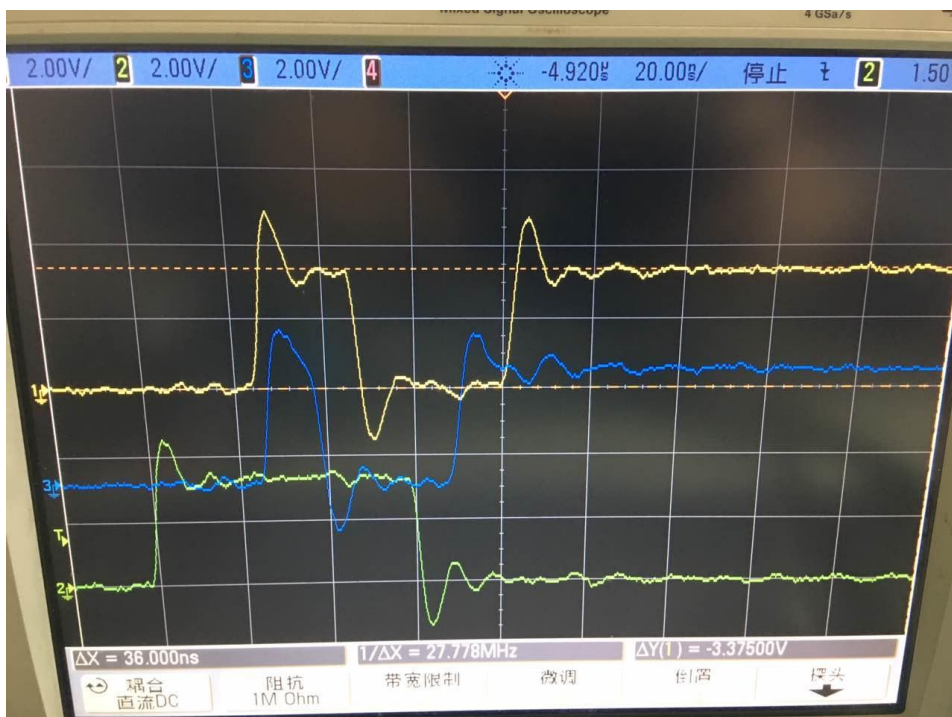


the test method is as follows: write the data 0xffff to the register of 0x5000008c in the FPGA by the MPC5121, and read the data in the register 0x50000008c in the FPGA, the compare data in the process of read and write , if the data is equal , the function of PCI is ok .

By the Continuous reading and writing , the data is equal in the twenty minutes , but the data is different now and then , so the PCI communication of MPC5121 and FPGA is ok in the most of the time, and the data is error now and then .

The test method is as follow:

Write the data 0xffff to the register of 0x50000008c in the FPGA by the MPC5121 by the continuous time, We measure the AD6 and AD11 of the PCI by oscilloscope, we find the the delay time is 10ns in the comparison of AD6(blue) and AD11(yellow),the green is IRDY.



So I need to confirm the question :the 10ns delay time in the comparison of AD6 and AD11 is normal phenomenon???

If the delay time is ok , why the communication of PCI is fail in the sometimes??