Core Registers

ADC - Analog to Digital Converters

AIPS - Peripheral Bridges

C55FMC - Embedded Flash Memory

CAN - FlexCan modules

CMU - Clock Monitor Units

CRC_0 - Cyclic Redundancy Check Unit

CTU - Cross-Triggering Units

DCL - Different Clock Register

DMA - Direct Memory Access

EIM - Error Injection Module

ENET_0 - Ethernet

eTimer - Enhanced Motor Control Timer Modules

FCCU - Fault Collection and Control Unit

FlexPWM - Pulse Width Modulator Modules

FR_0 - FlexRay Communication Controller

INTC_0 - Interrupt Controller

IRCOSC - 16 MHz internal RC oscillator

LFAST_0 - LVDS Fast Asynchronous Serial Transmission

LINFlexD Controllers

MC_CGM - Clock Generation Module

MC_ME - Mode Entry Module

MC_PCU - Power Control Unit

MC_RGM - Reset Generation Module

MEMU - Memory Error Management Unit

PCM - Platform Configuration Module

PFLASH - Platform Flash Controller

PIT_0 - Periodic Interval Timer

PLLDIG - PLL Digital Interface

PMC - Power Management Controller

PRAMC - Platform RAM Controller

SGEN_0 - Sine Wave Generator

SIPI_0 - System Interprocessor Interface

SIUL2 - System Integration Unit Lite2

SMPU_0 - System Memory Protection Unit

SPI - Serial Peripheral Interfaces

SRX - SENT (SAE J2716) Receivers

SSCM - System Status and Configuration Module

STCU - Self-Test Control Unit

RUNSW	00000000	MBSWPLLEN	00: Shutdown MBIST is executed without using the on-chip
		LBSWPLLEN	00: Shutdown LBIST is executed without using the on-chip
		RUNSW	00: Idle
SKC	XXXXXXXX	SKC	
CFG	12100008	PTR	12
		LB_DELAY	10
		WRP	0
		PMOSEN	00: MBIST PMOS Test is not enabled
		MBU	01: MBIST simplified Multi Bit Upset algo is used to che
		CLK_CFG	0
WDG	00020000	WDGEOC	00020000
ERR_STAT	00000000	LOCKESW	00: The PLL was correctly locked during the self-test se
		WDTOSW	00: LBIST and MBIST time slots have been completed withi
		ENGESW	00: Valid Engine execution
		INVPSW	00: Valid linked pointer list
		CFSF	00: No errors that trigger the Critical Faults condition
		NCFSF	00: No errors that trigger the Non-Critical Faults condi
		LOCKE	00: The PLL was correctly locked during the startup self
		WDTO	00: LBIST and MBIST time slots have been completed withi
		ENGE	00: Valid Engine execution
		INVP	00: Valid linked pointer list
LBS	0000000F	LBS3	01: Successful LBIST execution
		LBS2	01: Successful LBIST execution
		LBS1	01: Successful LBIST execution
		LBS0	01: Successful LBIST execution
LBE	0000000F	LBE3	01: LBIST execution finished
		LBE2	01: LBIST execution finished
		LBE1	01: LBIST execution finished
		LBE0	01: LBIST execution finished
LBSSW	00000000	LBSSW3	00: Failed LBIST execution
		LBSSW2	00: Failed LBIST execution
		LBSSW1	00: Failed LBIST execution
		LBSSW0	00: Failed LBIST execution
LBESW	00000000	LBESW3	00: LBIST execution not yet completed
		LBESW2	00: LBIST execution not yet completed
		LBESW1	00: LBIST execution not yet completed
		LBESW0	00: LBIST execution not yet completed
LBRMSW	00000000	LBRMSW15	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW14	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW13	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW12	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW11	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW10	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW9	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW8	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW7	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW6	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW5	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW4	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW3	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW2	00: Dedicated functional reset is pulsed at the end of L
		LBRMSW1	00: Dedicated functional reset is pulsed at the end of L

		LBRMSW0	00: Dedicated functional reset is pulsed at the end of L
MBSL	07FFFFFF	MBS26	01: No Fault detected during the BIST execution
		MBS25	01: No Fault detected during the BIST execution
		MBS24	01: No Fault detected during the BIST execution
		MBS23	01: No Fault detected during the BIST execution
		MBS22	01: No Fault detected during the BIST execution
		MBS21	01: No Fault detected during the BIST execution
		MBS20	01: No Fault detected during the BIST execution
		MBS19	01: No Fault detected during the BIST execution
		MBS18	01: No Fault detected during the BIST execution
		MBS17	01: No Fault detected during the BIST execution
		MBS16	01: No Fault detected during the BIST execution
		MBS15	01: No Fault detected during the BIST execution
		MBS14	01: No Fault detected during the BIST execution
		MBS13	01: No Fault detected during the BIST execution
		MBS12	01: No Fault detected during the BIST execution
		MBS11	01: No Fault detected during the BIST execution
		MBS10	01: No Fault detected during the BIST execution
		MBS9	01: No Fault detected during the BIST execution
		MBS8	01: No Fault detected during the BIST execution
		MBS7	01: No Fault detected during the BIST execution
		MBS6	01: No Fault detected during the BIST execution
		MBS5	01: No Fault detected during the BIST execution
		MBS4	01: No Fault detected during the BIST execution
		MBS3	01: No Fault detected during the BIST execution
		MBS2	01: No Fault detected during the BIST execution
		MBS1	01: No Fault detected during the BIST execution
		MBS0	1
MBEL	07FFFFFF	MBE26	01: MBIST execution finished
		MBE25	01: MBIST execution finished
		MBE24	01: MBIST execution finished
		MBE23	01: MBIST execution finished
		MBE22	01: MBIST execution finished
		MBE21	01: MBIST execution finished
		MBE20	01: MBIST execution finished
		MBE19	01: MBIST execution finished
		MBE18	01: MBIST execution finished
		MBE17	01: MBIST execution finished
		MBE16	01: MBIST execution finished
		MBE15	01: MBIST execution finished
		MBE14	01: MBIST execution finished
		MBE13	01: MBIST execution finished
		MBE12	01: MBIST execution finished
		MBE11	01: MBIST execution finished
		MBE10	01: MBIST execution finished
		MBE9	01: MBIST execution finished
		MBE8	01: MBIST execution finished
		MBE7	01: MBIST execution finished
		MBE6	01: MBIST execution finished
		MBE5	01: MBIST execution finished
		MBE4	01: MBIST execution finished
		MBE3	01: MBIST execution finished
		MBE2	01: MBIST execution finished
		MBE1	01: MBIST execution finished
		MBE0	1
MBSLSW	00000000	MBSSW26	00: Failed BIST execution
		MBSSW25	00: Failed BIST execution
		MBSSW24	00: Failed BIST execution
		MBSSW23	00: Failed BIST execution
		MBSSW22	00: Failed BIST execution
		MBSSW21	00: Failed BIST execution
		MBSSW20	00: Failed BIST execution
		MBSSW19	00: Failed BIST execution
		MBSSW18	00: Failed BIST execution
		MBSSW17	00: Failed BIST execution

		MBSSW16	00: Failed BIST execution
		MBSSW15	00: Failed BIST execution
		MBSSW14	00: Failed BIST execution
		MBSSW13	00: Failed BIST execution
		MBSSW12	00: Failed BIST execution
		MBSSW11	00: Failed BIST execution
		MBSSW10	00: Failed BIST execution
		MBSSW9	00: Failed BIST execution
		MBSSW8	00: Failed BIST execution
		MBSSW7	00: Failed BIST execution
		MBSSW6	00: Failed BIST execution
		MBSSW5	00: Failed BIST execution
		MBSSW4	00: Failed BIST execution
		MBSSW3	00: Failed BIST execution
		MBSSW2	00: Failed BIST execution
		MBSSW1	00: Failed BIST execution
		MBSSW0	00: Failed BIST execution
MBELSW	00000000	MBESW26	00: MBIST execution still ongoing
		MBESW25	00: MBIST execution still ongoing
		MBESW24	00: MBIST execution still ongoing
		MBESW23	00: MBIST execution still ongoing
		MBESW22	00: MBIST execution still ongoing
		MBESW21	00: MBIST execution still ongoing
		MBESW20	00: MBIST execution still ongoing
		MBESW19	00: MBIST execution still ongoing
		MBESW18	00: MBIST execution still ongoing
		MBESW17	00: MBIST execution still ongoing
		MBESW16	00: MBIST execution still ongoing
		MBESW15	00: MBIST execution still ongoing
		MBESW14	00: MBIST execution still ongoing
		MBESW13	00: MBIST execution still ongoing
		MBESW12	00: MBIST execution still ongoing
		MBESW11	00: MBIST execution still ongoing
		MBESW10	00: MBIST execution still ongoing
		MBESW9	00: MBIST execution still ongoing
		MBESW8	00: MBIST execution still ongoing
		MBESW7	00: MBIST execution still ongoing
		MBESW6	00: MBIST execution still ongoing
		MBESW5	00: MBIST execution still ongoing
		MBESW4	00: MBIST execution still ongoing
		MBESW3	00: MBIST execution still ongoing
		MBESW2	00: MBIST execution still ongoing
		MBESW1	00: MBIST execution still ongoing
		MBESW0	00: MBIST execution still ongoing
LB0_CTRL	83071107	CSM	01: Concurrent mode
		PTR	03
		PRPGEN	00: Default LBIST value of the PRPG is used during LBIST
		SHS	07: Shift at 1/8 rate (bist_clk)
		SCEN_OFF	01: 1 delay cycle
		SCEN_ON	01: 1 delay cycle
		CWS	07: controller waits 7 shift cycles for capture to finis
LB0_PCS	00000A5A	PCS	00000A5A
LB0_MISRELSW	FFFFFFFF	MISRESWx	FFFFFFFF
LB0_MISREHSW	FFFFFFFF	MISRESWx	FFFFFFFF
LB1_CTRL	82031107	CSM	01: Concurrent mode
		PTR	02
		PRPGEN	00: Default LBIST value of the PRPG is used during LBIST
		SHS	03: Shift at 1/4 rate (bist_clk)
		SCEN_OFF	01: 1 delay cycle
		SCEN_ON	01: 1 delay cycle
		CWS	07: controller waits 7 shift cycles for capture to finis
LB1_PCS	00000540	PCS	00000540
LB1_MISRELSW	FFFFFFFF	MISRESWx	FFFFFFFF
LB1_MISREHSW	FFFFFFFF	MISRESWx	FFFFFFFF
LB2_CTRL	7F031107	CSM	00: Sequential mode

		PTR	7F
		PRPGEN	00: Default LBIST value of the PRPG is used during LBIST
		SHS	03: Shift at 1/4 rate (bist_clk)
		SCEN_OFF	01: 1 delay cycle
		SCEN_ON	01: 1 delay cycle
		CWS	07: controller waits 7 shift cycles for capture to finis
LB2_PCS	00000B54	PCS	00000B54
LB2_MISRELSW	FFFFFFFF	MISRESWx	FFFFFFFF
LB2_MISREHSW	FFFFFFFF	MISRESWx	FFFFFFFF
LB3_CTRL	01031107	CSM	00: Sequential mode
		PTR	01
		PRPGEN	00: Default LBIST value of the PRPG is used during LBIST
		SHS	03: Shift at 1/4 rate (bist_clk)
		SCEN_OFF	01: 1 delay cycle
		SCEN_ON	01: 1 delay cycle
		CWS	07: controller waits 7 shift cycles for capture to finis
LB3_PCS	0000076C	PCS	0000076C
LB3_MISRELSW	FFFFFFFF	MISRESWx	FFFFFFFF
LB3_MISREHSW	FFFFFFFF	MISRESWx	FFFFFFFF
MB0_CTRL	91000000	CSM	01: Concurrent mode
		PTR	11
MB1_CTRL	98000000	CSM	01: Concurrent mode
		PTR	18
MB2_CTRL	93000000	CSM	01: Concurrent mode
		PTR	13
MB3_CTRL	94000000	CSM	01: Concurrent mode
		PTR	14
MB4_CTRL	95000000	CSM	01: Concurrent mode
		PTR	15
MB5_CTRL	96000000	CSM	01: Concurrent mode
		PTR	16
MB6_CTRL	97000000	CSM	01: Concurrent mode
		PTR	17
MB7_CTRL	10000000	CSM	00: Sequential mode
		PTR	10
MB8_CTRL	99000000	CSM	01: Concurrent mode
		PTR	19
MB9_CTRL	9A000000	CSM	01: Concurrent mode
		PTR	1A
MB10_CTRL	9B000000	CSM	01: Concurrent mode
		PTR	1B
MB11_CTRL	9C000000	CSM	01: Concurrent mode
		PTR	1C
MB12_CTRL	9D000000	CSM	01: Concurrent mode
		PTR	1D
MB13_CTRL	9E000000	CSM	01: Concurrent mode
		PTR	1E
MB14_CTRL	9F000000	CSM	01: Concurrent mode
		PTR	1F
MB15_CTRL	A0000000	CSM	01: Concurrent mode
		PTR	20
MB16_CTRL	A1000000	CSM	01: Concurrent mode
		PTR	21
MB17_CTRL	A2000000	CSM	01: Concurrent mode
		PTR	22
MB18_CTRL	A3000000	CSM	01: Concurrent mode
		PTR	23
MB19_CTRL	A4000000	CSM	01: Concurrent mode
		PTR	24
MB20_CTRL	A5000000	CSM	01: Concurrent mode
		PTR	25
MB21_CTRL	A6000000	CSM	01: Concurrent mode
		PTR	26
MB22_CTRL	A7000000	CSM	01: Concurrent mode
		PTR	27

MB23_CTRL	A8000000	CSM	01: Concurrent mode
		PTR	28
MB24_CTRL	A9000000	CSM	01: Concurrent mode
		PTR	29
MB25_CTRL	AA000000	CSM	01: Concurrent mode
		PTR	2A
MB26_CTRL	00000000	CSM	00: Sequential mode
		PTR	00

STM 0 - System Timer Module

SWT 0 - Software Watchdog Timer

WKPU - Wakeup Unit

XBAR 0 - Crossbar Switch

XBIC - Crossbar Integrity Checkers

XOSC - External Oscillator