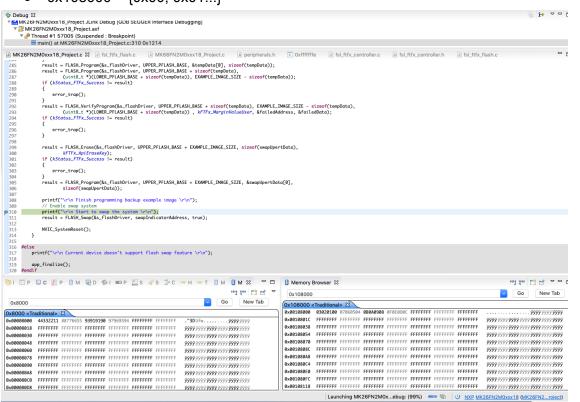
#### This is FIRST SWAP

#### Just before first SWAP

We have (check image below):

- 0x8000 = {0x11, 0x12...}
- 0x108000 = {0x00, 0x01...}



We have (check image below):

- $0x8000 = \{0x00, 0x01...\}$
- $0x108000 = \{0x11, 0x12...\}$

So SWAP was OK, now we are in UPPER

|  | 3<br>FN2M0xxx18_Project JLink Debug [GDB SEGGER Interface De   | ebuggingi  |   |  |   |   | 🦌 i+ 🔻 i   |
|--|--|--|---|--|---|---|--|
| ▼ 2 MK2  | 26FN2M0xxx18_Project.axf<br>[hread #1 57005 (Suspended : Breakpoint)   |  |   |  |   |   |  |
| =  | main() at MK26FN2M0xxx18_Project.c:108 0xea6   |  |   |  |   |   |  |
| MK26FN   | I2M0xxx18_Project.c 22 G fsl_ftfx_flash.c 区 MK66FN2  | M0xxx18_Project.c 🕞 peripherals.h  | C 0xfffffffe  | sl_ftfx_controller.c   | fsl_ftfx_controller.h   | fsl_ftfx_flash.c  | -  |
| 6 <b>{</b><br>7 <b>f</b>   |  |  |   |  |   |   |  |
| S  | <pre>Ftfx_security_state_t securityStatus = kFTFx_SecurityStat status_t result: /* Return code from each flash driver fu</pre>   |  | s */  |  |   |   |  |
|  |  |  |   |  |   |   |  |
|  | <pre>sint32_t pflashTotalSize = 0;<br/>sint32_t pflashBlockCount = 0;</pre>  |  |   |  |   |   |  |
|  | unt32_t pflashSectorSize = 0;  |  |   |  |   |   |  |
|  |  |  |   |  |   |   |  |
|  | /* Init hardware */<br>BOARD InitHardware():   |  |   |  |   |   |  |
| // B   | sownD_intendrowdre();  |  |   |  |   |   |  |
|  | /* Clean up structures*/   |  |   |  |   |   |  |
|  | <pre>nemset(&amp;s_flashDriver, 0, sizeof(flash_config_t));</pre>  |  |   |  |   |   |  |
| 1  | /* Setup flash driver structure for device and initialize  | variables. */  |   |  |   |   |  |
|  | result = FLASH_Init(&s_flashDriver);   |  |   |  |   |   |  |
| í  | if (kStatus_FTFx_Success != result)  |  |   |  |   |   |  |
|  | error_trap();  |  |   |  |   |   |  |
| }  |  |  |   |  |   |   |  |
|  | /* Get flash properties*/<br>FLASH_GetProperty(&s_flashDriver, kFLASH_PropertyPflash07   | atal Size & anflachTotal Size)   |   |  |   |   |  |
| F  | FLASH_GetProperty(&s_flashDriver, kFLASH_PropertyPflash0B  | lockCount, &pflashBlockCount);   |   |  |   |   |  |
|  | FLASH_GetProperty(&s_flashDriver, kFLASH_PropertyPflash0S  | <pre>ectorSize, &amp;pflashSectorSize);</pre>  |   |  |   |   |  |
|  |  |  |   |  |   |   |  |
|  | /* print welcome message */  |  |   |  |   |   |  |
| / p  | printf("\r\n PFlash Swap Example Start \r\n");   |  |   |  |   |   |  |
| L /<br>2 p<br>3 /  | <pre>printf("\r\n PFlash Swap Example Start \r\n"); /* Print flash information - PFlash. */</pre>  |  |   |  |   |   |  |
| 2 p<br>3 /<br>1 p  | printf("\r\n PFlash Swap Example Start \r\n");   | )", (pflashTotalSize / 1024), pflashTo   | talSize);   |  |   |   |  |
| L /<br>2 p<br>3 /<br>1 p<br>5 p  | printf("\n'n Pflash Swap Example Start \n'n");<br>(* Print flash information - Pflash. */<br>printf("\n'n Pflash Information: ");<br>printf("\n'n Total Program Flash Size:\tKd KB, Hex: (0x6x<br>printf("\n'n Total Program Flash Size:\tKd Count:\tKd", pflas  | hBlockCount);  |   |  |   |   |  |
| / p/ p p   | printf("\r\n PFlash Swap Example Start \r\n");<br>** Print flash information - PFlash. */<br>printf("\r\n PFlash Information: ");<br>printf("\r\n Total Program Flash Size:\t%d KB, Hex: (@x%x   | hBlockCount);  |   |  |   |   |  |
| L /<br>P P<br>F P<br>F P<br>F P  | printf("\n'n Pflash Swap Example Start \n'n");<br>(* Print flash information - Pflash. */<br>printf("\n'n Pflash Information: ");<br>printf("\n'n Total Program Flash Size:\tKd KB, Hex: (0x6x<br>printf("\n'n Total Program Flash Size:\tKd Count:\tKd", pflas  | hBlockCount);  |   |  |   |   |  |
| / p<br>/ p<br>p<br>p<br>p<br>p<br>/ r  | printf("\\"\\Priad Supp Escape Start \\"\);<br>M'nint (Tash information - Pflash, \"<br>printf("\\\"\Priad Hadrington;<br>printf("\\"\"\Priad Program Flash Size\'tdd H3, Hex: (BdK<br>printf("\\"\"\Priad Program Flash Size\'tdd H3, Hex: (BdK<br>printf("\\"\"\Priad Program Flash Size\'tdd H3, Hex: (BdK<br>"\"\Printf("\\"\"\Priad Program Flash Size\'tdd H3, Hex: (BdK<br>"\"\Printf("\\"\"\Printf("\\"\"\"\"\"\"\"\"\"\"\"\"\"\"\"\"\"\"  | hBlockCount);<br>x) ", (pflashSectorSize / 1024), pflash   |   |  |   |   |  |
| / p / p p p / r  | printf("\n'\n' PFlash Swip Example Start \n'\);<br>Print Gab information - PFlash. \n'<br>printf("\n'\n' PFlash Enformation: ");<br>printf("\n'\n' Pflash Enformation: "Baba'\dd Ba, Hec: (Belds<br>printf("\n' hold: Program Flash Baba'\dd Ba, Hec: (Belds<br>printf("\n' hold: Program Flash Sector Size:\dd Ba, Hec: (Belds<br>Pflash Sector Size:\dd Ba, Hec: (Beld<br>"Codes security status. \n'<br>result = RASU_GetSecurityState(&R_Baba'\new, Security<br>ft (Satistat_T)Sccress in result)  | hBlockCount);<br>x) ", (pflashSectorSize / 1024), pflash   |   |  |   |   |  |
| / p<br>p<br>p<br>p<br>p<br>r<br>i;   | printf("\n'\n' PFlash Swip Example Start \n'\);<br>Print Gab information - PFlash. \n'<br>printf("\n'\n' PFlash Enformation: ");<br>printf("\n'\n' Pflash Enformation: "Baba'\dd Ba, Hec: (Belds<br>printf("\n' hold: Program Flash Baba'\dd Ba, Hec: (Belds<br>printf("\n' hold: Program Flash Sector Size:\dd Ba, Hec: (Belds<br>Pflash Sector Size:\dd Ba, Hec: (Beld<br>"Codes security status. \n'<br>result = RASU_GetSecurityState(&R_Baba'\new, Security<br>ft (Satistat_T)Sccress in result)  | hBlockCount);<br>≈) ", (pflashSectorSize / 1024), pflash<br>Status);<br>₩ H ₩ T () M () M ⊠ □ □  |   | er X   |   |   | ait for 🔒 🖏 🛆 📾  |
| / P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P   | printf("\\'\\'\Plank isspip Example Start \\'\\');<br>M'rint (Tash information - Flanka.\'<br>printf("\\\') Tash Information; ');<br>printf("\\') Tash Information; ');<br>printf("\\') Tash Program Flank Black Count(\LKF, pflanka,<br>inft("\\\') Tash Program Flank Starts, ');<br>security status. '/<br>male scartly status. '/<br>f (Status_FTR_Success != result);   | hBlockCount);<br>x) °, (pflashSectorSize / 1824), pflash<br>Status);<br>■ H ■ T [] M [] M ⊠ □ □<br>□ g un 📑 😤 ♥  | hSectorSize);   | er ⊠   |   |   | ₩13 1410 <b>[5] 121 ⊽ 12</b><br>Go New Tab   |
| / P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P   | printf("\\'\\'\Plank isspip Example Start \\'\\');<br>M'rint (Tash information - Flanka.\'<br>printf("\\\') Tash Information; ');<br>printf("\\') Tash Information; ');<br>printf("\\') Tash Program Flank Black Count(\LKF, pflanka,<br>inft("\\\') Tash Program Flank Starts, ');<br>security status. '/<br>male scartly status. '/<br>f (Status_FTR_Success != result);   | hBlockCount);<br>≈) ", (pflashSectorSize / 1024), pflash<br>Status);<br>₩ H ₩ T () M () M ⊠ □ □  | hSectorSize);   |  |   |   |  |
| / P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>8000   | printf("\\'\\'\Plank isspip Example Start \\'\\');<br>M'rint (Tash information - Flanka.\'<br>printf("\\\') Tash Information; ');<br>printf("\\') Tash Information; ');<br>printf("\\') Tash Program Flank Black Count(\LKF, pflanka,<br>inft("\\\') Tash Program Flank Starts, ');<br>security status. '/<br>male scartly status. '/<br>f (Status_FTR_Success != result);   | hBlockCount);<br>x) **, (pflashSectorSize / 1824), pflash<br>Status);<br>■ H ■ T [] M [] M ⊠ □ □<br>□ 19 40 [] M [] M ℤ  | Memory Brows     0x108000     0x108000  | tional> 83   | 97969594 <b>FFFFFF</b> FFFFF  |   | Go New Tab   |
| / p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p<br>p  | printf("\\'\\'\\ PFlank issip Example Start \\'\\');<br># Print flash information = PFlash, \\'<br>printf("\\'\\ PFlank information; ');<br>printf("\\'\\ PFlank information; ');<br>printf("\\'\\ Program Flash Mexic Cauchitike", pflas<br>printf("\\'\ Program Flash Sector Size:\\'tid K8, Hex: (BMG<br>"\) Deck security Status. \\<br># C ENL = FLAS(ExecurityStateEx8, FlashOriver, & security<br># (Status_fFR_Garcess in = result)<br>E C ENL = M EQ D & I => P ENL \$ \$ \$ \$ C<br>Executionals E<br>Execution \$ C<br>Execution \$ C | hBlockCount);<br>x) **, (pflashSectorSize / 1824), pflash<br>Status);<br>■ H ■ T [] M [] M ⊠ □ □<br>□ 19 40 [] M [] M ℤ  | Memory Brows     0x108000     0x108000     0x108000 <tradi 0x8018881c="" 0x808800="" 4433="" ffff<="" td=""><td>tional&gt; X<br/>2211 88776655 93919190<br/>FFFF FFFFFFF FFFFFFF</td><td>FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF</td><td>FF FFFFFFF <b>ÿÿÿÿ</b>ÿÿÿÿ</td><td>Go New Tab</td></tradi>  | tional> X<br>2211 88776655 93919190<br>FFFF FFFFFFF FFFFFFF                              | FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF  | FF FFFFFFF <b>ÿÿÿÿ</b> ÿÿÿÿ   | Go New Tab   |
| / P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P  | printf("\\'\\'\\ PFlank issip Example Start \\'\\');<br>Printf ("Ani frame information - Pflank, \\'<br>printf("\\'\\ \\ Pflank Information - Pflank, \\'<br>printf("\\'\\ \\ Pflank Information - Pflank Example Start, (BdK<br>- pflank), \\ Pflank Information - Start, \\ Md BB, Hex: (BdK<br>- pflank), \\ Pflank Information - Start, \\ Md BB, Hex: (BdK<br>- Occk security Status, \'<br>result = FLASI, GetSecurity State(Ss, flashOriver, & Security<br>f (Status, JTK, Scccess In result),<br>compared to a start, \\ Sta   | islicicant);<br>w '', (plasisectorSize / 1824), pflasi<br>Status);<br>m H m T [ M [] M ⊠<br>m 2 m [] m [] m ]<br>m 2 m [] m ]<br>m 3 m [] m ]<br>m 3 m | Memory Brows     0x108000     0x108000 <tradi 0x0188000="" 0x0188038="" <fff="" <tradi="" ffff<="" td=""><td>tional&gt; 83<br/>12211 88776655 93919190<br/>FFFF FFFFFFFF FFFFFFFF<br/>FFFFF FFFFFFFF FFFFFF</td><td>FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF</td><td>FF         FFFFFFFF         ÿÿÿÿ yyyy           FF         FFFFFFFF         ÿÿÿÿ yyyy</td><td>Go New Tab<br/>99999 9999 9999 9999 9999 9999 9999</td></tradi>  | tional> 83<br>12211 88776655 93919190<br>FFFF FFFFFFFF FFFFFFFF<br>FFFFF FFFFFFFF FFFFFF | FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF  | FF         FFFFFFFF         ÿÿÿÿ yyyy           FF         FFFFFFFF         ÿÿÿÿ yyyy   | Go New Tab<br>99999 9999 9999 9999 9999 9999 9999  |
| P <p< td=""><td>print("\\'\\'\\ PTilah Sup Lample Start \\'\\);<br/>Print("Lah 'Information - PTilah.\'<br/>print("\\'\\ PTilah Information '];<br/>print("\\'\ PTilah Information');<br/>print("\\'\ Program Flash Sector Size:\\'dd KB, Hex: (Bed<br/>print("\\'\ Program Flash Sector Size:\\'dd KB, Hex: (Bed<br/>print("\\ Program Flash Sector Size:\\'dd KB, Hex: (Bed<br/>print("\\ Program Flash Sector Size:\\'dd KB, Hex: (Bed<br/>Bed<br/>Bed Size: Bed Size: Bed Size:\\ Dd Size:\\ Dd</td><td>MillosiCount);       **       (#10456ctor5ize / 1824), pflasi         Status);       **       (#10456ctor5ize / 1824), pflasi         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ****       ***       ****         ****       ****       ****         ****       ****       *****         ****       ****       *****         ****       *****       ******         ****       *****       *******         *****       ********       ***********         *****       ************************************</td><td>Memory Brows           0x108000           0x108000           0x0108000           0x0108000           0x0108000           0x0108000</td><td>tional&gt; 83<br/>12211 88776655 93919190<br/>1FFF FFFFFFF FFFFFFFF<br/>FFFF FFFFFFFF FFFFFF</td><td>FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF</td><td>FF         FFFFFFFF         SYSY         &lt;</td><td>Go New Tab<br/>9999 9999 9999 9999 9999<br/>9999 9999 9</td></p<> | print("\\'\\'\\ PTilah Sup Lample Start \\'\\);<br>Print("Lah 'Information - PTilah.\'<br>print("\\'\\ PTilah Information '];<br>print("\\'\ PTilah Information');<br>print("\\'\ Program Flash Sector Size:\\'dd KB, Hex: (Bed<br>print("\\'\ Program Flash Sector Size:\\'dd KB, Hex: (Bed<br>print("\\ Program Flash Sector Size:\\'dd KB, Hex: (Bed<br>print("\\ Program Flash Sector Size:\\'dd KB, Hex: (Bed<br>Bed<br>Bed Size: Bed Size: Bed Size:\\ Dd   | MillosiCount);       **       (#10456ctor5ize / 1824), pflasi         Status);       **       (#10456ctor5ize / 1824), pflasi         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ***       ***       ***         ****       ***       ****         ****       ****       ****         ****       ****       *****         ****       ****       *****         ****       *****       ******         ****       *****       *******         *****       ********       ***********         *****       ************************************  | Memory Brows           0x108000           0x108000           0x0108000           0x0108000           0x0108000           0x0108000  | tional> 83<br>12211 88776655 93919190<br>1FFF FFFFFFF FFFFFFFF<br>FFFF FFFFFFFF FFFFFF   | FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF  | FF         FFFFFFFF         SYSY         < | Go New Tab<br>9999 9999 9999 9999 9999<br>9999 9999 9  |
| / P<br>P P<br>P P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P  | printf("\\'\\'\\ PFlank issip Example Start \\'\\');<br>Printf Clash information - Pflank .\'<br>printf ("\\'\\ \\ Pflank Information - Pflank .\'<br>printf ("\\'\\ \\ Pflank Information - Pflank ExactVed VB, Herc: (BekS<br>printf ("\\'\\ Pflank Information - Black\Ved VB, Herc: (BekS<br>printf ("\\'\\ Pflank Information - Black\Ved VB, Herc: (BekS<br>printf ("\\'\\ Pflank Information - Start Ved VB, Herc: (BekS  | islicicant);<br>w H m T M O M ⊠  | Memory Brows     (x108000     (x108000     (x108000     (x040800     (x040800     (x040808     (x10800     (x040808     (x1080     (x040808     (x1080     (x108 | tional> 8<br>2211 88776655 93919190<br>FFFF FFFFFFFF FFFFFFFFFFFFFFFFFF<br>FFFF FFFFFF   | FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF  | FF         FFFFFFFF         SSSS SSSS           FF         FFFFFFFF         SSSS SSSS           FF         FFFFFFFF         SSSS SSSS           FF         FFFFFFFFF         SSSS SSSS           FF         FFFFFFFFF         SSSS SSSS   | Go New Tab<br>9999 9999 9999 9999<br>9999 9999 9999 9  |
| / P  | print("\\'\\'\\ PFlank isob taxeple Start \\'\\);<br># Print (Bak information - Pflank.\'<br>print("\\'\\ PFlank information ');<br>print("\\'\\ Pflank information ');<br>print("\\'\\ Program Flank Sector Size:\\'dd KB, Hex: (Beds<br>print("\\'\\ Program Flank Sector Size:\\'dd KB, Hex: (Beds<br>f \dd States_TFR_Sectors in result)<br>C C P P () M (QD ()) = P [] S % S \$*C<br>Beds20100 0706354 68640966 0766000; FFFFFFFF FFFFFFF<br>FFFFFFF FFFFFFFF FFFFFFFF  | MillosiCount);       *, "(pflaskietorSize / 1824), pflaski         Status);       ************************************   | Memory Brows           0x108000           0x108000           0x008080           0x08080           0x080800           0x0808000           0x0808000           0x08080000           0x08080000000           0x080800000000000000000000000000000000   | tional> 83<br>22211 88776655 93919190<br>FFFF FFFFFFFF FFFFFFFF<br>FFFF FFFFFFFFFF       | FREEFEF FFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF   | FF         FFFFFFFF         30303 30300           FF         FFFFFFFF         30303 30300         30300   | Go New Tab<br>9999 9999 9999 9999<br>9999 9999 9999 9  |
| ✓ P  | print("\\`\\`\\`\`PTilah isop Loople Start \\`\\`);<br>Print ("Lah information - PTilah.\`<br>print("\\\`\`\`PTilah Information 'Dil<br>print("\\\`\`PTilah Information 'Dil<br>print("\\\``Ptilah Information 'Dil<br>print("\\`\`Ptilah Information 'Dil<br>print("\\`\`Ptilah Information 'Dil<br>print("\\`\`Ptilah Information 'Dil<br>print("\\`\`Ptilah Information 'Dil<br>print("\\`\`Ptilah Information 'Dil<br>print("\\`\'Ptilah Information 'Dil<br>print("\\`Ptilah Information 'Dil<br>print("\\Ptilah Information 'Dil<br>print("\Ptilah Information 'Dil<br>print("\Ptilah Information 'Dil<br>print("\Ptilah Information 'Dil<br>print("\Ptilah Information 'Dil<br>print("\Ptilah Information 'Dil<br>print("\Ptilah Information 'Dilah Information 'Dil<br>print("\Ptilah Information 'Dil   | المَالْمَالَمَالَةُ اللَّهُ ال<br>Status);<br>اللَّهُ اللَّهُ اللَّ<br>اللَّهُ اللَّهُ اللَّهُ اللَّهُ اللَّهُ الللَّهُ اللَّهُ اللَّهُ اللَّهُ اللَّهُ اللَّهُ اللَّهُ اللَّهُ اللَّ<br>اللَّهُ اللَّهُ اللَّالِيلَ<br>اللَّالِيلَةُ اللَّهُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّ<br>اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلِيلَةُ اللَّالِيلِيلَةُ اللَّالِيلِيلَةُ اللَّالِيلَةُ اللَّالِيلِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّلَّالِيلَةُ اللَّالِيلَةُ اللَّلُولِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلِيلَةُ اللَّالِيلَةُ اللَّالِيلِيلِيلَةُ اللَّالِيلَةُ اللَّالِيلَةُ اللَّالِيلِيلُ  | Memory Brows     0x108000     0x108000     0x108000     0x108000 4Trad     web18802 FFFF     web18802 FFFF     web18887 FFFF     web18887 FFFF     web18887 FFFF     web18888 FFFF  | tional» 23<br>22211 88776655 93919190<br>FFFF FFFFFFFF FFFFFFF<br>FFFF FFFFFFFF FFFFFF   | FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFFFF  | FFFFFFFF         9999 9999           FF         FFFFFFF         9999 9999           FF         FFFFFFFF         9999 9999   | Go         New Tab           9999 9999 9999 9999 9999 9999 9999 99   |
| ✓ P  | print("\^\\r\Plank isop Example Start \/\\);<br>Print("Lah 'Information - Plank\/<br>Print("Lah 'Information ');<br>print("\/\\Plank Information ');<br>print("L\\Plank Information ');<br>print("L\Plank Information ');   | MillosiCount);       *, (pflaskiestorSize / 1824), pflaski         Status);       ************************************   | Memory Brows     0x108000     (0x108000     (0x10800     (0x1080     (0x1080     (0x1080     (0x10800     (0x10800     (0x1080     (0x108     (0x1080     (0x1080     (0x1080     | tional> &<br>2211 88776655 93919190<br>FFFF FFFFFFFF FFFFFFFF<br>FFFF FFFFFFFFFF         | FREEFEF FFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF   | FF         FFFFFFF         SYSY SYSY           FF         FFFFFFF         SYSY SYSY           FF         FFFFFFF         SYSY SYSY           FF         FFFFFFFF         SYSY SYSY  | Ga         New Tab           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999 |
| L / P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P<br>P   | print("\\`\\`\\`\`PTilah Sup Law]e Start \\`\\`);<br>Print("\\.`\\`\`PTilah Sup Law]e Start \\`\\`);<br>Print("\\`\`\`\`PTilah Information - "Jilah Sup Law]e<br>print("\\`\`\`Ptilah Information 'Jilah Sub ("Auto: (Bokk<br>"ritt("\\`\`Ptilah Compan Flah Societ Size:\\`L\\`R. (Bok<br>print("\\`\`\`Ptilah Compan Flah Societ Size:\\`L\\`R. (Bok<br>"C C C P N M C D O I D P S S ~ S C<br>C C P N M C D O I D P S S ~ S C<br>C C P N M C D O I D P S S ~ S C<br>C C C P P M C D O I D P S S ~ S C<br>C C C P P M C D O I D P S S ~ S C<br>C C C P P M C D O I D P S S ~ S C<br>C C C P P M C D O I D P S S ~ S C<br>C C C P P M C D O I D P S S C S C<br>C C C P P M C D O I D P S S C S C<br>C C C P P M C D O C S C C C C C C C C C C C C C C C C C  | المَال مَعْلَمَ مَعْلَم اللَّهُ مَعْلَمَ اللَّهُ مَعْلَمَ اللَّهُ مَعْلَمُ مَعْلَمُ مَعْلَمُ اللَّهُ مَعْلَم ال<br>Status);<br>الله الله الله الله الله الله الله الله   | Memory Brows     0x108000     0x10800     0x10     0x10800     0x1080     0x10800     0x1080     0x10800     0x1080     0x10800     0x1080     0x10     0x1080     0x10     0x1080     0x1080     0x1080     0x10     0x1080     0x1080 | tional> 23<br>22211 88776655 93919190<br>FFFF FFFFFFF FFFFFFF FFFFFFF<br>FFFF FFFFFF     | EFFEFFFF         FFFFFFFF         FFFFFFFF           FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFF           FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF           FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF           FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF | FF         FFFFFFF         SOUND STATE           FF         FFFFFFF         SOUND STATE         SOUND STATE           FF         FFFFFFF         SOUND STATE         STATE           FF         FFFFFFFF         SOUND STATE         STATE  | Go         New Tab           9999 1001 5999 1001 5999         5999 1001 5999           9999 1001 5999 1001 5999         5999 1001 5999           9999 1001 5999 1001 5999         5999 1001 5999           9999 1001 5999 1001 5999         5999 1001 5999           9999 1001 5999 1001 5999         5999 1001 5999           9999 1001 5999 1001 5999         5999 1001 5999           9999 1001 5999 1001 5999         5999 1001 5999           9999 1001 5999 1001 5999         5999 1001 5999   |
| L ( )<br>P<br>P<br>P<br>P<br>C<br>C<br>C<br>C<br>C<br>C<br>C<br>C<br>C<br>C<br>C<br>C<br>C   | print("\\`\\`\\`\`PTilah Sup Law]e Start \\`\\`);<br>Print("\\.`\\`\`PTilah Sup Law]e Start \\`\\`);<br>Print("\\`\`\`\`PTilah Information - "Jilah Sub\'tild BB, Hoc: (Boks<br>print("\\`\`\`Ptilah Information - BB.sch\'tild BB, Hoc: (Boks<br>print("\\`\`\`Ptilah Program Flash Sector Size:\\'Lil BB, Hoc: (Boks<br>print("\\`\`\`Ptilah Sector Size:\\'Lil BB, Hoc: (Boks<br>print("\\`\`\`Ptilah Sector Size:\\'Lil BB, Hoc: (Boks<br>"C C P P M M D D I Im P I S ~ S - C<br>Baselionals E<br>Baselionals E<br>Baselionals E<br>Baselionals E<br>Baselionals C Print(" Print("Print(" Print(" Prin(" Prin(" Print(" Print(" Print(" Prin(" Print(" Prin(" Pri  | MillosiCount);       *, (pflaskiestorSize / 1824), pflaski         Status);       ************************************   | Memory Brows     0x108000     10x108000     10x0000     10x0000     10x0000     10x0000     10x0000     10x0000     10x0000     10x000     10x000     10x00     10x0     10x00     10 | tional> (2)<br>2211 8277655 93919190<br>FFFF FFFFFFF FFFFFFF<br>FFFF FFFFFFF FFFFFF      |   | FF         FFFFFFF         9999 3939           FF         FFFFFFF         9999 3939           FF         FFFFFFFF         9999 3939           FF         FFFFFFF         9999 3939           FF         FFFFFFF         9999 3939           FF         FFFFFFF         9999 3939  | Ga         New Tab           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999         9999 0000 9999           9999 0000 9999 0000 9999 0000 9999         9999 0000 9999   |

## This is SECOND SWAP

1) Write 16 values at the 0x8000

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| Debug S   | 3  |   |  |   |   |   | ie 🗸 🗧  |
| MK 26   | *<br>hv2MUxxx18_Project JLink Debug [GDB SEGGER Interface Dr<br>26FN2MUxxx18_Project.axf<br>Ihread #1 57005 (Suspended : Step)<br>= main() at MK26FN2M0xxx18_Project.c:270 0x1148  | ebuggingj   |  |   |   |   |   |
| MK26FN  | 12M0xxx18 Project.c 🕱 🗟 fsl ftfx flash.c 🛛 MK66FN2   | M0xxx18 Project.c   | peripherals.h  | fsl ftfx controller.c   | A fsl ftfx controller.h   | fsl ftfx flash.c  | main() at MK26FN2M0xxx1   |
| 46  | error_trap();  |   |  |   |   |   |   |
| 47  | break;   |   |  |   |   |   |   |
| 48  | }  |   |  |   |   |   |   |
| 49<br>50  | /* Check if the original indicator address is valid *  | /   |  |   |   |   |   |
| 51  | if (returnSwapInfo.flashSwapState != kFTFx_SwapStateU  |   |  |   |   |   |   |
| 52  | /* if (flashSwapIfrField.swapIndicatorAddress !=   | (uint16_t)(~0)) */  |  |   |   |   |   |
| 53<br>54  | {<br>if ((swapIndicatorAddress < EXAMPLE IMAGE SIZE)   | I Countralization to date   |  | BACENN  |   |   |   |
| 55  | ((swopinateatorwooress < EXAMPLE_IMAGE_SIZE)   | (swapinatcacornaares  | S >= OFFER_FFEASE  | DASE))  |   |   |   |
| 56  | printf("\r\n Original swap indicator address   | couldn't be used for t  | this example \r\n")  | );  |   |   |   |
| 57  | printf(  |   |  | and the state of the   |   |   |   |
| !58<br>!59  | <pre>"\r\n Swap system needs to be cleared<br/>"\r\n");</pre>  | back to uninitialized   | d, This example car  | not clear swap system '   |   |   |   |
| 260   | app_finalize();  |   |  |   |   |   |   |
| 61  | }  |   |  |   |   |   |   |
| 62  | }  |   |  |   |   |   |   |
| 63<br>64  | result = FLASH_Erase(&s_flashDriver, pflashBaseAddr +  | EXAMPLE THAGE STZE  | izeof(swap]owerDat   | (n)   |   |   |   |
| 65  | kFTFx_ApiEraseKey);  |   |  |   |   |   |   |
| 66  | result = FLASH_Program(&s_flashDriver, pflashBaseAddr  | + EXAMPLE_IMAGE_SIZE,   | &swaplowerData[0]  | <pre>, sizeof(swaplowerData)</pre>  | ));   |   |   |
| 267<br>268  | result = FLASH_VerifyProgram( &s_flashDriver, pflashB  | grander + EXAMPLE THAC  | E STZE sizeof(rm   | anlamanData) framalama  | nData[9]  |   |   |
| 269   | kFTFx_MarginValueUser, &failedAddress, &faile  |   | all areas areas ( and  | apromer burca), aunopromer  | buculoj,  |   |   |
|   | if (kStatus_FTFx_Success != result)  |   |  |   |   |   |   |
|   |  |   |  |   |   |   |   |
| 271   | {  |   |  |   |   |   |   |
| 271 272   |  |   |  |   |   |   |   |
| 271<br>272<br>273<br>274  | {<br>error_trap();<br>}  |   |  |   |   |   |   |
| 271<br>272<br>273<br>274<br>275   | {     error_trap();     }     printf("\r\n Backup example image is invalid \r\n");   |   |  |   |   |   |   |
| 271<br>272<br>273<br>274<br>275<br>276  | {     error_trap(); } printf("\r\n Backup example image is invalid \r\n"); printf("\r\n Start to program backup example image \r   | \n");   |  |   |   |   |   |
| 271<br>272<br>273<br>274<br>275<br>276<br>277   | {     error_trap();     }     printf("\r\n Backup example image is invalid \r\n");   |   | :a));  |   |   |   |   |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279   | <pre>{     error_trap(); } printf("\n &amp; dodug assayls image is invalid \n'n"); printf("\n &amp; dodug assayls image is invalid \n'n"); printf("\n &amp; dodug assayls image \n'n"); printf("\n &amp; dodug assayls   long = \n'n"); memory(dink1, *)(Republic); memory(din</pre> | seAddr, sizeof(tempDat  |  |   |   |   |   |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280  | <pre>} error_trap(); } printf("\u00f3\n Backup example image is invalid \u00f3\u00f3 printf("\u00f3\n Backup example image \u00f3\u0</pre>                 | seAddr, sizeof(tempDat  |  | ð;  |   |   |   |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280<br>281   | <pre>{     error_trap();     printf("\n Backup example image is invalid \n'n");     printf("\n'n Start to program backup example image 'n     prints/backudd=. URLEF/ISUBLess;     memory(const.t. 'n)(kimpdate(0)); (cinit.t. 'n)(kimpdate(0));     remails - ERBEDFERG(A. Inabhress;     remails - ERBEDFERG(A. Inabhress;     f(isitetus_FTP_success != result) </pre>  | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,  | , kFTFx_ApiEraseKey  |   |   |   |   |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280<br>281   | <pre>} error_trap(); } printf("\u00f3\n Backup example image is invalid \u00f3\u00f3 printf("\u00f3\n Backup example image \u00f3\u0</pre>                 | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,  | , kFTFx_ApiEraseKey  | ට ;<br>[] Memory Browser ස  |   |   | nd fa 🖬 🖓 🛆   |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280<br>281   | <pre>{     error_trap();     printf("\n Backup example image is invalid \n'n");     printf("\n'n Start to program backup example image 'n     prints/backudd=. URLEF/ISUBLess;     memory(const.t. 'n)(kimpdate(0)); (cinit.t. 'n)(kimpdate(0));     remails - ERBEDFERG(A. Inabhress;     remails - ERBEDFERG(A. Inabhress;     f(isitetus_FTP_success != result) </pre>  | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,<br>MM: H MM: T () M ()   | kFTFx_ApiEraseKey<br>M 23  |   |   |   | ing teri 디 명 후 ·<br>•<br>• Go New Tab   |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280<br>281   | <pre>{     error_trap();     printf("\n Backup example image is invalid \n'n");     printf("\n'n Start to program backup example image 'n     prints/backudd=. URLEF/ISUBLess;     memory(const.t. 'n)(kimpdate(0)); (cinit.t. 'n)(kimpdate(0));     remails - ERBEDFERG(A. Inabhress;     remails - ERBEDFERG(A. Inabhress;     f(isitetus_FTP_success != result) </pre>  | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,  | , kFTFx ApiEraseKey<br>M 12  | Memory Browser      Ox108000     Ox108000 <traditionals< td=""><td></td><td></td><td>Go New Tab</td></traditionals<>  |   |   | Go New Tab  |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>277<br>279<br>280<br>281<br>31<br>31   | <pre>{     error_trap();     printf("\n Backup example image is invalid \n'n");     printf("\n'n Start to program backup example image 'n     prints/backudd=. URLEF/ISUBLess;     memory(const.t. 'n)(kimpdate(0)); (cinit.t. 'n)(kimpdate(0));     remails - ERBEDFERG(A. Inabhress;     remails - ERBEDFERG(A. Inabhress;     f(isitetus_FTP_success != result) </pre>  | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,<br>MM: H MM: T () M ()   | , kFTFx ApiEraseKey<br>M 12  | Memory Browser         33           0x108000         0x108000            0x108000          44332211           0x09188900 44332211         44332211  | 88776655 <b>93919190</b> 9796959  |   | Go New Tab  |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280<br>281<br>211 P<br>0x8000<br><br>(8000<br>The second s   | <pre>{     error_trap();     print("\"h Bickup example image is invalid \\\\");     print("\"h Bickup example image is invalid \\\");     print("\\ Bickup example image is invalid \\\");     print("\\ Bickup example image is invalid \\\");     (init, i');     framework();     (init, i');     (init, i</pre>                  | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,<br>MM: H MM: T () M ()   | , kFTFx_ApiEraseKey<br>M 12 □<br>1 1000 □ 11 11 11 11 11 11 11 11 11 11 11 11 1  | Memory Browser II           0x108000           0x108000            0x108000            0x808800 44332211           0x8018801C FFFFFFFF  | 88776655 <b>93919190</b> 9796955<br>FFFFFFFF <b>FFFFFF</b> FFFFFF   | F FFFFFFFF FFFFFFFF FFFF  | Go New Tab  |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>280<br>281<br>0<br>1  | {     error_trap();     printf("\\ Backup example image is invalid \\\\\");     printf("\\ Backup example image is invalid \\\\");     printf("\\ Backup example image \\                            | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,<br>H H H T () M ()<br>Go<br>Go   | kFTFx_ApiEroseKey<br>M 83  | Memory Browser 23           0x108000           0x108000            0x108000            0x00108000 44332211           0x00108000 44332211           0x00108000 84332211           0x00108000 84332211           0x00108000 84332211           0x00108000 875676767   | 88776655 <b>93919190</b> 9796955<br>FFFFFFFF <b>FFFFFFF</b> FFFFFFF<br>FFFFFFF <b>FFFFFFF</b> FFFFFFF   | F FFFFFFFF FFFFFFFF FFFF  | Go New Tab     Go New Tab     FFFFF   |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280<br>1 	 P<br>0x8000 < The<br>constant of the second  | <pre>{     error_trap();     printf("\u03c4 backup example image is invalid \u03c4\u03c4);     printf("\u03c4 is program backup example image '\u03c4 image</pre>                  | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,<br>INF H INF T () M ()<br>Go<br>."30Ufw  | kFTFx_ApiEraseKey<br>M 22 □<br>1 4 <sup>10</sup> S S V<br>New Tab<br>19 99999  | Memory Browser 23           0x108000           0x108000           0x00108000           0x00108000           0x00108000           0x00108000           0x00108001           0x00108001           0x00108003           0x00108003           0x00108003           0x00108005           0x00108005  | 88776655 <b>93919190</b> 9796955<br>FFFFFFFF <b>FFFFFFF</b> FFFFFFF<br>FFFFFFFF <b>FFFFFFF</b> FFFFFFF<br>FFFFFFFF <b>FFFFFFF</b> FFFFFFF   | F FFFFFFFF FFFFFFFF FFFF<br>F FFFFFFFF FFFFFF   | Go New Tab  |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280<br>281<br>0 x8000 < The<br>colorest of the second   | <pre>{     error_trap();     printf("\\ Backup example image is invalid \\\\");     printf("\\ Backup example image is invalid \\\");     printf("\\ Backup example image \\     printf("\\ Backup example image \\</pre>                   | seAddr, sizeof(terpDat<br>E, EXAMPLE_DMAGE_SIZE,<br>E, ET I T M G<br>Go<br>"3DUFw   | kFTFx_ApiEraseKey           M         Image: Imag | Memory Browser 33           0x108000           0x108000           0x0008000           0x00180800           0x00180801           0x00180801           0x00180803           0x001808034           0x001808054           0x001808074   | 88776655 <b>93919190</b> 9796955<br>FFFFFFFFFFF <b>FFFFFF</b> FFFFFFFF<br>FFFFFFFF <b>FFFFFFF</b> FFFFFFFF  | F FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFF  | Go New Tab  |
| 271<br>272<br>273<br>274<br>275<br>276<br>277<br>278<br>279<br>280<br>280<br>281<br>0x8000<br>x8000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x80000<br>x8000000<br>x80000<br>x800000000   | <pre>{     error_trap();     printf("\u03c6 lockup example induce is invalid \u03c6 \u03c6 \u03c6 lockup example induce example induce example induce example induce example induce induce \u03c6 \u0</pre>                  | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,<br>  | kFTFx_ApiEraseKey           M 83           1 4***           1 4***           1 5***           New Tab           1000000           1000000           1000000           1000000           1000000           1000000           1000000           1000000           1000000           1000000           1000000           10000000           100000000           1000000000           100000000000           10000000000000           1000000000000000000000000000000000000  | Memory Browser         Xi           0x108000         Crraditionals           0x0108000         4332211           0x0188006         4332211           0x00188016         FFFFFFF           0x00188054         FFFFFFF           0x00188054         FFFFFFF           0x00188054         FFFFFFF           0x00188054         FFFFFFF   | 88776655         33919190         9796955           FFFFFFFF         FFFFFFFF         FFFFFFF           FFFFFFFF         FFFFFFFF         FFFFFFF           FFFFFFFF         FFFFFFFF         FFFFFFF           FFFFFFFF         FFFFFFFF         FFFFFFF           FFFFFFFF         FFFFFFF         FFFFFFF  | T FFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF  | Co     New Tab     So     FFFF     ''30.0fm     ''30 |
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| 271<br>272<br>273<br>274<br>275<br>275<br>275<br>279<br>280<br>279<br>280<br>279<br>280<br>279<br>280<br>279<br>280<br>279<br>280<br>279<br>280<br>280<br>279<br>280<br>280<br>279<br>280<br>280<br>280<br>280<br>280<br>280<br>280<br>280  | {     error_trag();     print("\'\\ Bockup example image is invalid \\'\'\');     print("\'\ Bockup example image is invalid \\'\'\');     print("\'\ Bockup example image \\'     print("\\') Source \\') Source \\'     print("\\') Source \\') Source \\'     print(                        | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,<br>IIII H IIII T MGE_SIZE,<br>IIIII G MGE<br>IIIII MGE_SIZE,<br>IIIII MGE_SIZE,<br>IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII | kFTFx_ApiEraseKey           M 32           g train 22           M 32           B train 22           M 32  | Memory Browser 32           0x108000           0x108000           0x000800           0x0018080           0x00180801           0x00188030   | 88776655         9391930         9796955           FFFFFFFF         FFFFFFF         FFFFFFF           FFFFFFF         FFFFFFF         FFFFFFF | T FFFFFFFF FFFFFFFFFFFFFFFFFFFFFFFFFFF  | Go New Tab     Go New Tab     Total (m  |
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| 0x8000  | <pre>{     error_trap();     }     printf("\v'n Backup example image is invalid \v'n");     printf("\v'n Sackup example image is invalid \v'n");     printf("\v'n Sackup example image is invalid image inv primt() (image inv) (image image inv v'n Sack is program the backup image 'v     reall t = HBUT() (image image inv v'n Sack is program the backup image 'v     reall t = HBUT() (image image im</pre>                  | seAddr, sizeof(tempDat<br>E, EXAMPLE_IMAGE_SIZE,<br>H H H T O M O<br>   | kFTx, ApiEraseKey           M 32           g um 32   | Memory Browser 32           0x108000           0x108000           0x108000           0x0018000           0x0018080           0x0018080           0x0018080           0x0018080           0x0018080  | 88776655         9391930         9796955           FFFFFFFF         FFFFFFF         FFFFFFF           FFFFFFF         FFFFFFF         FFFFFFF | P. SPERFER         CONTRACT         SPERFER   | Go New Tab  |

# 2) Write 16 values at 0x10\_8000

|  |  | 10 to 1  |
|--|--|--|
| : Debug 🕅<br>RMK26FN2M0xxx18_Project JLink Debug [GDB SEGGER Interface Debugging]  |  | ······································   |
| T RMK26FN2M0xxx18_Project.axf  |  |  |
| Thread #1 57005 (Suspended : Breakpoint)   |  |  |
| main() at MK26FN2M0xxx18_Project.c:308 0x120e  |  |  |
| MK26FN2M0xxx18_Project.c 12 C fsl_ftfx_flash.c C MK66FN2M0xxx18_Project.c C peripherals.h  | i 🖻 fsl_ftfx_controller.c 🗈 fsl_ftfx_controller.h 🗟 fsl_ftfx_flash.c 💽 main() at MK26FN2   | M0xxx1 🖷 🕻   |
| <pre>result = FLASH_Program(&amp;s_flashDriver, UPPER_PFLASH_BASE, &amp;tempData[0], sizeof(tempData));</pre>  |  |  |
| <pre>86 result = FLASH_Program(&amp;s_flashDriver, UPPER_PFLASH_BASE + sizeof(tempData), 87 (uint8_t *)(LONER_PFLASH_BASE + sizeof(tempData)), EXAMPLE_IMAGE_SIZE - sizeof(temp</pre>  | n04ta))-   |  |
| <pre>88 if (kStatus_FTFx_Success != result)</pre>  |  |  |
| 0 {<br>error_trap();   |  |  |
| 10 error_crop(),   |  |  |
| <pre>92 result = FLASH_VerifyProgram(&amp;s_flashDriver, UPPER_PFLASH_BASE + sizeof(tempData), EXAMPLE_</pre>  |  |  |
| 13 (uint8_t *)(LOWER_PFLASH_BASE + sizeof(tempData)), kFTFx_MarginValueUser, &failedA<br>14 if (kStatus_FTFx_Success != result)  | Address, &failedData);   |  |
| 35 {   |  |  |
| 96 error_trap();<br>97 }   |  |  |
| 7 }  |  |  |
| 99 result = FLASH_Erase(&s_flashDriver, UPPER_PFLASH_BASE + EXAMPLE_IMAGE_SIZE, sizeof(swapUpe   | ertData),  |  |
| <pre>30 kFTFx_ApiEraseKey);<br/>31 if (kStatus_FTFx_Success != result)</pre>   |  |  |
| 12 {   |  |  |
| <pre>8 error_trap();</pre>   |  |  |
| H }<br>result = FLASH_Program(&s_flashDriver, UPPER_PFLASH_BASE + EXAMPLE_IMAGE_SIZE, &swapUpertDa   | ata[0].  |  |
| 6 sizeof(swapUpertData));  |  |  |
| <pre>printf("\r\n Finish programming backup example image \r\n");</pre>  |  |  |
| 89 // Enable swap system   |  |  |
| 10 printf("\r\n Start to swap the system \r\n");<br>11 result = FLASH_Swap(&s_flashDriver, swapIndicatorAddress, true);  |  |  |
| 11 result = FLASM_SWap(&S_rlashuriver, swapinalcatorAddress, true);<br>12  |  |  |
| 13 WVIC_SystemReset();   |  |  |
| 14 }<br>15   |  |  |
| 16 #else   |  |  |
| <pre>printf("\r\n Current device doesn't support flash swap feature \r\n"); 18</pre>   |  |  |
| app_finalize();  |  |  |
| 20 #endif<br>I □ P □ C 第 P ∩ M ℝD ŚNI #D P ⊡ S 🛷 S 违 C ##H ##T ∩ M ∩ M ⊠ □ □   | Memory Browser 52  |  |
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| NOORAAL IFFFFFFF FITTIFFF FFFFFFF FFFFFFFFFFFF   | 0x001088A8 FFFFFFF FFFFFFF FFFFFFFF FFFFFFFF FFFFF   | 999 <b>9999</b> 9999 <b>9999</b>   |
| BOORDAAL         FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFF         FFFFFFF         FFFFFFF         FFFFFFFF         FFFFFFF         FFFFFF         FFFFFF         FFFFFFF         FFFFFFF         FFFFFFF         FFFFFFF         FFFFFFF         FFFFFFF         FFFFFFF         FFFFFFF         FFFFFFF         FFFFFFFF         FFFFFFF         FFFFFFF   | 0x001080A8 FFFFFFF FFFFFFF FFFFFFFF FFFFFFFF FFFFF   | 999 <b>9999</b> 9999 <b>9999</b><br>999 <b>9999</b> 9999 <b>9999</b>                                     |
| NANDALA         FIFTHEFF FIFTHEFFF FIFTHEFFF FIFTHEFFF FIFTHEFFF         SUMMAND FIFTHEFFFF              | 0.001380.02         FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF         S999999999999999999999999999999999999 | 999 <b>9999</b> 9999 <b>9999</b><br>999 <b>9999</b> 9999 <b>9999</b><br>999 <b>9999</b> 9999 <b>9999</b> |
| BORBAGA         FEFFEFFF         FEFFEFF         FEFFEFF         FEFFEFFF         FEFFEFF         FEFFEFFF         FEFFEFFFF <t< td=""><td>bodtlabas freffere interne vereine erefere interne vereine voor voor voor voor voor voor voor voo</td><td>999 <b>9999</b> 9999 <b>9999</b><br/>999 <b>9999</b> 9999 <b>9999</b></td></t<> | bodtlabas freffere interne vereine erefere interne vereine voor voor voor voor voor voor voor voo  | 999 <b>9999</b> 9999 <b>9999</b><br>999 <b>9999</b> 9999 <b>9999</b>                                     |

### 3) Execute SWAP command:

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|---|---|--|--|
| 🎋 Debug   |   |  | 🦌 i+ 🗢 🗆   |
|   | 16FN2M0xxx18_Project JLink Debug [GDB SEGGER Interface Debugging]<br>IK26FN2M0xxx18_Project.axf   |  |  |
|   | Thread #1 57005 (Suspended : Breakpoint)  |  |  |
|   | main() at MK26FN2M0xxx18_Project.c:310 0x1214   |  |  |
| MK26F   | N2M0xxx18_Project.c 🕱 🔄 fsl_ftfx_flash.c  | I fsl ftfx controller.c I fsl ftfx controller.h I fsl ftfx flash.c I fsl   | nain() at MK26FN2M0xxx1  |
| 286<br>287<br>288<br>289<br>290<br>291<br>292   | <pre>result = FLASH_Program(&amp;s_flostDriver, UPPER_PFLASH_BASE + sizeof(tempData),</pre>   | oto));   |  |
| 293<br>294<br>295<br>296<br>297   | <pre>result = FLASH_VerifyProgram(&amp;s_flashbriver, UPPER_PFLASH_BASE * sizeof(tempData), EXAMPLE_]<br/>(uint&amp;t *)(UINER_FLASH_BASE * sizeof(tempData)), kFTFx_MarginValueUser, &amp;failedAc<br/>if (kStatus_FTFx_Success != result)<br/>{<br/>error_trap();<br/>}</pre> |  |  |
| 298<br>299<br>300<br>301  | <pre>result = FLASH_Erose(&amp;s_floshDriver, UPPER_PFLASH_BASE + EXAMPLE_IDAGE_SIZE, sizeof(swapUper</pre>   | tData),  |  |
| 302<br>303<br>304   | {<br>error_trap();<br>}   |  |  |
| 305<br>306<br>307   | <pre></pre>   | α[0],  |  |
| 308   | <pre>printf("\r\n Finish programming backup example image \r\n"); // Enable swap system</pre>   |  |  |
| 310   | <pre>printf("\r\n Start to swap the system \r\n"); result = FLASH_Swap(&amp;s_flashDriver, swapIndicatorAddress, true);</pre>   |  |  |
| 311<br>312  | result = rLASh_Swap(&s_rlashuriver, swapinalcatorAddress, true);  |  |  |
| 313   | NVIC_SystemReset();   |  |  |
| 314<br>315  |   |  |  |
| 316 #els  |   |  |  |
| 317<br>318  | <pre>printf("\r\n Current device doesn't support flash swap feature \r\n");</pre>   |  |  |
| 319   | app_finalize();   |  |  |
| 320 #end  | dif   |  |  |
| 222   | nation Ar   |  |  |
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| 8000 <1   | Traditional> 🕄  | 0x00108000 03020100 07060504 080A0908 0F0E0D0C FFFFFFF FFFFFFF FFFFFFF   |  |
| 8000 <1   | Traditional> 83           0         44332211 88776655 93919190 97969594 FFFFFFF FFFFFFFF  | 0x00108000 03020100 07060504 080A0908 0F0E0D0C FFFFFFF FFFFFFF FFFFFFF<br>0x0010801C FFFFFFF FFFFFFF FFFFFFF FFFFFFF FFFFFF  | <b>9999</b> 9999 <b>9999</b> 9999 <b>9999</b> 9999 <b>9999</b>   |
| 8000 <1   | Traditional>         C3           0         44332211         88776655         93919190         97969594         FFFFFFF   | 0x00108000         03020100         07060504         080A0908         0F0E000C         FFFFFFF         FFFFFFFF           0x0010801C         FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFFF           0x00108038         FFFFFFF         FFFFFFF         FFFFFFFF         FFFFFFFF         FFFFFFF   | <b>3333</b> 3333 <b>3339</b> 3333 <b>3339</b> 3339 3337 <b>3339</b><br><b>3339</b> 3333 <b>3339</b> 3337 <b>3339</b> 3337 <b>3339</b>  |
| 8000 <1   | Craditional>         23           0         4332211         88776655         93919190         97069594         FFFFFFF         ,"30U/m  | 0x00108000 03020100 07060504 080A0908 0F0E0D0C FFFFFFF FFFFFFF FFFFFFF<br>0x0010801C FFFFFFF FFFFFFF FFFFFFF FFFFFFF FFFFFF  | <b>9999</b> 5999 <b>9999</b> 5999 <b>9999</b> 5999 9999<br>9999 5999 9999 5999 5999  |
| <pre>&lt;8000 &lt;1 &lt;00008006 &lt;00008018 &lt;00008036 &lt;00008036 &lt;00008036 </pre>   | Operationals         S3           0         44332211         88776655         93919190         97369594         FFFFFFFF         .*30U/m  | 6x80108808         83820108         87860504         888A4988         0f60200C         FFFFFFF         FFFFFFF           6x8010881C         FFFFFFF         FFFFFFFF         FFFFFFF         FFFFFF         FFFFFFF  | 9939 5995 9999 5999 5999 5999 5999 5999  |
| x8000 <1<br>x00008000<br>x00008018<br>x00008030<br>x00008048<br>x00008048   | Traditionals         S2           0         4332211         8276655         93913130         97065394         FFFFFFFF  | andologead 03202100 0706054 00040900 0705000 FFFFFFF FFFFFF FFFFFF<br>040010302C FFFFFFFF FFFFFF FFFFFF FFFFFFF FFFFFFF  | 9999 9999 9999 9999 9999 9999 9999 9999 9999   |
| x8000 <1<br>x00008000<br>x00008018<br>x00008030<br>x00008048<br>x00008048<br>x00008060<br>x00008078   | Traditional>         S3           0         44332211         88776555         93919190         97965594         FFFFFFF         .*30U/m   | andologoad galacide allocation generation and allocation entering for the formation of a state of the state o | \$9993 0000 9999 9000 9000 9000 9000 9999<br>9999 0000 9999 0000 9999 9000 9999<br>9999 0000 9999 9990 9999 9990 0000 9999<br>9999 0000 9999 9990 9999 9990 9990 9999<br>9999 0000 9999 9000 9999 9990 9990 9999<br>9999 0000 9999 9990 9000 9999 9000 9999            |
| x8000 <1<br>x00008000<br>x00008018<br>x00008030<br>x00008048<br>x00008048<br>x00008078<br>x00008078   | Traditionals         23           0         44332211         8276655         93919190         97969594         FFFFFFF         .*30U/m.         .99999999           0         44332211         8276655         93919190         97969594         FFFFFFF                        | andologead 0320100 0706054 0044090 0706000 FFFFFFF FFFFFF FFFFFF FFFFFF FFFFFF<br>chologead: FFFFFFF FFFFFFF FFFFFFF FFFFFFF FFFFFF  | 5959 0000 5959 5000 5059 0000 5959 5000 5959<br>5959 0000 5959 5050 5050   |
| 0x8000<br>x8000 <1<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x0008808<br>x008808<br>x0008808<br>x008808<br>x008808<br>x008808<br>x0088808<br>x0088808<br>x008888<br>x008888<br>x0088888<br>x0088888<br>x0088888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x008888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x008888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x008888888<br>x008888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x0088888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x00888888<br>x008888888<br>x00888888<br>x00888888<br>x0088888888<br>x0088888888<br>x00888888888<br>x008888888888 | Traditional>         23           0         4332211         8776655         93919190         9766534         FFFFFFFF         ,*30U/m   | andlassen 3320126 SPACESA 0804098 0F0EC00C FFFFFFF FFFFFF FFFFFFF FFFFFFF<br>6x0108802 FFFFFFF FFFFFFF FFFFFFF FFFFFFFFFF  | 9099 5171 9099 5171 9099 5171 9099<br>9099 5171 9099 5171 9099 5171 9099 |

4) **PROBLEM**: System reset is never executed

Please note that there is a breakpoint at line 313 (NVIC\_SystemReset) but it is never reached because the SWAP state machine blocks at kFTFx\_SwapStateUpdate (it remains always in this state)

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|--|---|---|---|
| Debug 🛿  |   |   | <b>i</b> ∳ ⊽ =  |
| ▼ 2000 MK26FN<br>▼ 2000 Threa<br>■ _s<br>■ pri<br>■ FTI<br>■ FTI | M0xxr18_Project JLink Debug [ODB SEGGER Interface Debugging]<br>{2M0xxr18_project.ax1<br>def 157005 [Suspended : Signal : SIGTRAP:Trace/breakpoint trap)<br>ys_write() at 0x43ea<br>mt() at 0x2100<br>Fx_CMD_Erase() at 1sL_ftfx_controller.c:351 0x133e<br>ASL_Swap() at 41_ftxfx_slash.c:520 0x1682 |   |   |
|  | in() at MK26FN2M0xxx18_Project.:311 0x1224<br>ne-eabi-gdb (8.0.50.20171128)   |   |   |
|  |   |   |   |
|  | 0xxx18_Project.c 💽 fsl_ftfx_flash.c 🕱 🗟 MK66FN2M0xxx18_Project.c 🕞 peripherals  | .h 🖻 fsl_ftfx_controller.c 🗎 fsl_ftfx_controller.h 🗟 fsl_ftfx_flash.c 📧 | printf() at 0x210c  |
| 520<br>521<br>522  | <pre>{     /* If current swap mode is Initialized/Ready, Initialize Swap to UPDATE     returnCode =</pre>   |   |   |
| 523<br>524   | FTFx_OMD_SwapControl(ftfxConfig, address, kFTFx_SwapControlOptionSe<br>}  | <i>LINUpdateState</i> , &returnInfo);                                   |   |
| 525  | break;  |   |   |
| 526<br>527 IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII                  | <pre>case kFTFx_SwapStateUpdate:     /* If current swap mode is Update. Erase indicator sector in non active blo</pre>  | -k  |   |
| 528  | * to proceed swap system to update-erased state */  |   |   |
| 529<br>530   | <pre>returnCode = FTFx_CMD_Erase(ftfxConfig, address + (ftfxConfig-&gt;flashDesc.to<br/>ftfxConfig-&gt;opsConfig.addrAligment.sectorCmd,</pre>  |   |   |
| 531  | break;  | ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( )                                 |   |
| 532<br>533   | <pre>case kFTFx_SwapStateUpdateErased:     /* If current swap mode is Update or Update-Erased, progress Swap to COMPLE</pre>  | TT Shake #/   |   |
| 533  | <pre>returnCode =</pre>   | E State -/  |   |
| 535  | FTFx_CMD_SwapControl(ftfxConfig, address, kFTFx_SwapControlOptionSetInC   | <pre>mpleteState, &amp;returnInfo);</pre>                               |   |
| 536<br>537   | break;<br>case kFTFx_SwapStateComplete:   |   |   |
| 538  | break;  |   |   |
| 539  | <pre>case kFTFx_SwapStateDisabled:</pre>  |   |   |
| 5400<br>541  | /* When swap system is in disabled state, We need to clear swap system back<br>* by issuing EraseAllBlocks command */   | to uninitialized  |   |
| 542  | <pre>returnCode = kStatus_FTFx_SwapSystemNotInUninitialized;</pre>  |   |   |
| 543<br>544   | break;<br>default:  |   |   |
| 545  | returnCode = kStatus_FTFx_InvalidArgument;  |   |   |
| 546<br>547   | break;  |   |   |
|  | 3   |   |   |
| I 🗆 P 📮  | С 🛿 🖹 Р 🗍 М 🖓 D 🌍 I 📼 Р 🖾 S 🖋 S 🐎 С 🚥 Н 🚥 Т 🗍 М 🗍 М 🧮   | □ 🟮 Memory Browser 🙁  | ··· · · · · · · · · · · · · · · · · ·                           |
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| 26FN2M0xxx1<br>asing: 109000                                     | 18_Project JLink Debug [GDB SEGGER Interface Debugging] MK26FN2M0xxx18_Project.axf  | 0x108000 <traditional> 🖾</traditional>                                  |   |
| d erase  |   | 0x00108000 FFFFFFFF FFFFFFFF FFFFFFFF FFFFFFFF                          | F 9999 9999 9999 9999 9999 9999 9999                            |
| art while era<br>asina: 109000                                   | se: 108000-108fff (1000)  | 0x0010801C FFFFFFF FFFFFFF FFFFFFFF FFFFFFFF FFFFF                      |   |
|  |   | 0x00108038 FFFFFFF FFFFFFF FFFFFFFF FFFFFFFF FFFFF                      | F 9999 9999 9999 9999 9999 9999 9999                            |
|  | se: 108000-108fff (1000)  | 0x00108054 FFFFFFFF FFFFFFFF FFFFFFFF FFFFFFFF FFFF                     |   |
| art while era  |   | 0x00108070 FFFFFFF FFFFFFF FFFFFFFF FFFFFFFF FFFFF                      | F 9999 9999 9999 9999 9999 9999 9999 9                          |
| art while era<br>asing: 109000                                   |   |   |   |
| asing: 109000<br>d erase<br>art while era                        | se: 108000-108fff (1000)  | 0x0010808C FFFFFFF FFFFFFF FFFFFFFF FFFFFFFF FFFFF                      | F <b>9999</b> 9999 <b>9999</b> 9999 <b>9999</b> 9999 <b>999</b> |
| art while era<br>asing: 109000<br>d erase                        | se: 108000-108fff (1000)  |   | F 9999 9999 9999 9999 9999 9999 9999 9                          |