NXP Semiconductors UM10850

## Chapter 24: LPC5410x Serial Peripheral Interfaces (SPI0/1)

## 24.7.2.3 Transfer\_delay

The Transfer\_delay value controls the minimum amount of time that SSEL is deasserted between transfers, because the EOT bit = 1. When Transfer\_delay = 0, SSEL may be deasserted for a minimum of one SPI clock time. Transfer\_delay is illustrated by the examples in Figure 52.

