I use SPI RAM(CY14B101Q2) .Timing is below(fig1)

For example below is read/write timing. I think /CS of CY14B101Q2 is same as SSEL of LPC54608

Can I acquire an end of SSEL by interrupt(CY14B101Q2 read/write end interrupt)?
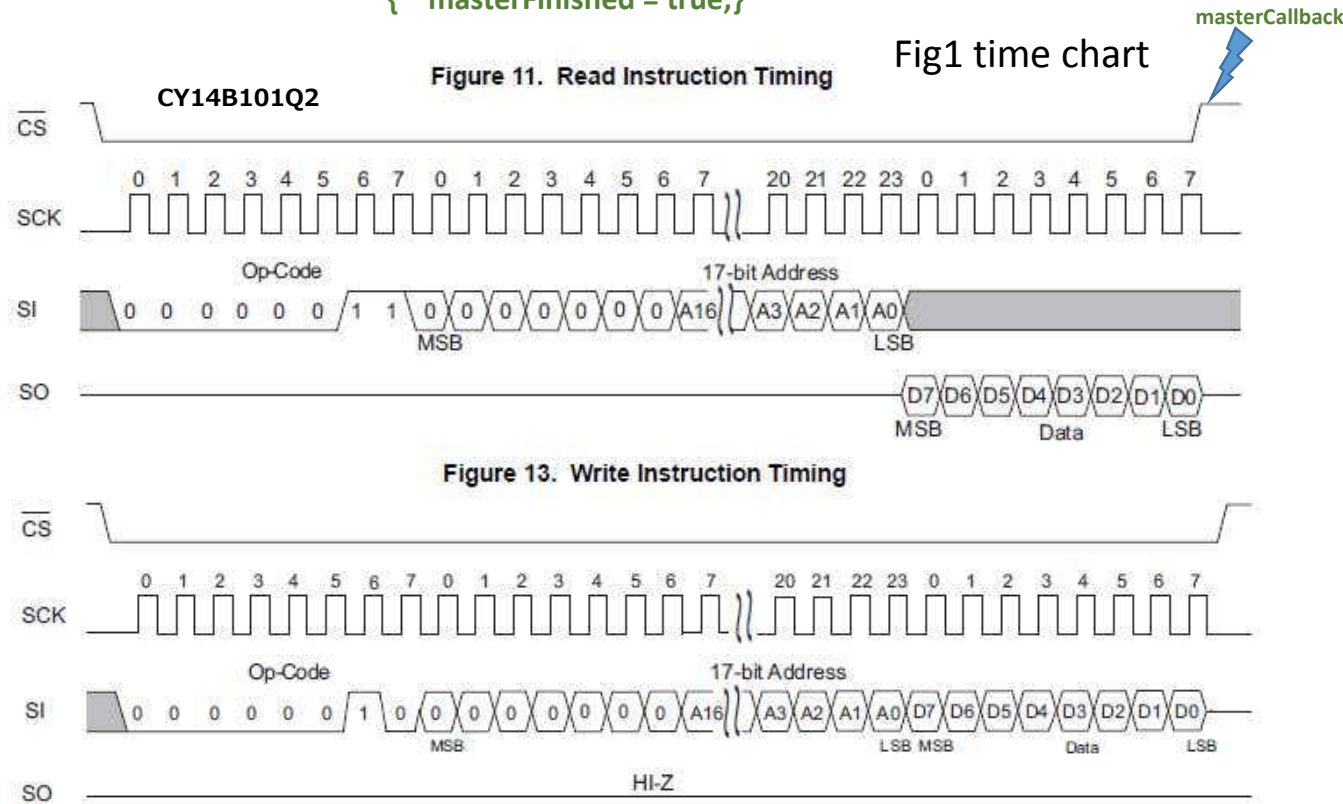
I download NXP SPI sample

"lpcxpresso54608_driver_examples_spi_spi_interrupt_b2b_transfer_master"

In this ,callback function exists. Its code is below(fig2)

Is it end interrupt of SSEL(/CS)?

Fig2 source code

```
static void masterCallback(SPI_Type *base, spi_master_handle_t
*masterHandle, status_t status, void *userData)
{    masterFinished = true;}
```

Fig1 time chart

LPC54608 users manual chart