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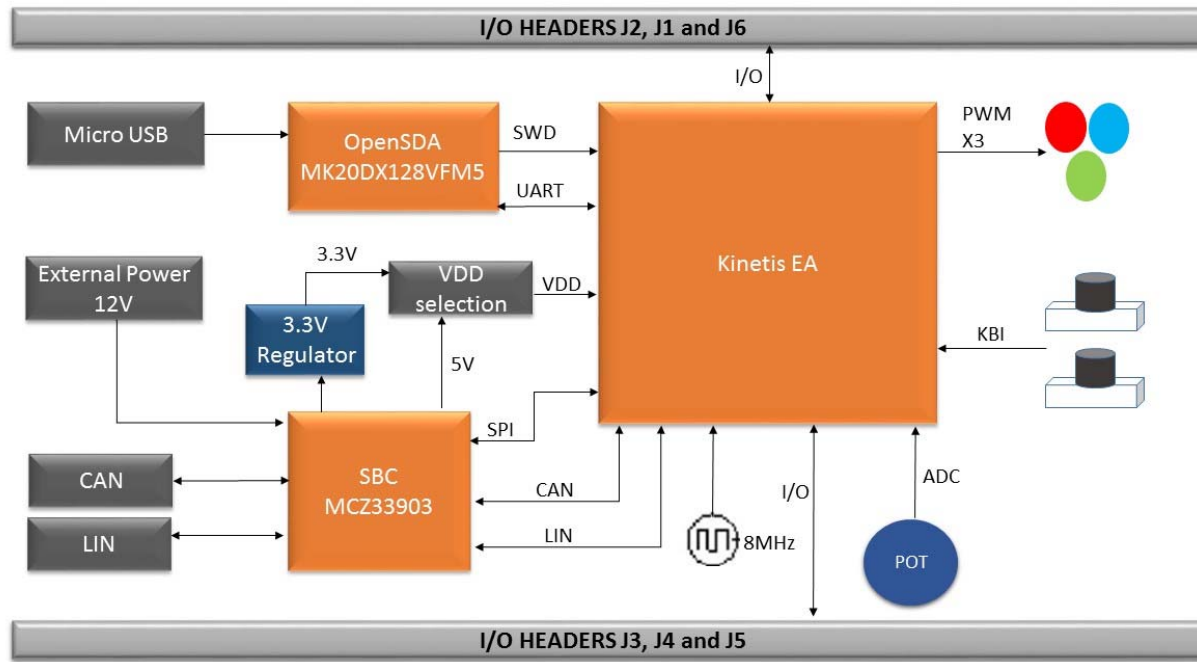
Revisions

Rev	Description	Date	Approved
X1	Original Release	DEC-17-2014	O. Romero
X2	Second Release	JAN-30-2015	O. Romero
A	Production Release	FEB-03-2015	O. Romero
B	2015 Production Release *J19 Connected to V1 instead of 5V_VDD_PSW *J13 Connect to VDD instead of V13 *Added U107 *Replaced D104 *Placed C48 and C47 after D9 and D13 *Placed C48 with 10 uF cap. *Placed U14 *Changed test name 06-V1 to V16 *Relocate PTMG channels *Relocate signals on C202 to C212 *Place R11 and CMP R94 *Changed C94 and 120 header with right angle headers *Place R10 by default and leave R104 DNP *J1 pin J connected to VDD *Replaced R50 with two 2K resistors in parallel *Removed L3 *Removed C14 *Placed D94 button, R18, R16 and C46 *Placed C16 *Button resistors changed to pull-down *Changed J19 voltage regulator *Changed C1 resistor *Changed C1 resistor *Replaced C1, L3 and D6 *Replaced C35 and C38	JUL-06-2015	J. Romero J. Sanchez

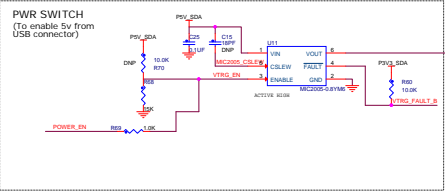
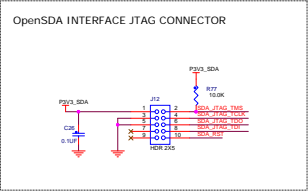
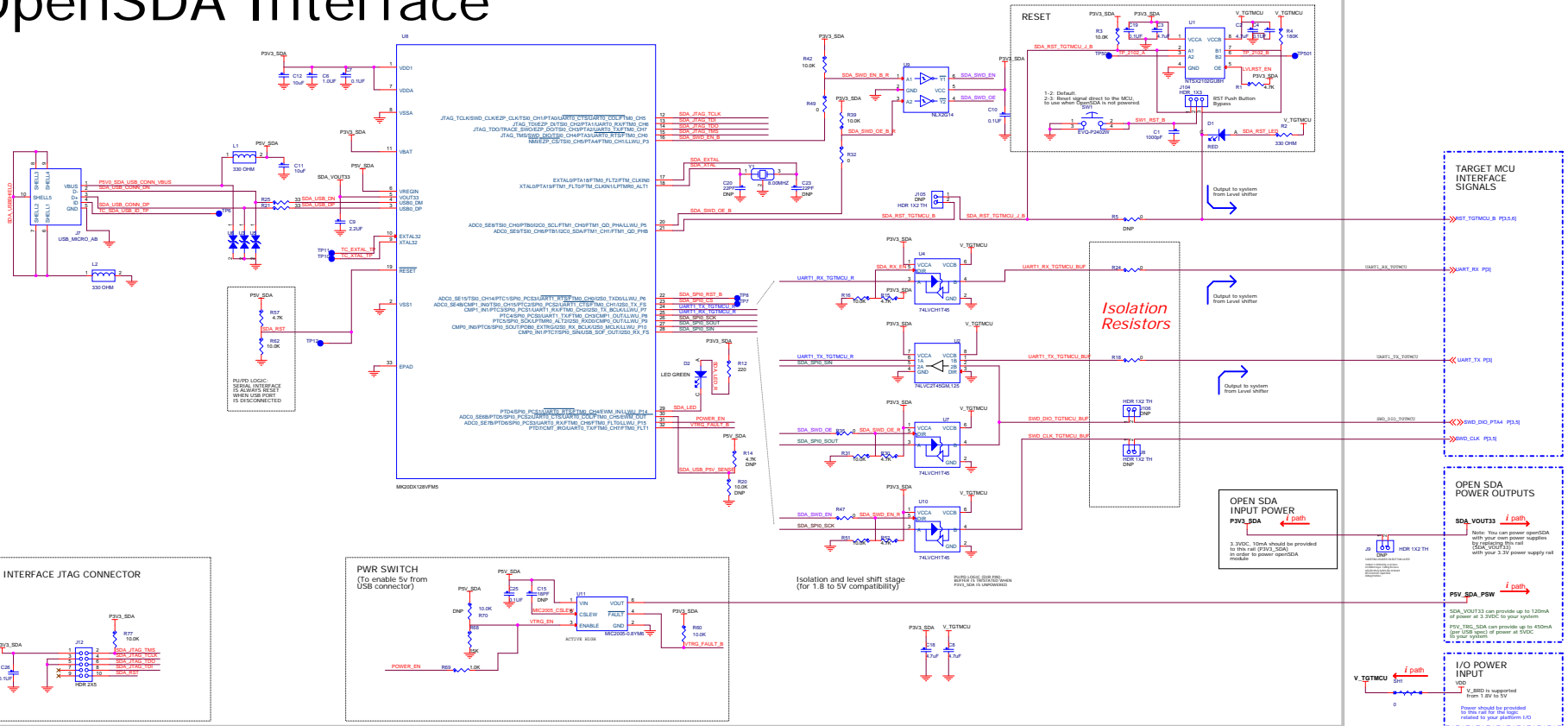
FREEDOM+ KEA

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- Unless Otherwise Specified:
 All resistors are in ohms, 1% and 5 %
 All capacitors are in uF, 10%, 20 % and 5%
 All voltages are DC
 All polarized capacitors are aluminum electrolytic
- Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- Special signal usage:
 _B Denotes - Active-Low Signal
 <> or [] Denotes - Vectored Signals
- Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



OpenSDA Interface



Isolation and level shift stage (for 1.8 to 5V compatibility)

Isolation Resistors

OPEN SDA INPUT POWER
PSV_SDA

3.3VDC, 10mA should be provided to this pin for the target MCU.

OPEN SDA POWER OUTPUTS

PSV_SDA_VOUT3 can provide up to 120mA of power at 3.3VDC to your system.

I/O POWER INPUT
V_TOTMCU

V_SDA is supported from 1.8V to 5V.

