

友联电子

United Electronic

PDF编辑器-未注册

技术文档QQ群: 226846867 (Kinetis Kxx Cortex™-M4)
(注册后这些水印字将不会被保存)

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友联电子

Kinetis Kxx Cortex™-M4

erase all and unsecure

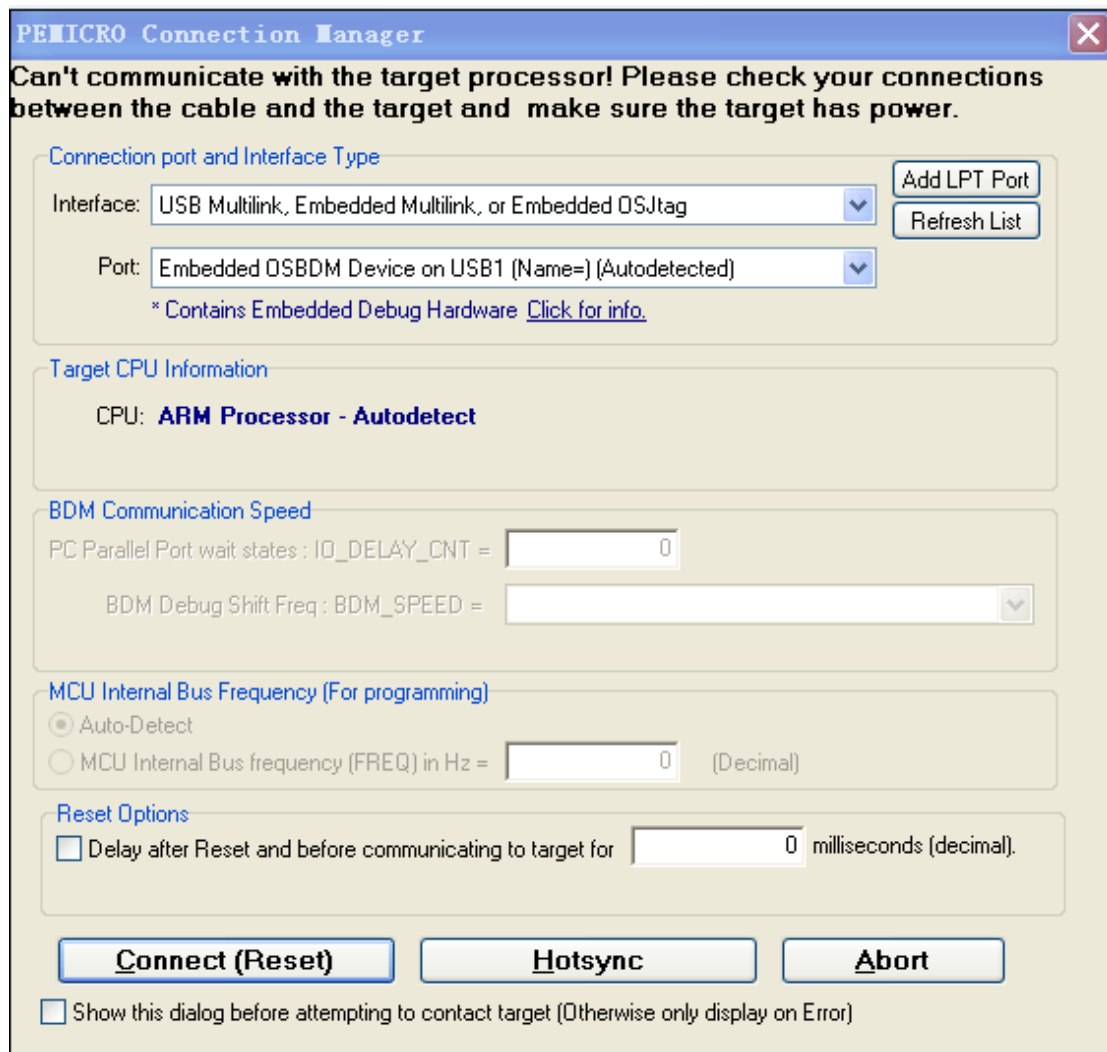
操作说明书

(ARM® Cortex™-M4 V1.0)

武汉友联电子

Issue description

现象描述:

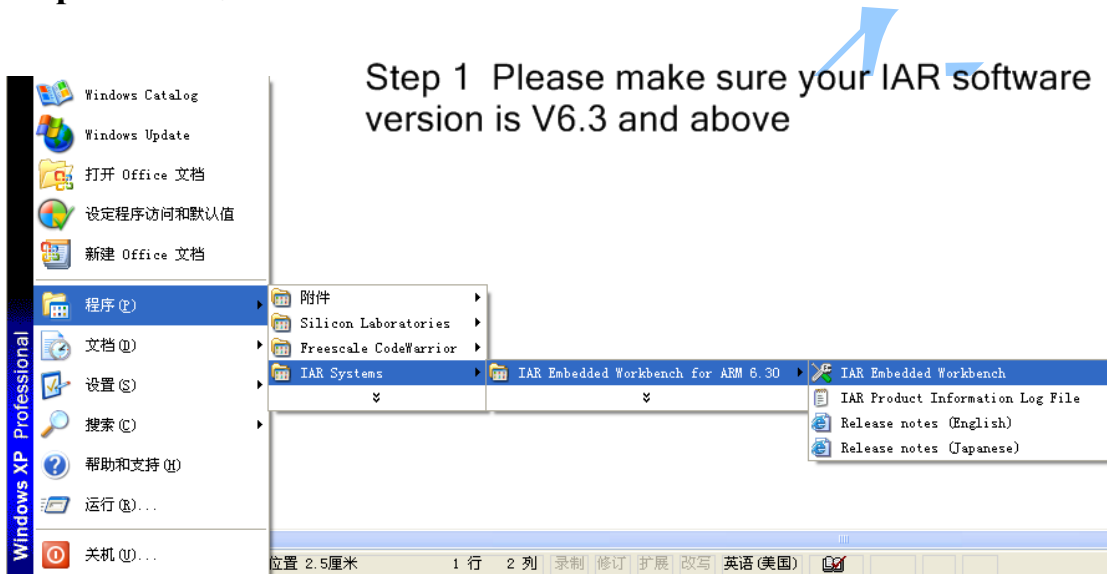


MCU 的复位引脚长期为低电平 MCU reset pin will be low voltage long time

Kinetic unlock way

Kinetic 芯片解锁方法如下:

Step 1: 确认使用的开发工具 IAR 的版本为 6.3 以上



Step 2: 安装 JLINK 仿真驱动 Setup_JLinkARM_V436.zip

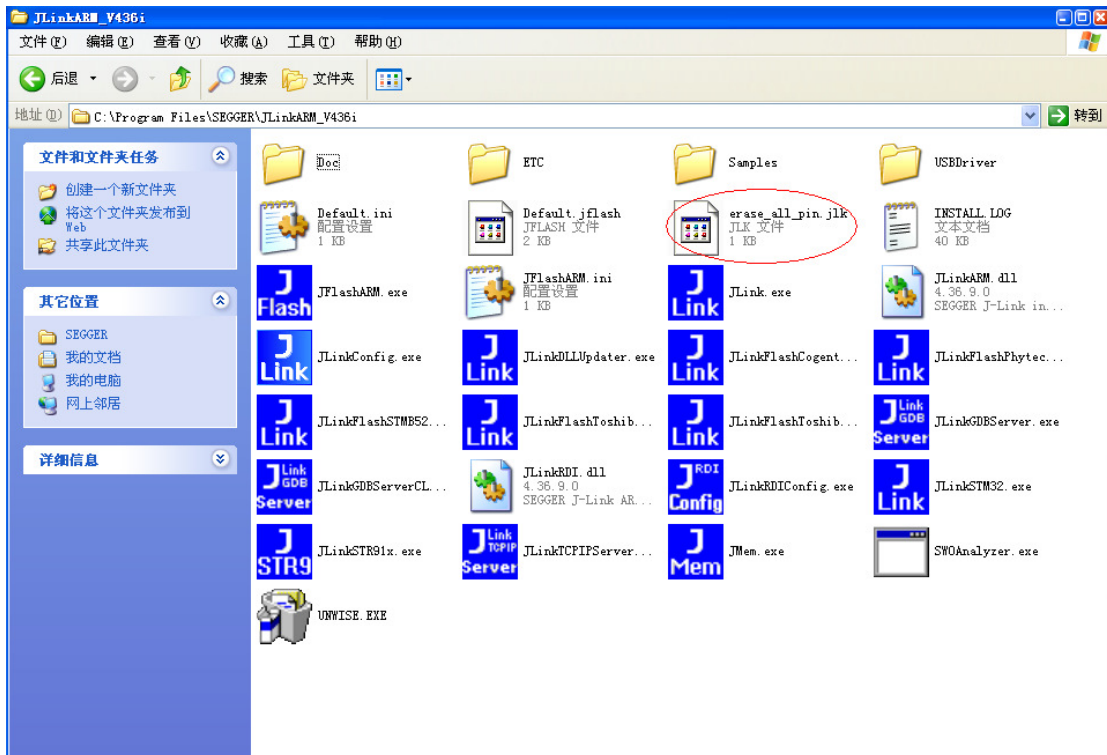
Download J-Link software

下载地址: http://www.segger.com/download_jlink.html



Step 3: 将本目录的 erase_all_pin.jlk 拷贝到 “C:\Program Files\SEGGER\JLinkARM_V436i” directory (Installation folder for your Segger JLink ARM)

Step 3: cope [erase_all_pin.jlk] file to above path(J-Link software installtion folder)



Step 4: 将 Kinetis 芯片的 reset pin 按键按下不动

Step 4: keep pushing reset button

Step 5: 将 Kinetis 芯片的开发板上电

Step 5: Power up the board

Step 6: typing and run "jlink.exe erase_all_pin.jlk" script at Windows's cmd panel(with related path).

Step 6: 在 PC 机的 cmd 命令中，输入“C:\Program Files\SEGGER\JLinkARM_V436i” directory and execute the script by typing “jlink.exe erase_all_pin.jlk”

```
C:\Users>cd ..  
C:\>cd program files (x86)/segger  
C:\Program Files (x86)\SEGGER>cd jlinkarm_v412  
C:\Program Files (x86)\SEGGER\JLinkARM_U412>jlink.exe erase_all_pin.jlk
```

```
C:\Program Files\SEGGER\JLinkARM_V436i>jlink.exe erase_all_pin.jlk
```

```
SEGGER J-Link Commander V4.36i ('?' for help)
```

```
Compiled Nov 11 2011 09:31:17
```

```
Script file read successfully.
```

```
DLL version V4.36i, compiled Nov 11 2011 09:31:01
```

```
Firmware: J-Link ARM V8 compiled Sep 22 2011 16:23:23
```

```
Hardware: V8.00
```

```
S/N: 20100214
```

```
Feature(s): RDI,FlashDL,FlashBP,JFlash,GDBFull
```

```
VTarget = 3.267V
```

```
Info: TotalIRLen = 4, IRPrint = 0x01
```

```
Info: Found Cortex-M4 r0p0, Little endian.
```

```
Info: TPIU fitted.
```

```
Info: ETM fitted.
```

```
Info: ETB present.
```

```
Info: CSTF present.
```

```
Info: FPUnit: 6 code (BP) slots and 2 literal slots
```

```
Found 1 JTAG device, Total IRLen = 4:
```

```
#0 Id: 0x4BA00477, IRLen: 04, IRPrint: 0x1, CoreSight JTAG-DP (ARM)
```

Cortex-M4 identified.

JTAG speed: 100 kHz

Processing script file...

JTAG speed: 1000 kHz

Sleep(10)

Reset type RESETPIN: Resets core & peripherals using RESET pin.

Reset delay: 0 ms

Reset type RESETPIN: Resets core & peripherals using RESET pin.

Info: TotalIRLen = 4, IRPrint = 0x01

Info: Found Cortex-M4 r0p0, Little endian.

Info: TPIU fitted.

Info: ETM fitted.

Info: ETB present.

Info: CSTF present.

Info: FPUnit: 6 code (BP) slots and 2 literal slots

WARNING: CPU did not halt after reset.

Info: Found Cortex-M4 r0p0, Little endian.

Info: TPIU fitted.

Info: ETM fitted.

Info: ETB present.

Info: CSTF present.

Info: FPUnit: 6 code (BP) slots and 2 literal slots

WARNING: CPU could not be halted

Info: Core did not halt after reset, trying to disable WDT.

WARNING: CPU did not halt after reset.

Info: Found Cortex-M4 r0p0, Little endian.

Info: TPIU fitted.

Info: ETM fitted.

Info: ETB present.

Info: CSTF present.

Info: FPUnit: 6 code (BP) slots and 2 literal slots

WARNING: CPU could not be halted

WARNING: CPU did not halt after reset.

Info: Found Cortex-M4 r0p0, Little endian.

Info: TPIU fitted.

Info: ETM fitted.

Info: ETB present.

Info: CSTF present.

Info: FPUnt: 6 code (BP) slots and 2 literal slots

WARNING: CPU could not be halted

WARNING: S_RESET_ST not cleared

WARNING: CPU could not be halted

Sleep(1000)

Selecting SWD as current target interface.

Setting target interface speed to 1MHz. Use "Speed" to change.

Sleep(10)

Select SWD by sending SWD switching sequence.

Found SWD-DP with ID 0x2BA01477

Sleep(10)

Write DP register 2 = 0x01000000

Sleep(10)

Read AP register 0 = 0x00000000

Sleep(10)

Read AP register 0 = 0x00000032

Sleep(10)

Read AP register 1 = 0x00000032

Sleep(10)

Read AP register 1 = 0x00000000

Sleep(10)

Read AP register 0 = 0x00000000

Sleep(10)

Read AP register 0 = 0x00000032

Sleep(10)

Write AP register 1 = 0x00000001

Sleep(1000)

Read AP register 0 = 0x00000032

Sleep(10)

Read AP register 0 = 0x00000033

Sleep(10)

Read AP register 1 = 0x00000033

Sleep(10)

Read AP register 1 = 0x00000000

Sleep(100)

Write DP register 2 = 0x00000000

Sleep(1000)

Selecting JTAG as current target interface.

Setting target interface speed to 1MHz. Use "Speed" to change.

Sleep(100)

Info: TotalIRLen = 4, IRPrint = 0x01

Reset delay: 0 ms

Reset type RESETPIN: Resets core & peripherals using RESET pin.

Info: TotalIRLen = 4, IRPrint = 0x01

Info: Found Cortex-M4 r0p0, Little endian.

Info: TPIU fitted.

Info: ETM fitted.

Info: ETB present.

Info: CSTF present.

Info: FPUUnit: 6 code (BP) slots and 2 literal slots

WARNING: CPU did not halt after reset.

Info: Found Cortex-M4 r0p0, Little endian.

Info: TPIU fitted.

Info: ETM fitted.

Info: ETB present.

Info: CSTF present.

Info: FPUUnit: 6 code (BP) slots and 2 literal slots

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Info: FPUUnit: 6 code (BP) slots and 2 literal slots

WARNING: CPU could not be halted

WARNING: S_RESET_ST not cleared

Script processing completed.

C:\Program Files\SEGGER\JLinkARM_V436i>

Step 7: 放开 Kinetis 芯片的 reset pin 按键

Step 7: release Kinetis's reset button.