Freescale Semiconductor
Engineering Bulletin

Document Number:EB802
Rev 0, 03/2014

TWR-K64F120M Revision B Notice

1 Introduction

Some early TWR-K64F120M Revision B boards were shipped with incorrect jumper configurations and demo application.

Jumper settings should be:

1. J39 shunt 1-2
2. J16 shunt 1-2
3. J29 shunt 1-2
4. J10 shunt 2-3
5. J15 shunt 2-3

Updated application demo code and Quick Start Guide can be found at http://www.freescale.com/twr-k64f120m.

TWR-K64F120M boards with updated jumper configurations and demo code will be tagged with a label marked TDA4906 on the bottom side of the board.

Contents

1 Introduction.....1
2 Revision history.....2

© 2014 Freescale Semiconductor, Inc.

2 Revision history

The table given in this section provides details regarding the current and previous versions of this document and the major changes incorporated in each of these versions.

Table 1. Revision history

Rev No.	Substantive change(s)
0	Initial release

How to Reach Us:

Home Page:
freescale.com

Web Support:
freescale.com/support

Information in this document is provided solely to enable system and software implementers to use Freescale products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document.

Freescale reserves the right to make changes without further notice to any products herein. Freescale makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. “Typical” parameters that may be provided in Freescale data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including “typicals,” must be validated for each customer application by customer's technical experts. Freescale does not convey any license under its patent rights nor the rights of others. Freescale sells products pursuant to standard terms and conditions of sale, which can be found at the following address: freescale.com/SalesTermsandConditions.

Freescale, the Freescale logo, AltiVec, C-5, CodeTest, CodeWarrior, ColdFire, ColdFire+, C-Ware, Energy Efficient Solutions logo, Kinetis, mobileGT, PowerQUICC, Processor Expert, QorIQ, Qorivva, StarCore, Symphony, and VortiQa are trademarks of Freescale Semiconductor, Inc., Reg. U.S. Pat. & Tm. Off. Airfast, BeeKit, BeeStack, CoreNet, Flexis, Layerscape, MagniV, MXC, Platform in a Package, QorIQ Qonverge, QUICC Engine, Ready Play, SafeAssure, SafeAssure logo, SMARTMOS, Tower, TurboLink, Vybrid, and Xtrinsic are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners.

© 2014 Freescale Semiconductor, Inc.

Document Number EB802
Revision 0, 03/2014

ARM® POWERED

freescale™