

▼ mode_config_info	struct {...}	{...}
⌘ status_reg_write_enable	uint32_t	112
⌘ reserved0	uint32_t	1
⌘ soft_reset_rescue_support	uint32_t	48
⌘ exit_4_byte_addressing	uint32_t	768
⌘ enter_4_byte_addrssing	uint32_t	128
▼ read_1_8_info	struct {...}	{...}
⌘ dummy_clocks_1_8_8_read	uint32_t	0
⌘ mode_clocks_1_8_8_read	uint32_t	0
⌘ inst_1_8_8_read	uint32_t	0
⌘ dummy_clocks_1_1_8_read	uint32_t	0
⌘ mode_clocks_1_1_8_read	uint32_t	0
⌘ inst_1_1_8_read	uint32_t	0
> xpi_misc_info	struct {...}	{...}
> mode_octal_info	struct {...}	{...}
> max_speed_info_xpi	struct {...}	{...}
⌘ has_4b_addressing_inst_table	_Bool	false
> flash_4b_inst_tbl	jedec_4byte_addressing_inst_table...	{...}
+ Add new expression		

▼ flash_4b_inst_tbl	jedec_4byte_addressing_inst_table...	{...}
▼ cmd_4byte_support_info	struct {...}	{...}
⌘ support_1_1_1_read	uint32_t	0
⌘ support_1_1_1_fast_read	uint32_t	0
⌘ support_1_1_2_fast_read	uint32_t	0
⌘ support_1_2_2_fast_read	uint32_t	0
⌘ support_1_1_4_fast_read	uint32_t	0
⌘ support_1_4_4_fast_read	uint32_t	0
⌘ support_1_1_1_page_program	uint32_t	0
⌘ support_1_1_4_page_program	uint32_t	0
⌘ support_1_4_4_page_program	uint32_t	0
⌘ support_erase_type1_size	uint32_t	0
⌘ support_erase_type2_size	uint32_t	0
⌘ support_erase_type3_size	uint32_t	0
⌘ support_erase_type4_size	uint32_t	0
⌘ support_1_1_1_dtr_read	uint32_t	0
⌘ support_1_2_2_dtr_read	uint32_t	0
⌘ support_1_4_4_dtr_read	uint32_t	0
⌘ support_volatile_sector_lock_read_cm	uint32_t	0
⌘ support_volatile_sector_lock_write_cm	uint32_t	0
⌘ support_nonvolatile_sector_lock_read	uint32_t	0
⌘ support_nonvolatile_sector_lock_write	uint32_t	0
⌘ reserved	uint32_t	0