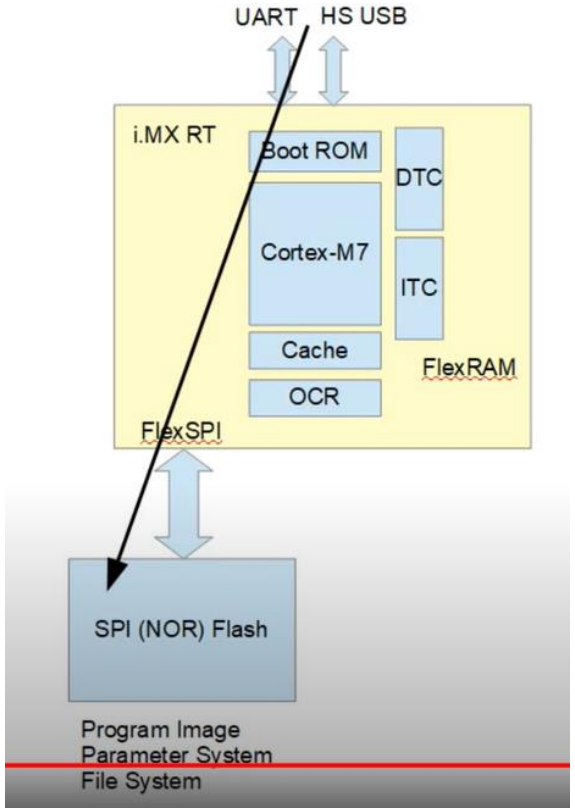
Reference: https://youtu.be/GXztWg9m6_8?t=495



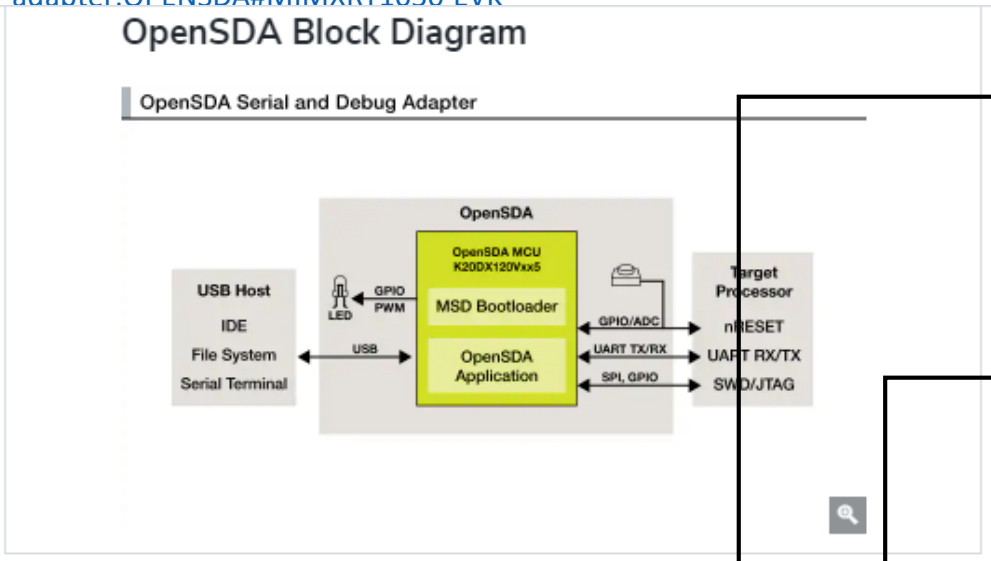
Questions:

1. From video, I understood that, if Boot ROM supports UART/HS-USB interfaces to flash images into SPI flash, shall we avoid OpenSDA associated USB interface in our custom board?
So, we will use the UART interface for production programming and JTAG interface for debug program. Please confirm.

2. There are two flash chips in board (hyper-flash & SPI flash). Does this boot ROM supports Both flash chips to load the application image using UART? So that we will populate both flash chips in custom board. Please confirm.

3. I have flashed many SDK examples and my application images (**awf file**) into IMXRT1050B board using MCUExpresso IDE in debug mode.
Where do they get flashed by default? Is it in SPI flash or Hyper Flash?

Reference: https://www.nxp.com/design/microcontrollers-developer-resources/ides-for-kinetis-mcus/opensda-serial-and-debug-adapter:OPENSDA#MIMXRT1050-EVK

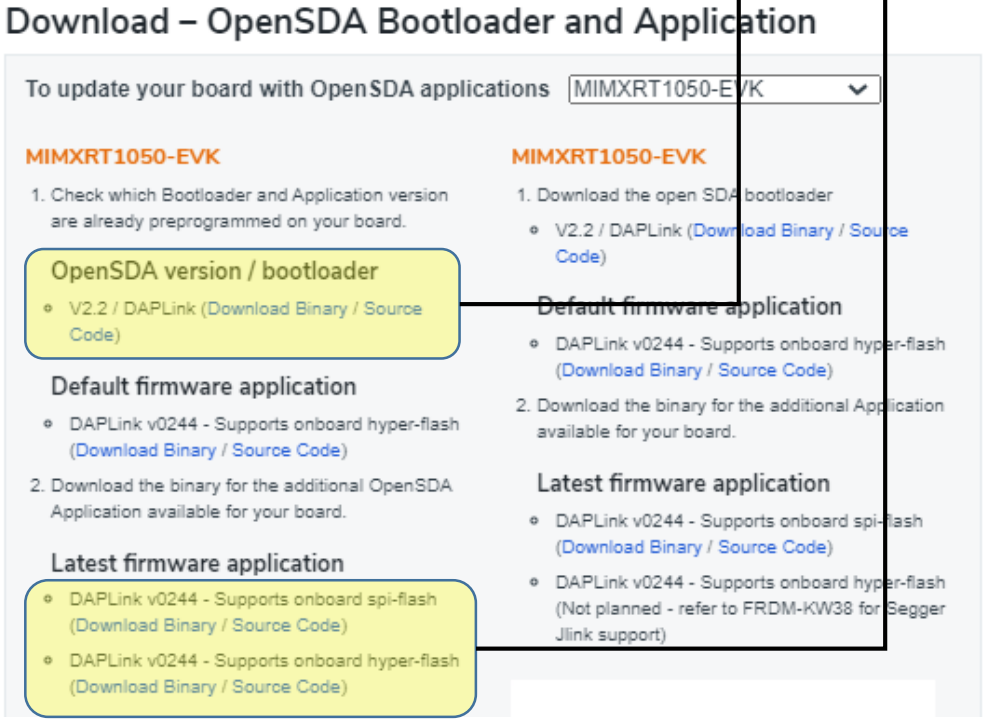


Questions:

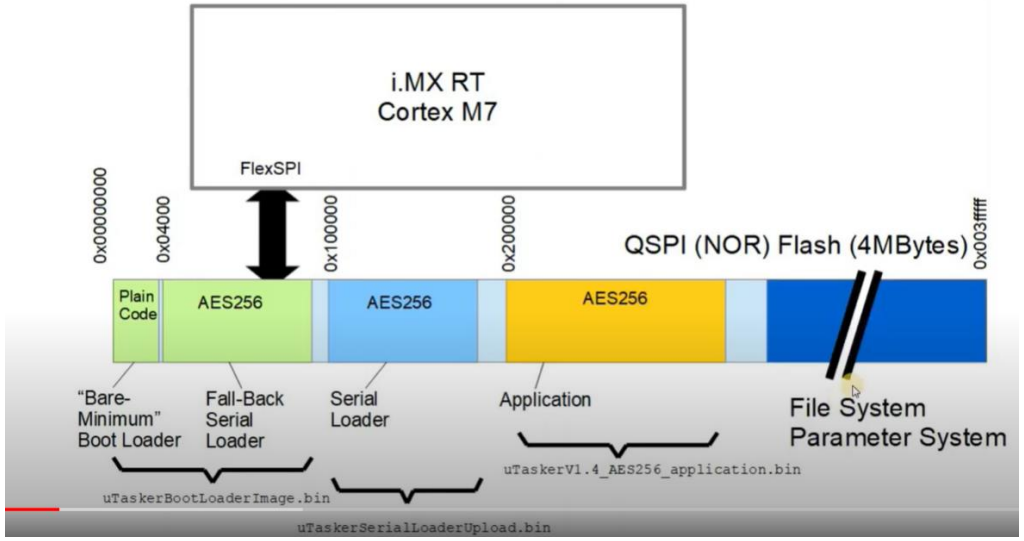
1. Where do this OpenSDA bootloader resides? I assume its part of another nxp MCU chip part of EVAL board for OpenSDA purpose, please confirm.

2. Does these DAPlink firmwares (Hyper-flash & spi-flash) are part of flash chips on EVAL board?

3. If yes, if I load uTasker binaries (bootloader and serial loader) (shown below) into SPI flash, Will it affects/overwrite DAPlink firmware on the Flashchip? I am bit confused. Please clarify.

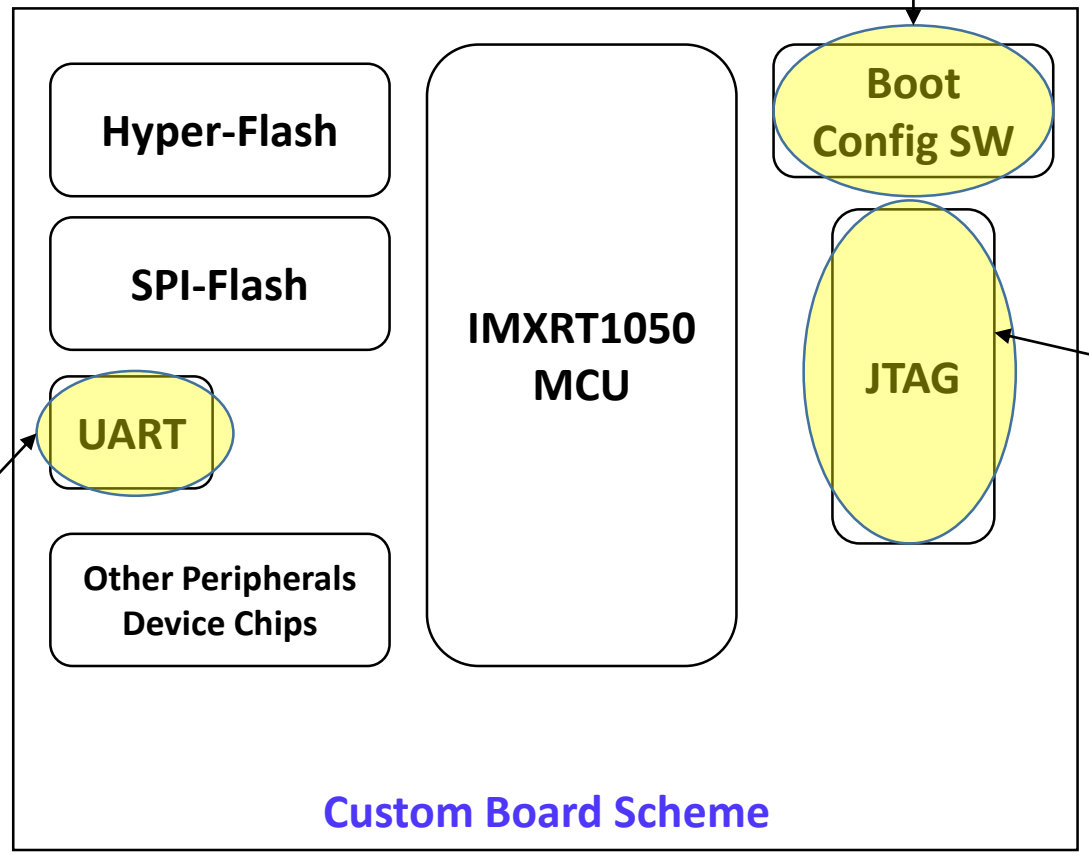


Reference: https://youtu.be/2XfgZq19XDw?t=422



Kindly review the boot load options in custom board scheme based on the your experience and suggest us.

Boot config switches, will be used till development stage and during production board, it will be hard-fused to fixed boot configuration



UART will be used for flashing application in production version and even in development stage as well

JTAG will be used till development stage for flashing application & debugging purposes and during production board, it will be removed