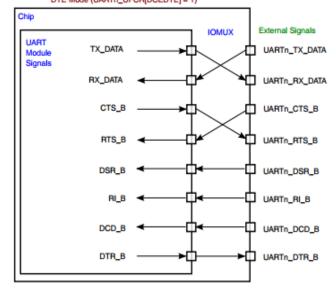
i.MX6SoloX UART connection

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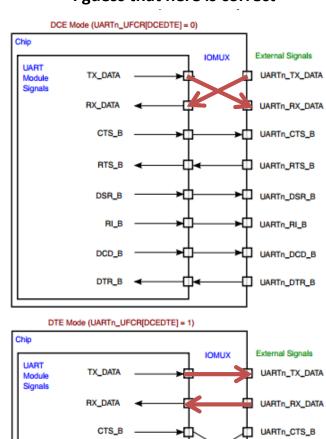
DCE Mode (UARTn_UFCR[DCEDTE] = 0) Chip External Signals IOMUX UART TX_DATA UARTn_TX_DATA Module Signals RX_DATA UARTn_RX_DATA CTS B UARTn_CTS_B RTS_B UARTn_RTS_B DSR_B UARTn_DSR_B RI_B UARTn_RI_B DCD_B UARTn_DCD_B UARTn_DTR_B

DTE Mode (UARTn_UFCR[DCEDTE] = 1)



mode

I guess that here is correct



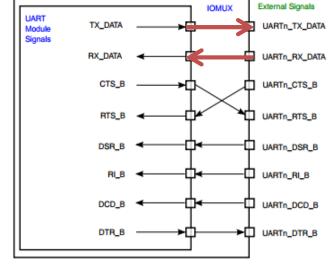


Figure 65-2. UART external signals to module signals routing with respect to DCE/DTE

Figure 65-2. UART external signals to module signals routing with respect to DCE/DTE mode