

NOTES:

1. eMMC\_CLK, eMMC\_CMD AND eMMC\_DAT\_X SIGNALS SHOULD BE ROUTED AS CONTROLLED IMPEDANCE AT 50ohm.
2. SERIAL RESISTANCE ON CLK LINE SHOULD BE PLACED AS CLOSE TO THE MCU AS POSSIBLE.
3. RST, CLK, CMD AND DAT\_X SIGNALS SHOULD BE LESS THAN 2000 MIL.
4. SPACE BETWEEN ADJACENT SIGNALS - AT LEAST THREE TIMES THE TRACE WIDTH.
5. MAX CLOCK SPEED - 52MHZ
6. SET PU RESISTORS ON MCU TO PREVENT BUS FLOATING  
 CMD PU - 22K\_OHM\_PU  
 DAT[7:0] PU - 47K\_OHM\_PU  
 RST PU - 47K\_OHM\_PU
7. MAX SKEW 50 MIL

