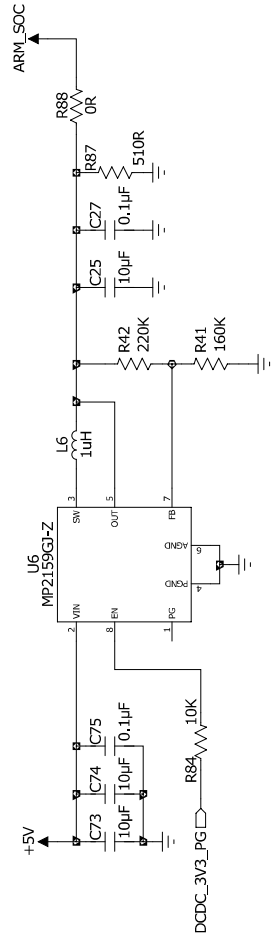
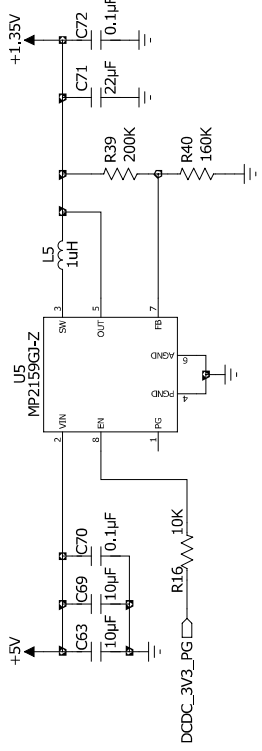
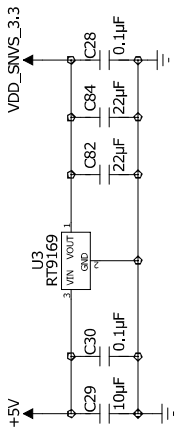
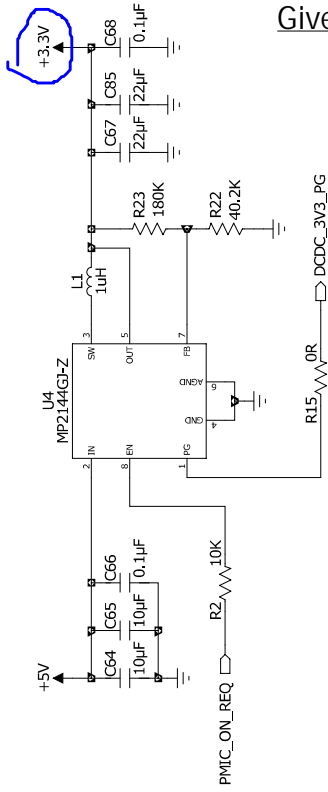


REVISION RECORD	
LTR	DATE



Given to all peripheral section of Processor. Given to VDD\_HIGH\_IN

D  
C  
B  
A  
D  
C  
B  
A  
D  
C  
B  
A