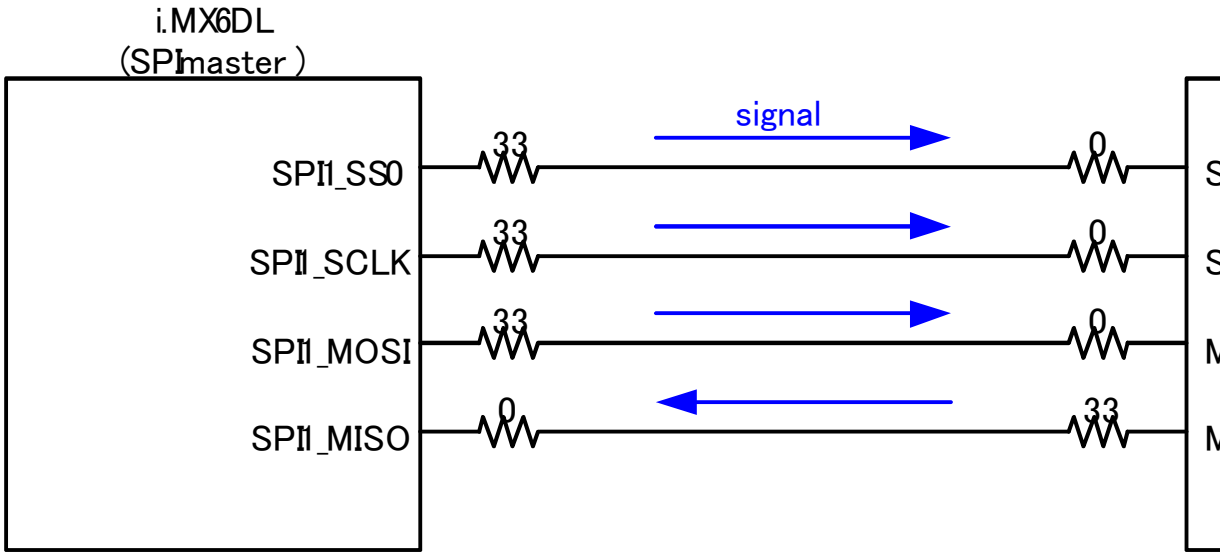
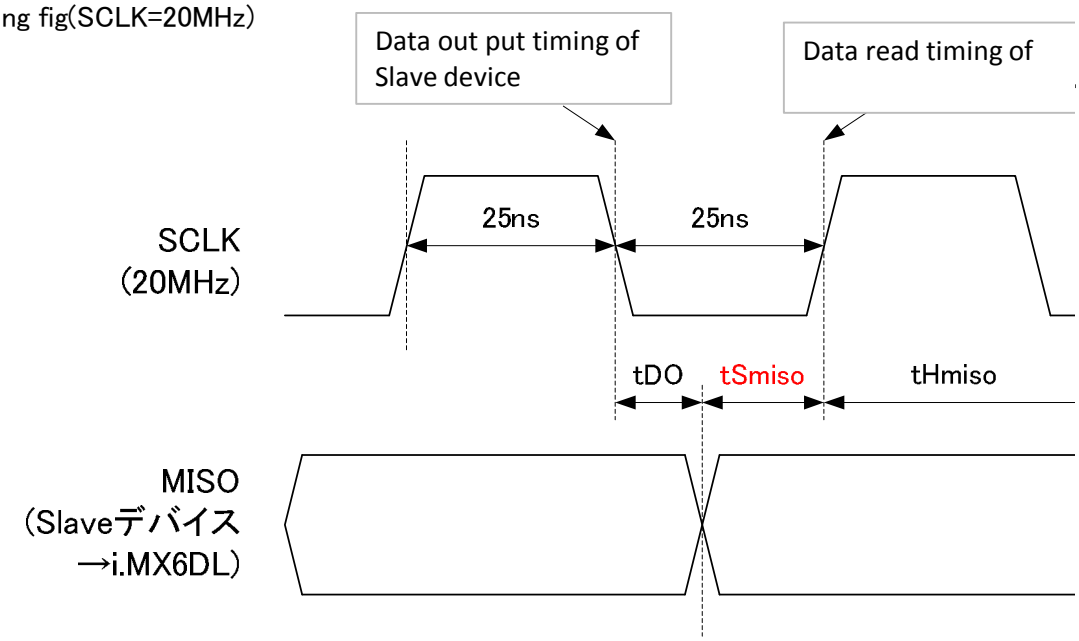
■SPI connection



ame of signa	mean
SS	SPI Slave-Select
SCLK	SPI Serial-Clock
MISO	SPI Serial-DATA. (Master-In, Slave-Out)
MOSI	SPI Serial-DATA. (Master-Out, Slave-In)

■MISOtiming fig(SCLK=20MHz)



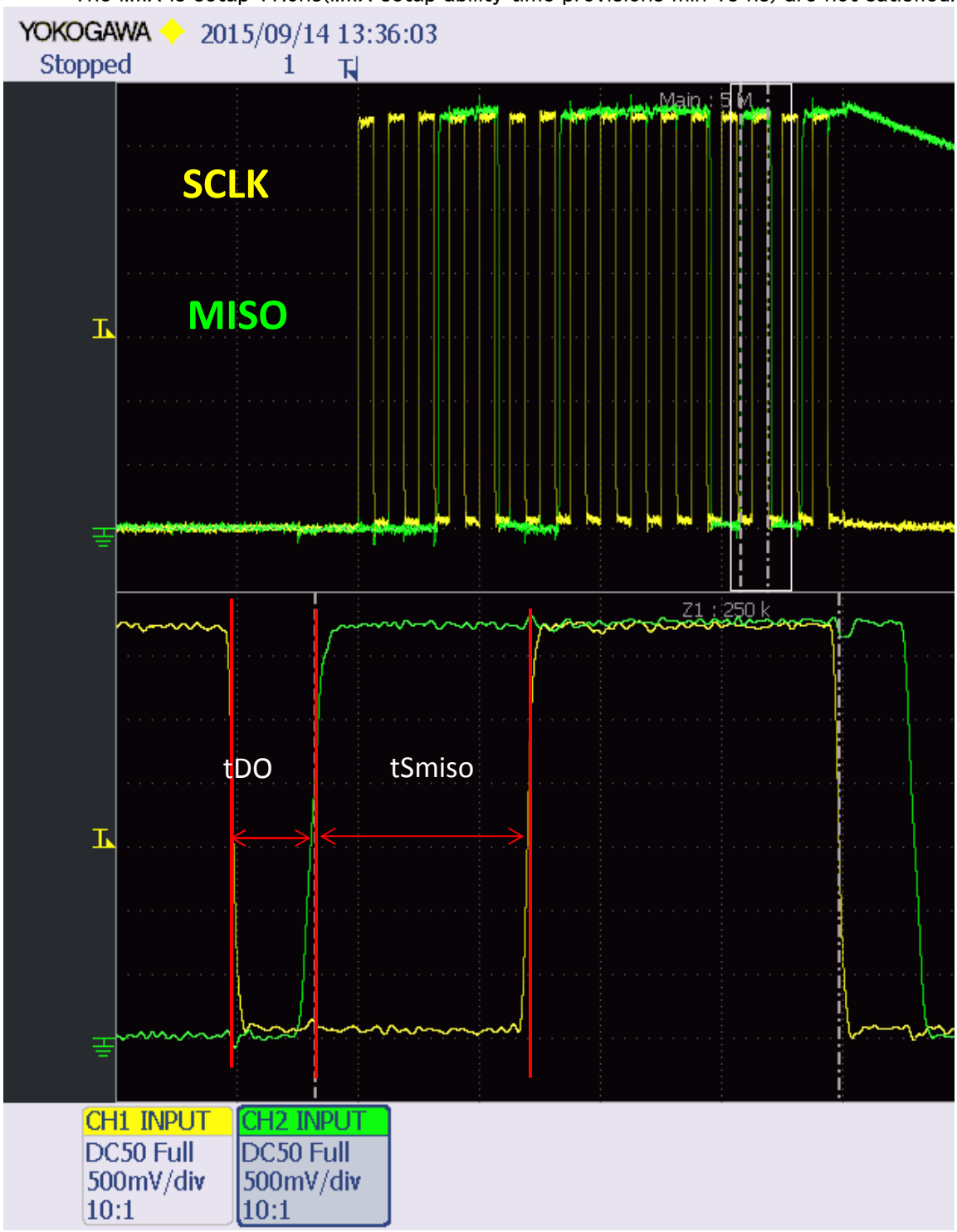
Parameter	Symbol	min	typ	max
SCLK Fall to MISO Propagation Delay	tDO	–	–	14.2
SPI1_MISO Setup Time	tSmiso	18	–	–

If the output of the MISO was delayed max value, i.MX6DL side of the set-up time is n

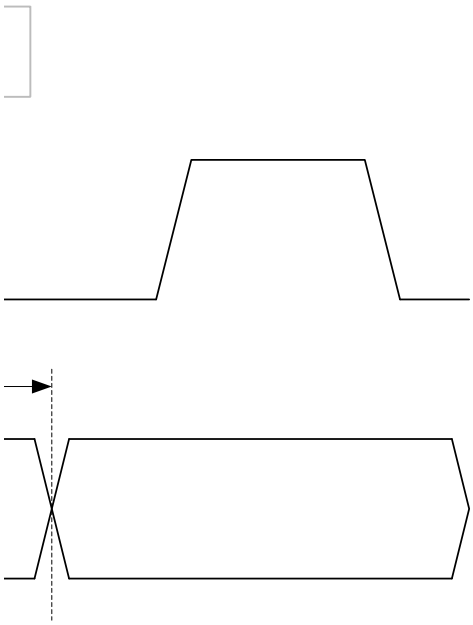
■実測波形

Output delay time of the Slave device, at the Spec MAX value 14.2ns, is measured (roo

The i.MX is setup 17.5ns(i.MX setup ability time provisions min 18 ns) are not satisfied.



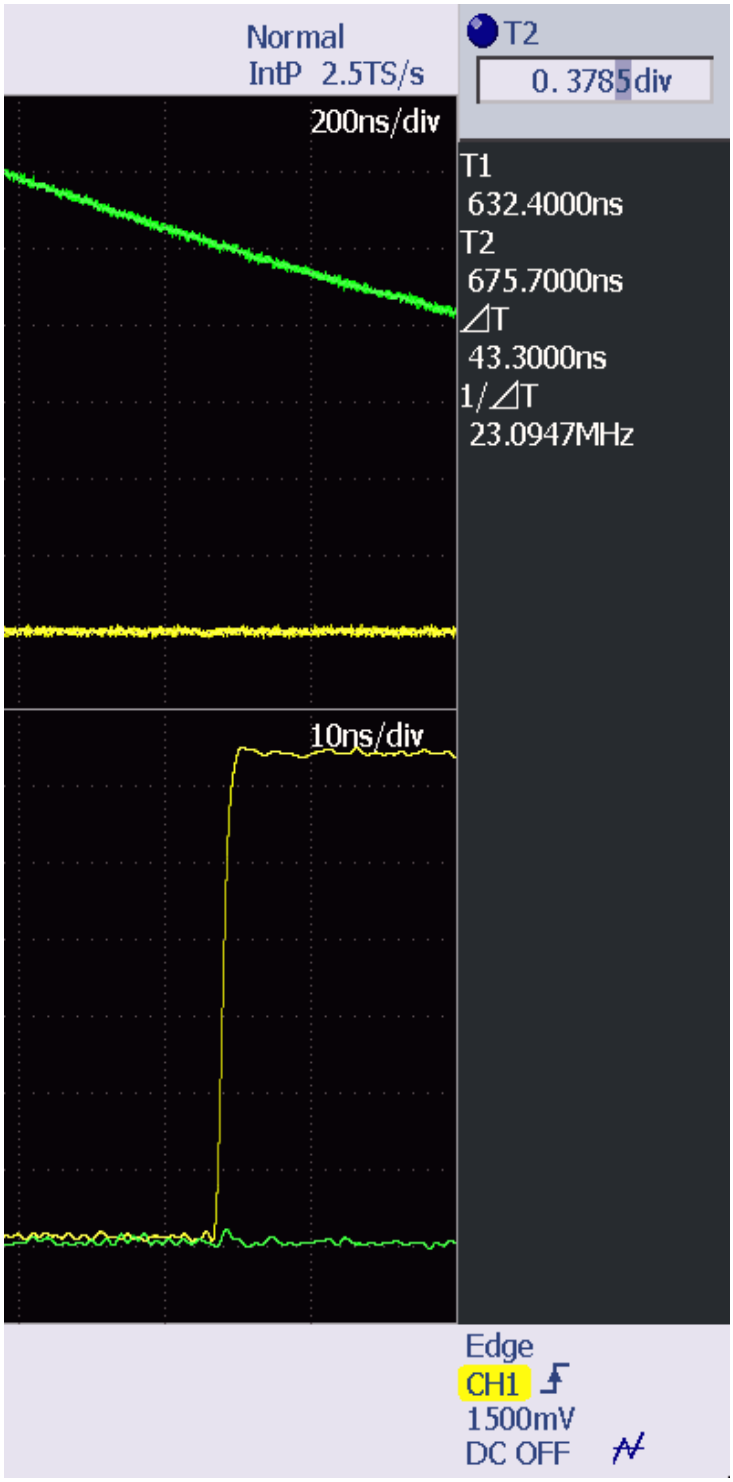
tSmiso=17.5ns ＜ 18ns NG



unit	備考
ns	Slave device spec
ns	i.MX6DL spec

ot meet the 25ns−14.2ns = 10.8ns provisions of 18ns (min)

om temperature) in about 7.5ns.


Normal
IntP 2.5TS/s
T2
0.3785div
200ns/div
T1
632.4000ns
T2
675.7000ns
⊿T
43.3000ns
1/⊿T
23.0947MHz
10ns/div
Edge
CH1
1500mV
DC OFF