

**Specify ODT Models**

Specify the IBIS Model Selector choices to be used when ODT (on-die termination) is on and off at DRAM and memory-controller input pins. ODT applies only to Data (DQ), Data Strobe (DQS), and Data Mask (DM) nets. Since most DRAMs in a design will typically use the same IBIS models and settings, choose the selectors for one DRAM, select the row containing that DRAM, and then click on the "Apply These Settings to Similar DRAMs" button to quickly apply those settings to all DRAMs.

**ODT IBIS Model Selectors:**

Device	IBIS Component	Type	DQS, DQS#		DQ		DM	
			ODT Disabled	ODT Enabled	ODT Disabled	ODT Enabled	ODT Disabled	ODT Enabled
J14	iMX6Q	Controller	ddr3_sel11_ds110_mio	ddr3odt_t60_sel11_mi	ddr3_sel11_ds110_mio	ddr3odt_t60_sel11_mi	ddr3_sel11_ds110_mio	ddr3odt_t60_sel11_mi
J1	MT41J128M16JT	DRAM	DQS_34_1066	DQS_40_ODT60_1066	DQ_34_1066	DQ_40_ODT60_1066	DM_INPUT_1066	DM_ODT60_1066
J2	MT41J128M16JT	DRAM	DQS_34_1066	DQS_40_ODT60_1066	DQ_34_1066	DQ_40_ODT60_1066	DM_INPUT_1066	DM_ODT60_1066
J3	MT41J128M16JT	DRAM	DQS_34_1066	DQS_40_ODT60_1066	DQ_34_1066	DQ_40_ODT60_1066	DM_INPUT_1066	DM_ODT60_1066
J4	MT41J128M16JT	DRAM	DQS_34_1066	DQS_40_ODT60_1066	DQ_34_1866	DQ_40_ODT60_1066	DM_INPUT_1066	DM_ODT60_1066

Apply These Settings to Similar DRAMs

Save < Back    Next >    Finish    Cancel    Help

