

SW21		SW22	
1	BT_MEM_CTL0	1	BT_USB_SRC0
2	BT_MEM_CTL1	2	BT_USB_SRC1
3	BT_MEM_TYPE0	3	BT_MLC_SEL
4	BT_MEM_TYPE1	4	BT_SPARE_SIZE
5	BT_PAGE_SIZE0	5	BT_SRC0
6	BT_PAGE_SIZE1	6	BT_SRC1
7	BT_BUS_WIDTH0	7	BT_EEPROM_CFG
8	BT_BUS_WIDTH1	8	

BT_MEM_CTL[1:0]	00 = WEIM	BT_PAGE_SIZE[1:0]	BT_MEM_CTL = NAND Flash
	01 = NAND Flash		00 = 512 bytes
	10 = Reserved		01 = 2K bytes
	11 = Expansion		10 = 4K bytes
BT_MEM_TYPE[1:0]	BT_MEM_CTL = WEIM		11 = Reserved
	00 = NOR		BT_MEM_CTL = NAND Flash
	01 = Reserved		00 = 8 bit bus
	10 = One Nand		01 = 16 bit bus
	11 = Reserved	BT_BUS_WIDTH[1:0]	10 = Reserved
	BT_MEM_CTL = NAND		11 = Reserved
	00 = 3 address cycles		BT_MEM_CTL = WEIM(NOR)
	01 = 4 address cycles		00 = 16 bit A/D mux
	10 = 5 address cycles		01 = 16 bit A/D unmux
	11 = Reserved		10 = Reserved
	BT_MEM_CTL = Expansion		11 = Reserved
	00 = SDMMC/MovinAND/CE_AFA		BT_MEM_CTL = Expansion
	01 = Reserved		00 = 2-Address SPI(16-bit)
	10 = Serial ROM via I2C		01 = 3-Address SPI(24-bit)
	11 = Serial ROM via SPI		10/11 = Reserved

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