

# i.MX8M MINI IOMUX

Name	Default	Alt0	Alt1	Alt2	Alt3	Alt4	Alt5	Alt6 Special EN	Alt7 Special EN
TEST_MODE	iu.TEST_MODE	iu.TEST_MODE	cmrcgpmix_BOOT_MODE[0]	cmrcgpmix_BOOT_MODE[1]					
BOOT_MODE0	iu.Boot_Mode0	iu.Boot_Mode0	cmrcgpmix_BOOT_MODE[0]	cmrcgpmix_BOOT_MODE[1]					
JTAG_M0D	iu.JTAG_M0D	iu.JTAG_M0D	ctag_wrapper.MODE	ctag_wrapper.MODE					
JTAG_TST_B	iu.JTAG_TST_B	iu.JTAG_TST_B	ctag_wrapper.TST_B	ctag_wrapper.TST_B					
JTAG_TMR	iu.JTAG_TMR	iu.JTAG_TMR	ctag_wrapper.TMR	ctag_wrapper.TMR					
JTAG_TDO	iu.JTAG_TDO	iu.JTAG_TDO	ctag_wrapper.TDO	ctag_wrapper.TDO					
JTAG_TDI	iu.JTAG_TDI	iu.JTAG_TDI	ctag_wrapper.RTS	ctag_wrapper.RTS					
RTC_XTAL0	iu.RTC_XTAL0	iu.RTC_XTAL0	anamix.RTC	anamix.RTC					
PMIC_STBY_REQ	iu.PMIC_STBY_REQ	iu.PMIC_STBY_REQ	anamix.PMIC_STBY_REQ	anamix.PMIC_STBY_REQ					
PMIC_ON_REQ	iu.PMIC_ON_REQ	iu.PMIC_ON_REQ	anamix.PMIC_ON_REQ	anamix.PMIC_ON_REQ					
ONOFF	iu.ONOFF	iu.ONOFF	anamix.ONOFF	anamix.ONOFF					
SD0_RESET_B	iu.SD0_RESET_B	iu.SD0_RESET_B	anamix.RTC_RESET_B	anamix.RTC_RESET_B					
GP01_I001	spi0.I01	spi0.I01	spi0.I01	spi0.I01					
GP01_I003	spi0.I03	spi0.I03	spi0.I03	spi0.I03					
GP01_I005	spi0.I05	spi0.I05	spi0.I05	spi0.I05					
GP01_I006	spi0.I06	spi0.I06	spi0.I06	spi0.I06					
GP01_I008	spi0.I08	spi0.I08	spi0.I08	spi0.I08					
GP01_I010	spi0.I10	spi0.I10	spi0.I10	spi0.I10					
GP01_I012	spi0.I12	spi0.I12	spi0.I12	spi0.I12					
GP01_I014	spi0.I14	spi0.I14	spi0.I14	spi0.I14					
GP01_I015	spi0.I15	spi0.I15	spi0.I15	spi0.I15					
ENET_M0D	iu.enet_m0d	iu.enet_m0d	enet1.10D	enet1.10D					
ENET_E0D	iu.enet_e0d	iu.enet_e0d	enet1.10E	enet1.10E					
ENET_T0D	iu.enet_t0d	iu.enet_t0d	enet1.10M	enet1.10M					
ENET_T2D	iu.enet_t2d	iu.enet_t2d	enet1.10T	enet1.10T					
ENET_T3D	iu.enet_t3d	iu.enet_t3d	enet1.10C	enet1.10C					
ENET_TX_CTL	iu.enet_tx_ctl	iu.enet_tx_ctl	enet1.10TX	enet1.10TX					
ENET_RX_CTL	iu.enet_rx_ctl	iu.enet_rx_ctl	enet1.10RX	enet1.10RX					
ENET_R0D	iu.enet_r0d	iu.enet_r0d	enet1.10R	enet1.10R					
ENET_R2D	iu.enet_r2d	iu.enet_r2d	enet1.10B	enet1.10B					
SD1_CLK	spi2.I00	spi2.I00	usdhc1.CLK	usdhc1.CLK					
SD1_DATA0	spi2.I01	spi2.I01	usdhc1.DATA0	usdhc1.DATA0					
SD1_DATA1	spi2.I03	spi2.I03	usdhc1.DATA1	usdhc1.DATA1					
SD1_DATA2	spi2.I05	spi2.I05	usdhc1.DATA2	usdhc1.DATA2					
SD1_DATA3	spi2.I07	spi2.I07	usdhc1.DATA3	usdhc1.DATA3					
SD1_DATA5	spi2.I09	spi2.I09	usdhc1.DATA5	usdhc1.DATA5					
SD1_DATA7	spi2.I11	spi2.I11	usdhc1.DATA7	usdhc1.DATA7					
SD2_RESET_B	spi2.I00	spi2.I00	usdhc2.RESET_B	usdhc2.RESET_B					
SD2_WP	spi2.I02	spi2.I02	usdhc2.WP	usdhc2.WP					
NAND_ALE	spi3.I01	spi3.I01	rawnand.ALE	rawnand.ALE					
NAND_CE1_B	spi3.I02	spi3.I02	rawnand.CE1_B	rawnand.CE1_B					
NAND_CE1_E	spi3.I03	spi3.I03	rawnand.CE1_E	rawnand.CE1_E					
NAND_CE3_B	spi3.I04	spi3.I04	rawnand.CE3_B	rawnand.CE3_B					
NAND_CE3_E	spi3.I05	spi3.I05	rawnand.CE3_E	rawnand.CE3_E					
NAND_DATA0	spi3.I06	spi3.I06	rawnand.DATA00	rawnand.DATA00					
NAND_DATA1	spi3.I07	spi3.I07	rawnand.DATA01	rawnand.DATA01					
NAND_DATA2	spi3.I08	spi3.I08	rawnand.DATA02	rawnand.DATA02					
NAND_DATA3	spi3.I09	spi3.I09	rawnand.DATA03	rawnand.DATA03					
NAND_DATA4	spi3.I10	spi3.I10	rawnand.DATA04	rawnand.DATA04					
NAND_DATA5	spi3.I11	spi3.I11	rawnand.DATA05	rawnand.DATA05					
NAND_DATA6	spi3.I12	spi3.I12	rawnand.DATA06	rawnand.DATA06					
NAND_DATA7	spi3.I13	spi3.I13	rawnand.DATA07	rawnand.DATA07					
NAND_R0D	spi3.I14	spi3.I14	rawnand.R0D	rawnand.R0D					
NAND_R1D	spi3.I15	spi3.I15	rawnand.R1D	rawnand.R1D					
NAND_READY_B	spi3.I16	spi3.I16	rawnand.REady_B	rawnand.REady_B					
NAND_WP_B	spi3.I18	spi3.I18	rawnand.WP_B	rawnand.WP_B					
SAI1_RXD	spi4.I01	spi4.I01	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXC	spi4.I02	spi4.I02	sa1.RX_BCLK	sa1.RX_BCLK					
SAI1_RXD0	spi4.I03	spi4.I03	sa1.RX_DATA[0]	sa1.RX_DATA[0]					
SAI1_RXD1	spi4.I04	spi4.I04	sa1.RX_DATA[1]	sa1.RX_DATA[1]					
SAI1_RXD2	spi4.I05	spi4.I05	sa1.RX_DATA[2]	sa1.RX_DATA[2]					
SAI1_RXD3	spi4.I06	spi4.I06	sa1.RX_DATA[3]	sa1.RX_DATA[3]					
SAI1_RXF5	spi4.I07	spi4.I07	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF6	spi4.I08	spi4.I08	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF7	spi4.I09	spi4.I09	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF8	spi4.I10	spi4.I10	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF9	spi4.I11	spi4.I11	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF10	spi4.I12	spi4.I12	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF11	spi4.I13	spi4.I13	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF12	spi4.I14	spi4.I14	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF13	spi4.I15	spi4.I15	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF14	spi4.I16	spi4.I16	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF15	spi4.I17	spi4.I17	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF16	spi4.I18	spi4.I18	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF17	spi4.I19	spi4.I19	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF18	spi4.I20	spi4.I20	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF19	spi4.I21	spi4.I21	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF20	spi4.I22	spi4.I22	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF21	spi4.I23	spi4.I23	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF22	spi4.I24	spi4.I24	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF23	spi4.I25	spi4.I25	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF24	spi4.I26	spi4.I26	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF25	spi4.I27	spi4.I27	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF26	spi4.I28	spi4.I28	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF27	spi4.I29	spi4.I29	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF28	spi4.I30	spi4.I30	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF29	spi4.I31	spi4.I31	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF30	spi4.I32	spi4.I32	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF31	spi4.I33	spi4.I33	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF32	spi4.I34	spi4.I34	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF33	spi4.I35	spi4.I35	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF34	spi4.I36	spi4.I36	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF35	spi4.I37	spi4.I37	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF36	spi4.I38	spi4.I38	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF37	spi4.I39	spi4.I39	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF38	spi4.I40	spi4.I40	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF39	spi4.I41	spi4.I41	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF40	spi4.I42	spi4.I42	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF41	spi4.I43	spi4.I43	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF42	spi4.I44	spi4.I44	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF43	spi4.I45	spi4.I45	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF44	spi4.I46	spi4.I46	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF45	spi4.I47	spi4.I47	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF46	spi4.I48	spi4.I48	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF47	spi4.I49	spi4.I49	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF48	spi4.I50	spi4.I50	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF49	spi4.I51	spi4.I51	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF50	spi4.I52	spi4.I52	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF51	spi4.I53	spi4.I53	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF52	spi4.I54	spi4.I54	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF53	spi4.I55	spi4.I55	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF54	spi4.I56	spi4.I56	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF55	spi4.I57	spi4.I57	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF56	spi4.I58	spi4.I58	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF57	spi4.I59	spi4.I59	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF58	spi4.I60	spi4.I60	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF59	spi4.I61	spi4.I61	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF60	spi4.I62	spi4.I62	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF61	spi4.I63	spi4.I63	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF62	spi4.I64	spi4.I64	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF63	spi4.I65	spi4.I65	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF64	spi4.I66	spi4.I66	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF65	spi4.I67	spi4.I67	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF66	spi4.I68	spi4.I68	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF67	spi4.I69	spi4.I69	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF68	spi4.I70	spi4.I70	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF69	spi4.I71	spi4.I71	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF70	spi4.I72	spi4.I72	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF71	spi4.I73	spi4.I73	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF72	spi4.I74	spi4.I74	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF73	spi4.I75	spi4.I75	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF74	spi4.I76	spi4.I76	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF75	spi4.I77	spi4.I77	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF76	spi4.I78	spi4.I78	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF77	spi4.I79	spi4.I79	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF78	spi4.I80	spi4.I80	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF79	spi4.I81	spi4.I81	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF80	spi4.I82	spi4.I82	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF81	spi4.I83	spi4.I83	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF82	spi4.I84	spi4.I84	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF83	spi4.I85	spi4.I85	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF84	spi4.I86	spi4.I86	sa1.RX_SYNC	sa1.RX_SYNC					
SAI1_RXF85	spi4.I87	spi4.I87	sa1.RX_SYNC	sa1.RX_SYNC					</td