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0xD2,
/* Image Length */
0x04, 0x80,
/* Version */
0x41,

/* COMMANDS */

/* group: 'Imported Commands' */
/* #1.1, command header bytes for merged 'Write - value'
command */
0xCC, 0x03, 0x84, 0x04,
/* #1.2, command: write_value, address: CCM_CCGR0, value:
0xFFFFFFFF, size: 4 */
0x40, 0x0F, 0xC0, 0x68, 0xFF, 0xFF, 0xFF, 0xFF,
/* #1.3, command: write_value, address: CCM_CCGR1, value:
0xFFFFFFFF, size: 4 */
0x40, 0x0F, 0xC0, 0x6C, 0xFF, 0xFF, 0xFF, 0xFF,
/* #1.4, command: write_value, address: CCM_CCGR2, value:
0xFFFFFFFF, size: 4 */
0x40, 0x0F, 0xC0, 0x70, 0xFF, 0xFF, 0xFF, 0xFF,
/* #1.5, command: write_value, address: CCM_CCGR3, value:
0xFFFFFFFF, size: 4 */
0x40, 0x0F, 0xC0, 0x74, 0xFF, 0xFF, 0xFF, 0xFF,
/* #1.6, command: write_value, address: CCM_CCGR4, value:
0xFFFFFFFF, size: 4 */
0x40, 0x0F, 0xC0, 0x78, 0xFF, 0xFF, 0xFF, 0xFF,
/* #1.7, command: write_value, address: CCM_CCGR5, value:
0xFFFFFFFF, size: 4 */
0x40, 0x0F, 0xC0, 0x7C, 0xFF, 0xFF, 0xFF, 0xFF,
/* #1.8, command: write_value, address: CCM_CCGR6, value:
0xFFFFFFFF, size: 4 */
0x40, 0x0F, 0xC0, 0x80, 0xFF, 0xFF, 0xFF, 0xFF,
/* #1.9, command: write_value, address: CCM_ANALOG_PLL_SYS,
value: 0x2001, size: 4 */
0x40, 0x0D, 0x80, 0x30, 0x00, 0x00, 0x20, 0x01,
/* #1.10, command: write_value, address: CCM_ANALOG_PFD_528,
value: 0x2001, size: 4 */
0x40, 0x0D, 0x81, 0x00, 0x00, 0x00, 0x80, 0x00,
/* #1.11, command: write_value, address: CCM_CBCDR, value:
0xA8340, size: 4 */
0x40, 0x0F, 0xC0, 0x14, 0x00, 0x0A, 0x83, 0x40,
/* #2.1, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_00, value: 0x00, size: 4 */
0x40, 0x1F, 0x80, 0x14, 0x00, 0x00, 0x00, 0x00,
/* #2.2, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_01, value: 0x00, size: 4 */
0x40, 0x1F, 0x80, 0x18, 0x00, 0x00, 0x00, 0x00,
/* #2.3, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_02, value: 0x00, size: 4 */
0x40, 0x1F, 0x80, 0x1C, 0x00, 0x00, 0x00, 0x00,
/* #2.4, command: write_value, address:

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IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_03, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x20, 0x00, 0x00, 0x00, 0x00,
    /* #2.5, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_04, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x24, 0x00, 0x00, 0x00, 0x00,
    /* #2.6, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_05, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x28, 0x00, 0x00, 0x00, 0x00,
    /* #2.7, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_06, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x2C, 0x00, 0x00, 0x00, 0x00,
    /* #2.8, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_07, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x30, 0x00, 0x00, 0x00, 0x00,
    /* #2.9, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_08, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x34, 0x00, 0x00, 0x00, 0x00,
    /* #2.10, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_09, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x38, 0x00, 0x00, 0x00, 0x00,
    /* #2.11, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_10, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x3C, 0x00, 0x00, 0x00, 0x00,
    /* #2.12, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_11, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x40, 0x00, 0x00, 0x00, 0x00,
    /* #2.13, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_12, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x44, 0x00, 0x00, 0x00, 0x00,
    /* #2.14, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_13, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x48, 0x00, 0x00, 0x00, 0x00,
    /* #2.15, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_14, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x4C, 0x00, 0x00, 0x00, 0x00,
    /* #2.16, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_15, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x50, 0x00, 0x00, 0x00, 0x00,
    /* #2.17, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_16, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x54, 0x00, 0x00, 0x00, 0x00,
    /* #2.18, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_17, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x58, 0x00, 0x00, 0x00, 0x00,
    /* #2.19, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_18, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x5C, 0x00, 0x00, 0x00, 0x00,
    /* #2.20, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_19, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x60, 0x00, 0x00, 0x00, 0x00,
    /* #2.21, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_20, value: 0x00, size: 4 */

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    0x40, 0x1F, 0x80, 0x64, 0x00, 0x00, 0x00, 0x00,
    /* #2.22, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_21, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x68, 0x00, 0x00, 0x00, 0x00,
    /* #2.23, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_22, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x6C, 0x00, 0x00, 0x00, 0x00,
    /* #2.24, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_23, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x70, 0x00, 0x00, 0x00, 0x00,
    /* #2.25, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_24, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x74, 0x00, 0x00, 0x00, 0x00,
    /* #2.26, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_25, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x78, 0x00, 0x00, 0x00, 0x00,
    /* #2.27, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_26, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x7C, 0x00, 0x00, 0x00, 0x00,
    /* #2.28, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_27, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x80, 0x00, 0x00, 0x00, 0x00,
    /* #2.29, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_28, value: 0x10, size: 4 */
    0x40, 0x1F, 0x80, 0x84, 0x00, 0x00, 0x00, 0x10,
    /* #2.30, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_29, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x88, 0x00, 0x00, 0x00, 0x00,
    /* #2.31, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_30, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x8C, 0x00, 0x00, 0x00, 0x00,
    /* #2.32, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_31, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x90, 0x00, 0x00, 0x00, 0x00,
    /* #2.33, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_32, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x94, 0x00, 0x00, 0x00, 0x00,
    /* #2.34, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_33, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x98, 0x00, 0x00, 0x00, 0x00,
    /* #2.35, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_34, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0x9C, 0x00, 0x00, 0x00, 0x00,
    /* #2.36, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_35, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0xA0, 0x00, 0x00, 0x00, 0x00,
    /* #2.37, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_36, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0xA4, 0x00, 0x00, 0x00, 0x00,
    /* #2.38, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_37, value: 0x00, size: 4 */
    0x40, 0x1F, 0x80, 0xA8, 0x00, 0x00, 0x00, 0x00,

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/* #2.39, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_38, value: 0x00, size: 4 */
0x40, 0x1F, 0x80, 0xAC, 0x00, 0x00, 0x00, 0x00,
/* #2.40, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_39, value: 0x00, size: 4 */
0x40, 0x1F, 0x80, 0xB0, 0x00, 0x00, 0x00, 0x00,
/* #2.41, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_40, value: 0x00, size: 4 */
0x40, 0x1F, 0x80, 0xB4, 0x00, 0x00, 0x00, 0x00,
/* #2.42, command: write_value, address:
IOMUXC_SW_MUX_CTL_PAD_GPIO_EMC_41, value: 0x00, size: 4 */
0x40, 0x1F, 0x80, 0xB8, 0x00, 0x00, 0x00, 0x00,
/* #2.43 , command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_01, value: 0x00, size: 4 */
0x40, 0x1F, 0x81, 0x00, 0x00, 0x00, 0x00, 0x00,
/* #2.44, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_31, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x04, 0x00, 0x00, 0x00, 0xE1,
/* #2.45, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_32, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x08, 0x00, 0x00, 0x00, 0xE1,
/* #2.46, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_33, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x0C, 0x00, 0x00, 0x00, 0xE1,
/* #2.47, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_34, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x10, 0x00, 0x00, 0x00, 0xE1,
/* #2.48, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_35, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x14, 0x00, 0x00, 0x00, 0xE1,
/* #2.49, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_36, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x18, 0x00, 0x00, 0x00, 0xE1,
/* #2.50, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_37, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x1C, 0x00, 0x00, 0x00, 0xE1,
/* #2.51, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_38, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x20, 0x00, 0x00, 0x00, 0xE1,
/* #2.52, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_39, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x24, 0x00, 0x00, 0x00, 0xE1,
/* #2.53, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_40, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x28, 0x00, 0x00, 0x00, 0xE1,
/* #2.54, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_EMC_41, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x2C, 0x00, 0x00, 0x00, 0xE1,
/* #2.55, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_00, value: 0xE1, size: 4 */
0x40, 0x1F, 0x82, 0x30, 0x00, 0x00, 0x00, 0xE1,
/* #2.56, command: write_value, address:

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IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_01, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x34, 0x00, 0x00, 0x00, 0xE1,
    /* #2.57, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_02, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x38, 0x00, 0x00, 0x00, 0xE1,
    /* #2.58, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_03, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x3C, 0x00, 0x00, 0x00, 0xE1,
    /* #2.59, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_04, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x40, 0x00, 0x00, 0x00, 0xE1,
    /* #2.60, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_05, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x44, 0x00, 0x00, 0x00, 0xE1,
    /* #2.61, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_06, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x48, 0x00, 0x00, 0x00, 0xE1,
    /* #2.62, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_07, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x4C, 0x00, 0x00, 0x00, 0xE1,
    /* #2.63, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_08, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x50, 0x00, 0x00, 0x00, 0xE1,
    /* #2.64, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_09, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x54, 0x00, 0x00, 0x00, 0xE1,
    /* #2.65, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_10, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x58, 0x00, 0x00, 0x00, 0xE1,
    /* #2.66, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_11, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x5C, 0x00, 0x00, 0x00, 0xE1,
    /* #2.67, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_12, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x60, 0x00, 0x00, 0x00, 0xE1,
    /* #2.68, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_13, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x64, 0x00, 0x00, 0x00, 0xE1,
    /* #2.69, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_14, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x68, 0x00, 0x00, 0x00, 0xE1,
    /* #2.70, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B0_15, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x6C, 0x00, 0x00, 0x00, 0xE1,
    /* #2.71, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_00, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x70, 0x00, 0x00, 0x00, 0xE1,
    /* #2.72, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_01, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x74, 0x00, 0x00, 0x00, 0xE1,
    /* #2.73, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_02, value: 0xE1, size: 4 */

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    0x40, 0x1F, 0x82, 0x78, 0x00, 0x00, 0x00, 0xE1,
    /* #2.74, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_03, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x7C, 0x00, 0x00, 0x00, 0xE1,
    /* #2.75, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_04, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x80, 0x00, 0x00, 0x00, 0xE1,
    /* #2.76, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_05, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x84, 0x00, 0x00, 0x00, 0xE1,
    /* #2.77, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_06, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x88, 0x00, 0x00, 0x00, 0xE1,
    /* #2.78, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_07, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x8C, 0x00, 0x00, 0x00, 0xE1,
    /* #2.79, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_08, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x90, 0x00, 0x00, 0x00, 0xE1,
    /* #2.80, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_09, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x94, 0x00, 0x00, 0x00, 0xE1,
    /* #2.81, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_10, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x98, 0x00, 0x00, 0x00, 0xE1,
    /* #2.82, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_11, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0x9C, 0x00, 0x00, 0x00, 0xE1,
    /* #2.83, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_12, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0xA0, 0x00, 0x00, 0x00, 0xE1,
    /* #2.84, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_13, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0xA4, 0x00, 0x00, 0x00, 0xE1,
    /* #2.85, command: write_value, address:
IOMUXC_SW_PAD_CTL_PAD_GPIO_AD_B1_14, value: 0xE1, size: 4 */
    0x40, 0x1F, 0x82, 0xA8, 0x00, 0x00, 0x00, 0xE1,
    /* #2.86, command: write_value, address: SEMC_MCR, value:
0x10000004, size: 4 */
    0x40, 0x2F, 0x00, 0x00, 0x10, 0x00, 0x00, 0x04,
    /* #2.87, command: write_value, address: SEMC_BMCR0, value:
0x30524, size: 4 */
    0x40, 0x2F, 0x00, 0x08, 0x00, 0x03, 0x05, 0x24,
    /* #2.88, command: write_value, address: SEMC_BMCR1, value:
0x6030524, size: 4 */
    0x40, 0x2F, 0x00, 0x0C, 0x06, 0x03, 0x05, 0x24,
    /* #2.89, command: write_value, address: SEMC_BR0, value:
0x8000001B, size: 4 */
    0x40, 0x2F, 0x00, 0x10, 0x80, 0x00, 0x00, 0x1B,
    /* #2.90, command: write_value, address: SEMC_BR1, value:
0x8200001B, size: 4 */
    0x40, 0x2F, 0x00, 0x14, 0x84, 0x00, 0x00, 0x1B,

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    /* #2.91, command: write_value, address: SEMC_BR2, value:
0x8400001B, size: 4 */
    0x40, 0x2F, 0x00, 0x18, 0x88, 0x00, 0x00, 0x1B,
    /* #2.92, command: write_value, address: SEMC_IOCRR, value:
0x7988, size: 4 */
    0x40, 0x2F, 0x00, 0x04, 0x00, 0x00, 0x79, 0x88,
    /* #2.93, command: write_value, address: SEMC_SDRAMCR0,
value: 0xF37, size: 4 */
    0x40, 0x2F, 0x00, 0x40, 0x00, 0x00, 0x0F, 0x37,
    /* #2.94, command: write_value, address: SEMC_SDRAMCR1,
value: 0x652922, size: 4 */
    0x40, 0x2F, 0x00, 0x44, 0x00, 0x65, 0x29, 0x22,
    /* #2.95, command: write_value, address: SEMC_SDRAMCR2,
value: 0x10920, size: 4 */
    0x40, 0x2F, 0x00, 0x48, 0x00, 0x01, 0x09, 0x20,
    /* #2.96, command: write_value, address: SEMC_SDRAMCR3,
value: 0x50210A08, size: 4 */
    0x40, 0x2F, 0x00, 0x4C, 0x50, 0x21, 0x0A, 0x08,
    /* #2.97, command: write_value, address: SEMC_DBICR0, value:
0x21, size: 4 */
    0x40, 0x2F, 0x00, 0x80, 0x00, 0x00, 0x00, 0x21,
    /* #2.98, command: write_value, address: SEMC_DBICR1, value:
0x888888, size: 4 */
    0x40, 0x2F, 0x00, 0x84, 0x00, 0x88, 0x88, 0x88,
    /* #2.99, command: write_value, address: SEMC_IPCR1, value:
0x02, size: 4 */
    0x40, 0x2F, 0x00, 0x94, 0x00, 0x00, 0x00, 0x02,
    /* #2.100, command: write_value, address: SEMC_IPCR2, value:
0x00, size: 4 */
    0x40, 0x2F, 0x00, 0x98, 0x00, 0x00, 0x00, 0x00,

/*SDRAM1*/
    /* #3.0, command: write_value, address: SEMC_IPCR0, value:
0x80000000, size: 4 */
    0x40, 0x2F, 0x00, 0x90, 0x80, 0x00, 0x00, 0x00,
    /* #3.1, command: write_value, address: SEMC_IPCMD, value:
0xA55A000F, size: 4 */
    0x40, 0x2F, 0x00, 0x9C, 0xA5, 0x5A, 0x00, 0x0F,
    /* #3.2, command: check_any_bit_set, address: SEMC_INTR,
value: 0x01, size: 4 */
    0xCF, 0x00, 0x0C, 0x1C, 0x40, 0x2F, 0x00, 0x3C, 0x00, 0x00,
0x00, 0x01,
    /* #3.3, command header bytes for merged 'Write - value'
command */
    0xCC, 0x00, 0x14, 0x04,
    /* #3.4, command: write_value, address: SEMC_IPCR0, value:
0x80000000, size: 4 */
    0x40, 0x2F, 0x00, 0x90, 0x80, 0x00, 0x00, 0x00,
    /* #3.5, command: write_value, address: SEMC_IPCMD, value:
0xA55A000C, size: 4 */
    0x40, 0x2F, 0x00, 0x9C, 0xA5, 0x5A, 0x00, 0x0C,
    /* #3.6, command: check_any_bit_set, address: SEMC_INTR,

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value: 0x01, size: 4 */
    0xCF, 0x00, 0x0C, 0x1C, 0x40, 0x2F, 0x00, 0x3C, 0x00, 0x00,
0x00, 0x01,
    /* #3.7, command header bytes for merged 'Write - value'
command */
    0xCC, 0x00, 0x14, 0x04,
    /* #3.8, command: write_value, address: SEMC_IPCR0, value:
0x80000000, size: 4 */
    0x40, 0x2F, 0x00, 0x90, 0x80, 0x00, 0x00, 0x00,
    /* #3.9, command: write_value, address: SEMC_IPCMD, value:
0xA55A000C, size: 4 */
    0x40, 0x2F, 0x00, 0x9C, 0xA5, 0x5A, 0x00, 0x0C,
    /* #3.10, command: check_any_bit_set, address: SEMC_INTR,
value: 0x01, size: 4 */
    0xCF, 0x00, 0x0C, 0x1C, 0x40, 0x2F, 0x00, 0x3C, 0x00, 0x00,
0x00, 0x01,
    /* #3.11, command header bytes for merged 'Write - value'
command */
    0xCC, 0x00, 0x1C, 0x04,
    /* #3.12, command: write_value, address: SEMC_IPTXDAT,
value: 0x33, size: 4 */
    0x40, 0x2F, 0x00, 0xA0, 0x00, 0x00, 0x00, 0x33,
    /* #3.13, command: write_value, address: SEMC_IPCR0, value:
0x80000000, size: 4 */
    0x40, 0x2F, 0x00, 0x90, 0x80, 0x00, 0x00, 0x00,
    /* #3.14, command: write_value, address: SEMC_IPCMD, value:
0xA55A000A, size: 4 */
    0x40, 0x2F, 0x00, 0x9C, 0xA5, 0x5A, 0x00, 0x0A,
    /* #3.15, command: check_any_bit_set, address: SEMC_INTR,
value: 0x01, size: 4 */
    0xCF, 0x00, 0x0C, 0x1C, 0x40, 0x2F, 0x00, 0x3C, 0x00, 0x00,
0x00, 0x01,

/*SDRAM2*/
    /* #4.0, command header bytes for merged 'Write - value'
command */
    0xCC, 0x00, 0x14, 0x04,
    /* #4.1, command: write_value, address: SEMC_IPCR0, value:
0x84000000, size: 4 */
    0x40, 0x2F, 0x00, 0x90, 0x84, 0x00, 0x00, 0x00,
    /* #4.2, command: write_value, address: SEMC_IPCMD, value:
0xA55A000F, size: 4 */
    0x40, 0x2F, 0x00, 0x9C, 0xA5, 0x5A, 0x00, 0x0F,
    /* #4.3, command: check_any_bit_set, address: SEMC_INTR,
value: 0x01, size: 4 */
    0xCF, 0x00, 0x0C, 0x1C, 0x40, 0x2F, 0x00, 0x3C, 0x00, 0x00,
0x00, 0x01,
    /* #4.4, command header bytes for merged 'Write - value'
command */
    0xCC, 0x00, 0x14, 0x04,
    /* #4.5, command: write_value, address: SEMC_IPCR0, value:

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0x84000000, size: 4 */
    0x40, 0x2F, 0x00, 0x90, 0x84, 0x00, 0x00, 0x00,
    /* #4.6, command: write_value, address: SEMC_IPCMD, value:
0xA55A000C, size: 4 */
    0x40, 0x2F, 0x00, 0x9C, 0xA5, 0x5A, 0x00, 0x0C,
    /* #4.7, command: check_any_bit_set, address: SEMC_INTR,
value: 0x01, size: 4 */
    0xCF, 0x00, 0x0C, 0x1C, 0x40, 0x2F, 0x00, 0x3C, 0x00, 0x00,
0x00, 0x01,
    /* #4.8, command header bytes for merged 'Write - value'
command */
    0xCC, 0x00, 0x14, 0x04,
    /* #4.9, command: write_value, address: SEMC_IPCR0, value:
0x84000000, size: 4 */
    0x40, 0x2F, 0x00, 0x90, 0x84, 0x00, 0x00, 0x00,
    /* #4.10, command: write_value, address: SEMC_IPCMD, value:
0xA55A000C, size: 4 */
    0x40, 0x2F, 0x00, 0x9C, 0xA5, 0x5A, 0x00, 0x0C,
    /* #4.11, command: check_any_bit_set, address: SEMC_INTR,
value: 0x01, size: 4 */
    0xCF, 0x00, 0x0C, 0x1C, 0x40, 0x2F, 0x00, 0x3C, 0x00, 0x00,
0x00, 0x01,
    /* #4.12, command header bytes for merged 'Write - value'
command */
    0xCC, 0x00, 0x1C, 0x04,
    /* #4.13, command: write_value, address: SEMC_IPTXDAT,
value: 0x33, size: 4 */
    0x40, 0x2F, 0x00, 0xA0, 0x00, 0x00, 0x00, 0x33,
    /* #4.14, command: write_value, address: SEMC_IPCR0, value:
0x84000000, size: 4 */
    0x40, 0x2F, 0x00, 0x90, 0x84, 0x00, 0x00, 0x00,
    /* #4.15, command: write_value, address: SEMC_IPCMD, value:
0xA55A000A, size: 4 */
    0x40, 0x2F, 0x00, 0x9C, 0xA5, 0x5A, 0x00, 0x0A,
    /* #4.16, command: check_any_bit_set, address: SEMC_INTR,
value: 0x01, size: 4 */
    0xCF, 0x00, 0x0C, 0x1C, 0x40, 0x2F, 0x00, 0x3C, 0x00, 0x00,
0x00, 0x01,

/* #5, command: write_value, address: SEMC_SDRAMCR3, value:
0x50210A09, REFRESH ALL size: 4 */

0xCC, 0x00, 0x0C, 0x04, 0x40, 0x2F, 0x00, 0x4C, 0x50, 0x21, 0x0A,
0x09

```