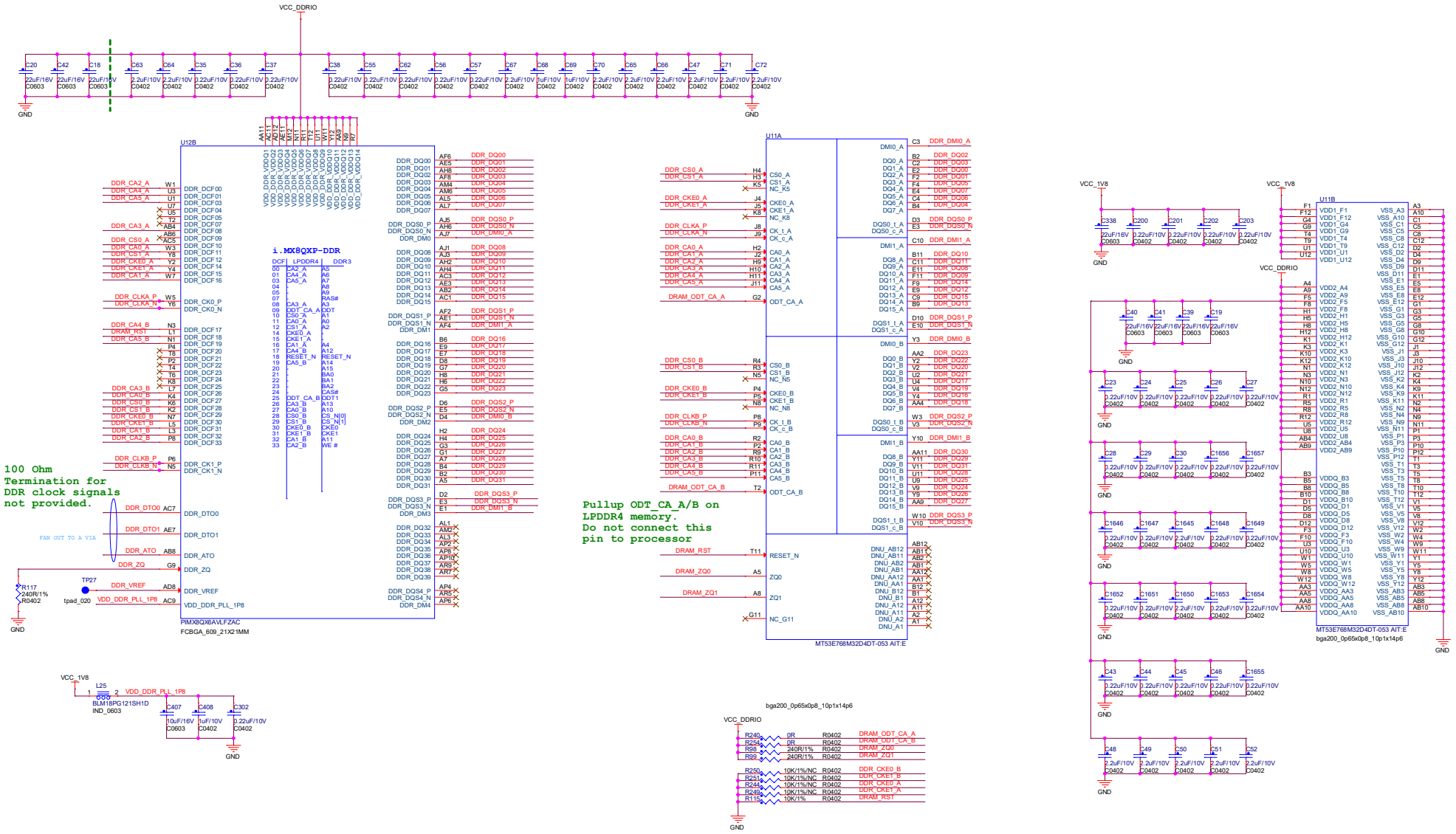
LPDDR4 DRAM


VCC_DDRIO
U12B
i.MX8QXP-DDR
PIMX8QX6AVLFZAC
FCBGA_609_21X21MM
U11A
MT53E768M32D4DT-053 AIT:E
U11B
MT53E768M32D4DT-053 AIT:E
bga200_0p65x0p8_10p1x14p6
100 Ohm Termination for DDR clock signals not provided.
Pullup ODT_CA_A/B on LPDDR4 memory. Do not connect this pin to processor
FAN OUT TO A VIA
VCC_1V8
VCC_DDRIO
GND