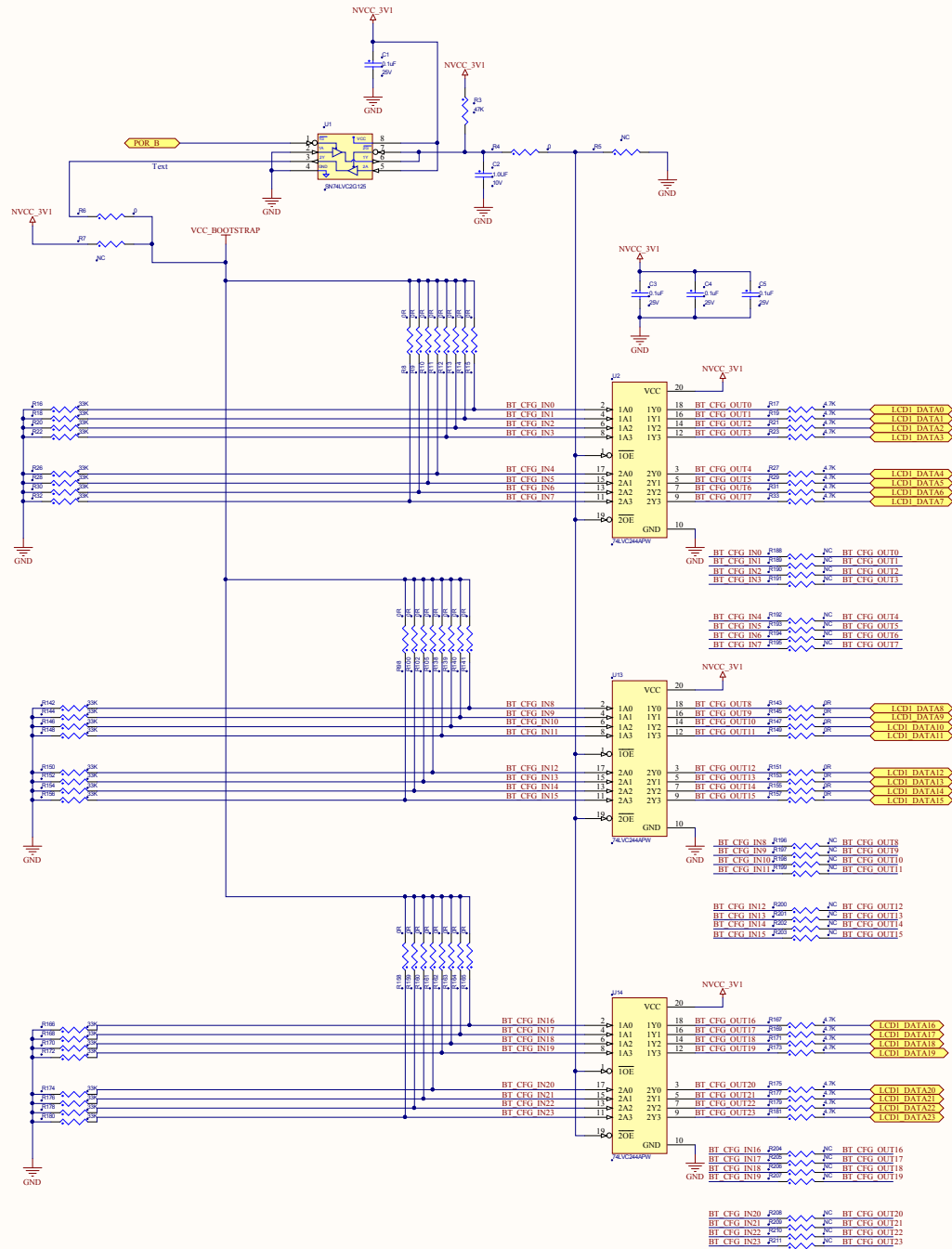


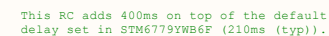
1、默认选择从ECSPI启动

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A2		
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File:	E:\开发云\Boot Strap.SchDoc	Drawn By:



RESET

WDOG B



This block is added in Rev.C for two purposes -

- i) Fix the SW reboot issue by toggling WDOG_B to issue power reset (ENGR00338067).
- ii) Delay the PMIC_PWRON>500ms for the 1st-time power-on (VSUNVS_3V0 is first applied), to ensure 32.768kHz xtal osc output is stable.

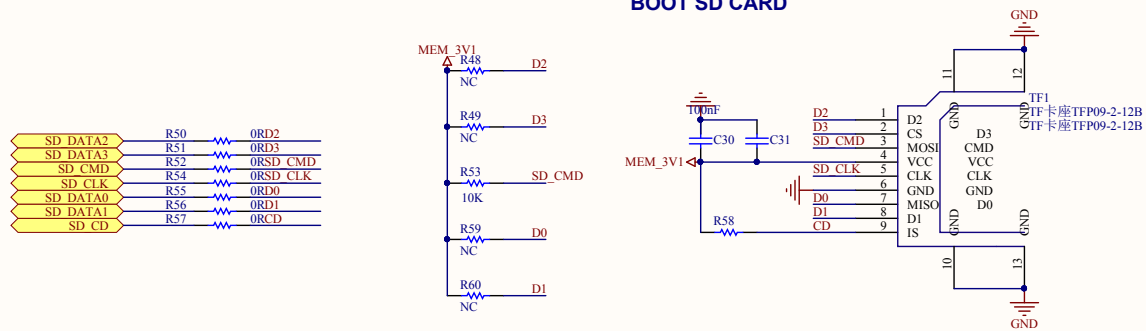


ICAP Classification:		FCP:	FIJO: <i>X</i>	PUBI:
Drawing Title: MCIMX6SX SDB				
Page Title: Buttons				
Size NC	Document Number SCM-27962 PDF: SPF-27962			Rev 0
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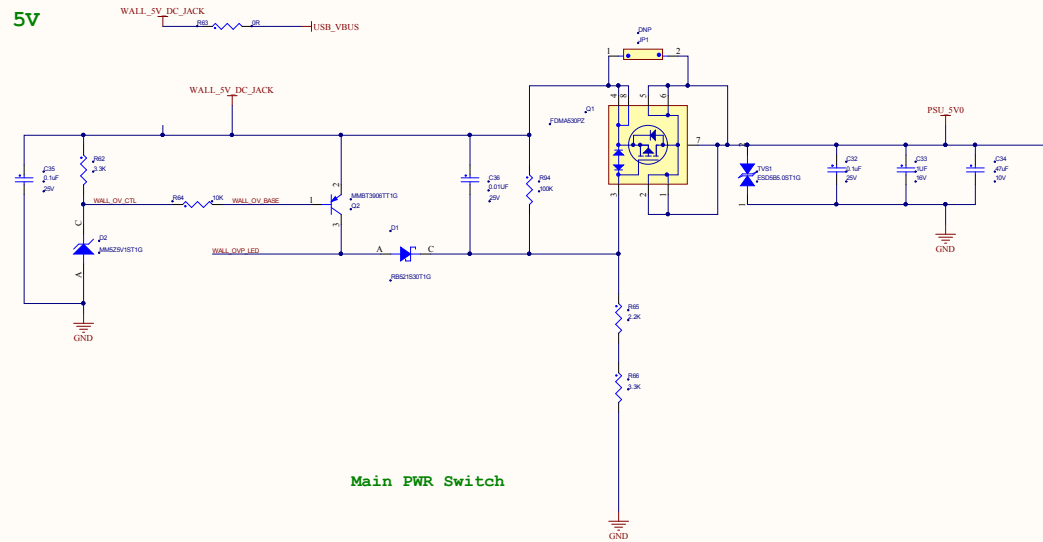


BOOT SD CARD



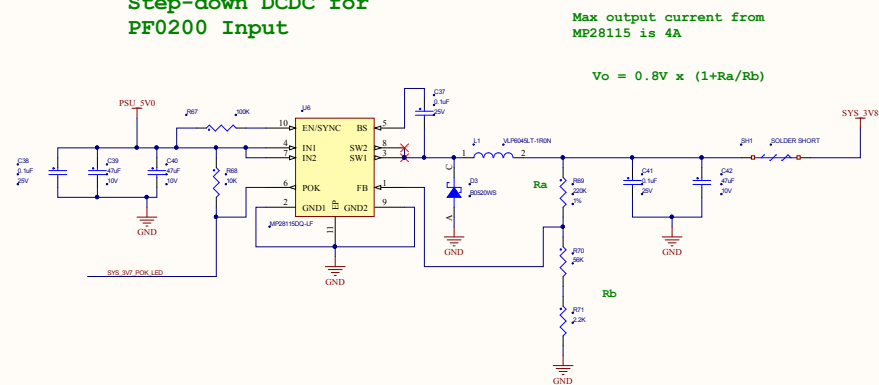
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File:	E:\百度云盘\Flash&SDcard SchDoc		Drawn By:

DC JACK 5V

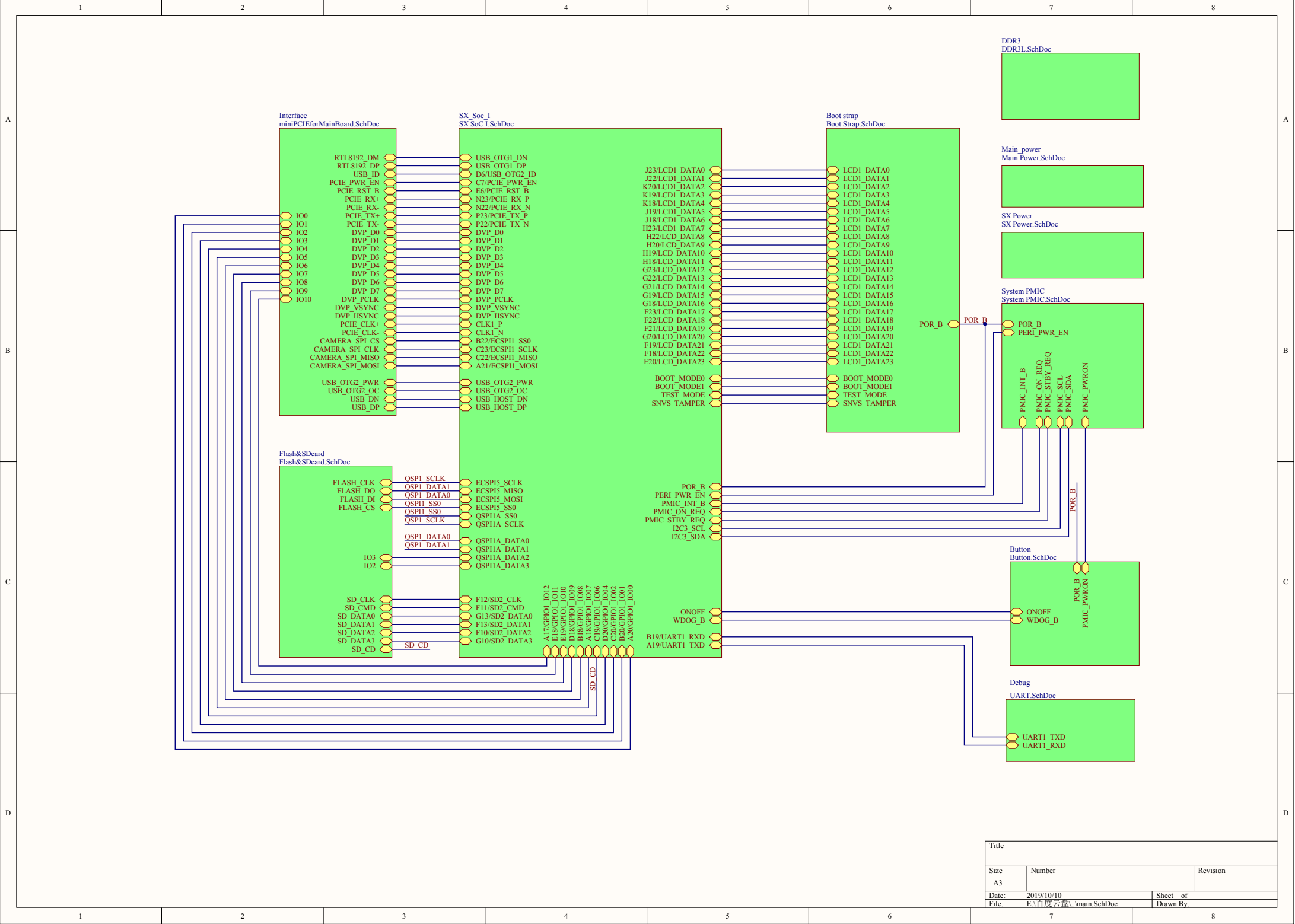


Main PWR Switch

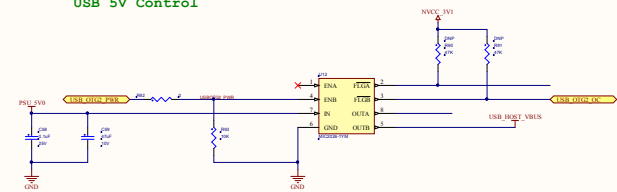
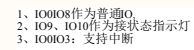
Step-down DCDC for
PF0200 Input



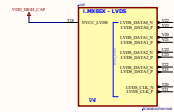
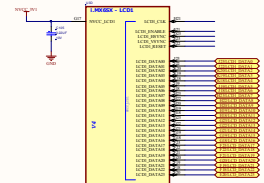
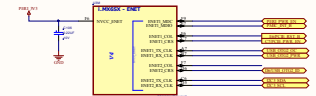
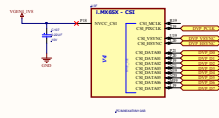
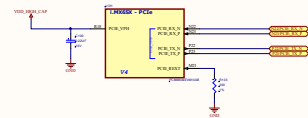
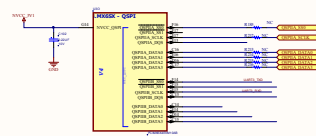
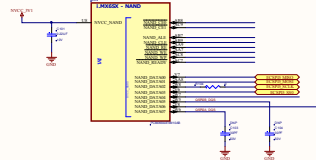
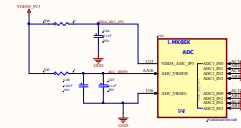
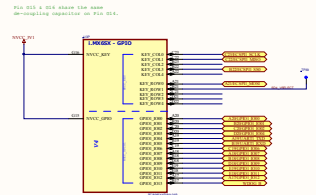
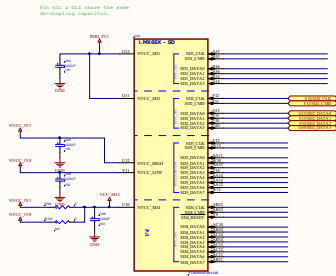
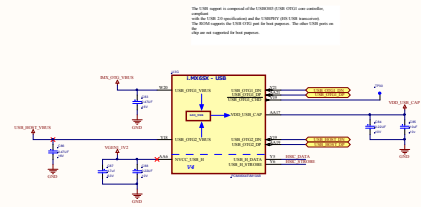
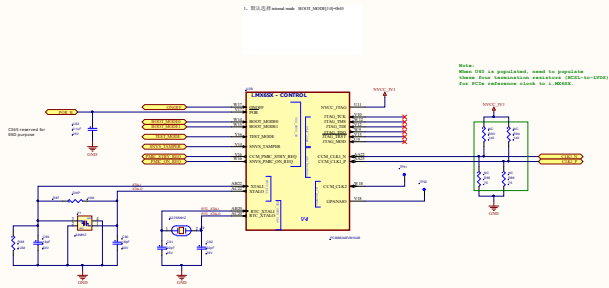
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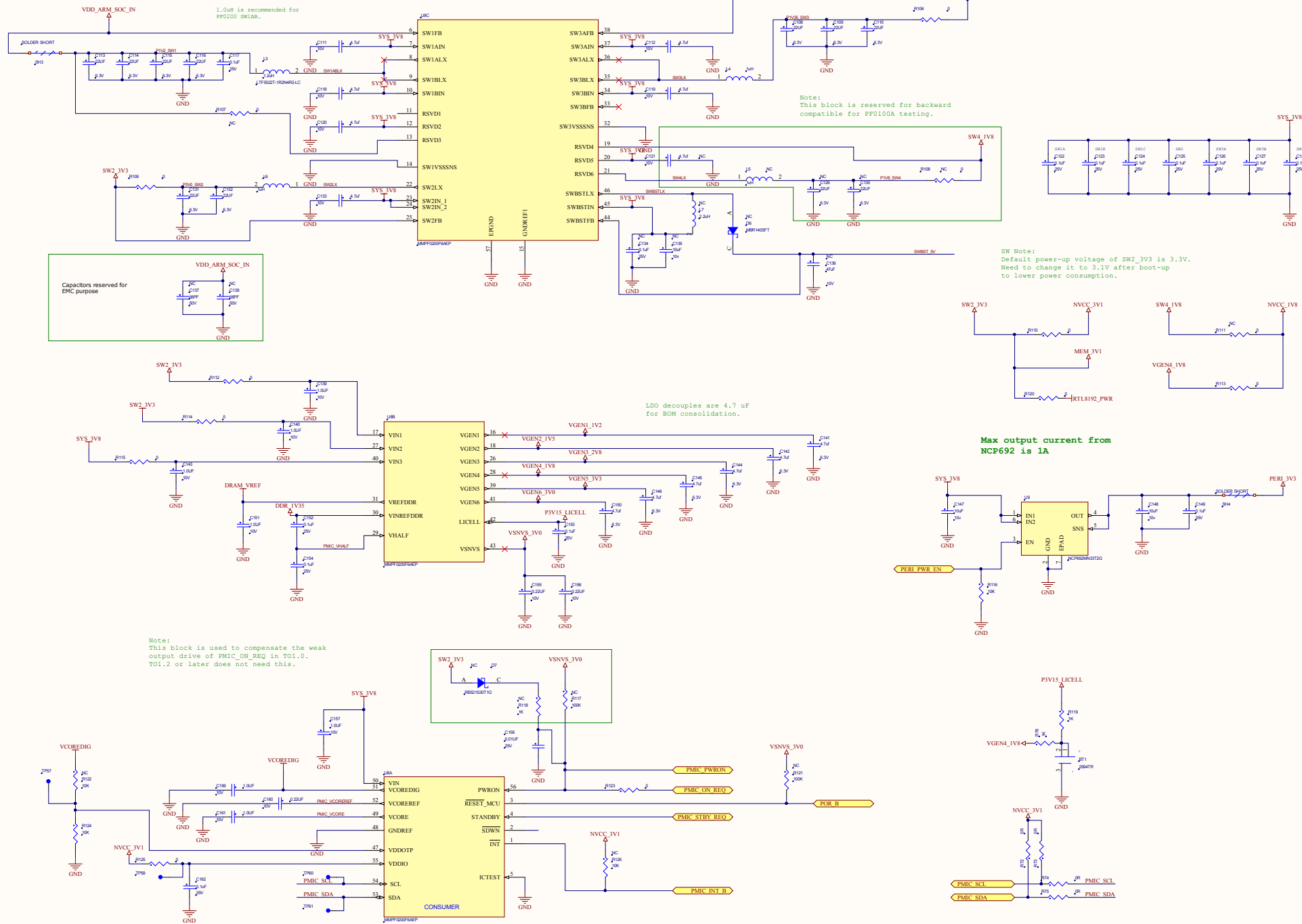


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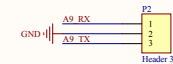
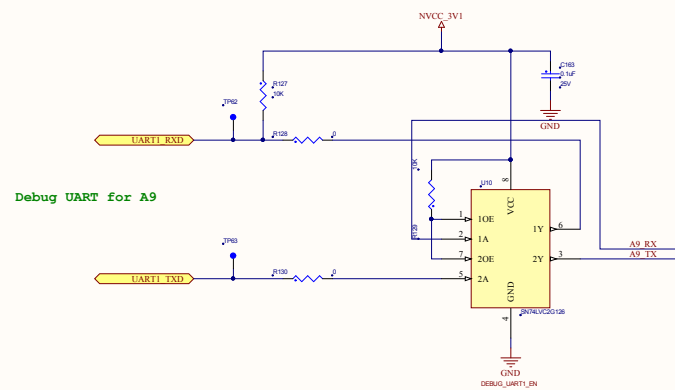
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File:	F:\11\PC\21\11\System PMIC SchDoc	Drawn By:

Debug UART to USB Converter



Title			
Size A2	Number		Revision
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