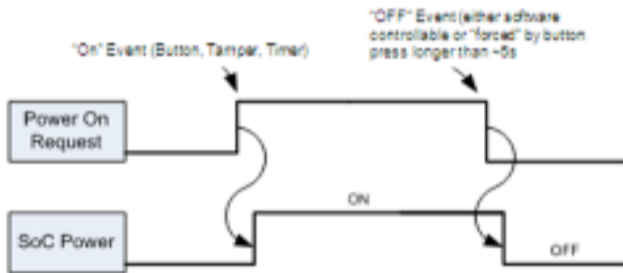
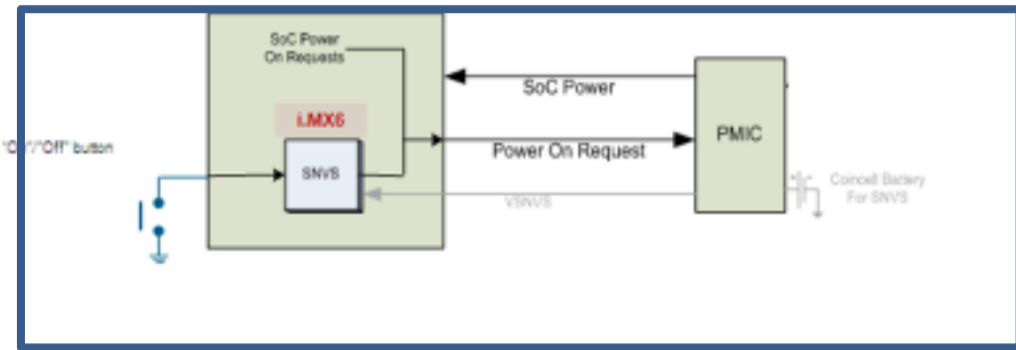


6.2.4.3 Power mode transitions

Table 6-8. Power mode transitions

Power mode	Configuration with external PMIC	Configuration with internal PMIC
ON, first time	<ol style="list-style-type: none"> 1. Either coin cell or SoC power supply is connected to SNVS. 2. When button is pressed, PMIC powers on. 	<ol style="list-style-type: none"> 1. Either coin cell or SoC power supply is connected to SNVS. 2. When button is pressed, 'state' goes ON, PMIC_ON_REQ goes '1'. 3. External regulator is enabled.
Normal ON to OFF, by button	<ol style="list-style-type: none"> 1. Button is pressed for a short duration on the external PMIC. 2. Interrupt request (irq) is sent to SoC from external PMIC. 3. SoC is programming PMIC for power off when standby is asserted. 	<ol style="list-style-type: none"> 1. SoC button is pressed for a short duration. 2. Interrupt request (irq) is sent to SoC from FSM. 3. Alarm timer is set up by software routine and started. 4. Upon alarm_in assertion to '1', PMIC_ON_REQ goes '0'.

Excerpt from Q&A: How is mx6 PMIC_ON_REQ under SW control?



mx6 Q & A and sch-28590_i.mx7d_saber_rev_d have the same circuit configuration

Excerpt from the schematic of sch-28590_i.mx7d_saber_rev_d

