

# i.MX6UL IOMUX

NAME	Default	ALTo	ALT1	ALT2	ALT3	ALT4	ALT5	ALT6	ALT7	ALT8	PAD DFU
TEST_MODE	tcu.TEST_MODE	tcu.TEST_MODE									100K PD
POR_B	src.POR_B	src.POR_B									100K PU
ONOFF	src.RESET_B	src.RESET_B									100K PU
SNVS_PMIC_ON_REQ	snvs_ip_wrapper.SNVS_WAKEUP_ALARM	snvs_ip_wrapper.PMIC_ON_REQ									100K PD
CCM_PMIC_STBY_REQ	ccm.PMIC_VSTBY_REQ	ccm.PMIC_VSTBY_REQ									100K PD
BOOT_MODE0	src.BOOT_MODE[0]	src.BOOT_MODE[0]									100K PD
BOOT_MODE1	src.BOOT_MODE[1]	src.BOOT_MODE[1]									100K PD
SNVS_TAMPER0	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[0]									Keeper
SNVS_TAMPER1	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[1]									Keeper
SNVS_TAMPER2	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[2]									Keeper
SNVS_TAMPER3	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[3]									Keeper
SNVS_TAMPER4	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[4]									Keeper
SNVS_TAMPER5	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[5]									Keeper
SNVS_TAMPER6	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[6]									Keeper
SNVS_TAMPER7	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[7]									Keeper
SNVS_TAMPER8	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[8]									Keeper
SNVS_TAMPER9	snvs_ip_wrapper.SNVS_TD1	snvs_ip_wrapper.TAMPER[9]									Keeper
JTAG_MOD	sjc.MOD	sjc.MOD									100K PU
JTAG_TMS	sjc.TMS	sjc.TMS									47K PU
JTAG_TDO	sjc.TDO	sjc.TDO									100K PU
JTAG_TDI	sjc.TDI	sjc.TDI									47K PU
JTAG_TX	sjc.TX	sjc.TX									47K PU
JTAG_TRST_B	sjc.TRSTB	sjc.TRSTB									Keeper
GPIO1_IO00	gpio1.IO[0]	gpio1.IO[0]									Keeper
GPIO1_IO01	gpio1.IO[1]	gpio1.IO[1]									Keeper
GPIO1_IO02	gpio1.IO[2]	gpio1.IO[2]									Keeper
GPIO1_IO03	gpio1.IO[3]	gpio1.IO[3]									Keeper
GPIO1_IO04	gpio1.IO[4]	gpio1.IO[4]									Keeper
GPIO1_IO05	gpio1.IO[5]	gpio1.IO[5]									Keeper
GPIO1_IO06	gpio1.IO[6]	gpio1.IO[6]									Keeper
GPIO1_IO07	gpio1.IO[7]	gpio1.IO[7]									Keeper
GPIO1_IO08	gpio1.IO[8]	gpio1.IO[8]									Keeper
GPIO1_IO09	gpio1.IO[9]	gpio1.IO[9]									Keeper
UART1_TXD	uart1.TXD	uart1.TXD									Keeper
UART1_RXD	uart1.RXD	uart1.RXD									Keeper
UART1_CTS	uart1.CTS_B	uart1.CTS_B									Keeper
UART1_RTS	uart1.RTS_B	uart1.RTS_B									Keeper
UART2_TXD	uart2.TXD	uart2.TXD									Keeper
UART2_RXD	uart2.RXD	uart2.RXD									Keeper
UART2_CTS	uart2.CTS_B	uart2.CTS_B									Keeper
UART2_RTS	uart2.RTS_B	uart2.RTS_B									Keeper
UART3_TXD	uart3.TXD	uart3.TXD									Keeper
UART3_RXD	uart3.RXD	uart3.RXD									Keeper
UART3_CTS	uart3.CTS_B	uart3.CTS_B									Keeper
UART3_RTS	uart3.RTS_B	uart3.RTS_B									Keeper
UART4_TXD	uart4.TXD	uart4.TXD									Keeper
UART4_RXD	uart4.RXD	uart4.RXD									Keeper
UART5_TXD	uart5.TXD	uart5.TXD									Keeper
UART5_RXD	uart5.RXD	uart5.RXD									Keeper
ENET1_RXD0	enet1.RDATA[0]	enet1.RDATA[0]									Keeper
ENET1_RXD1	enet1.RDATA[1]	enet1.RDATA[1]									Keeper
ENET1_CRS_DV	enet1.CRS_EN	enet1.CRS_EN									Keeper
ENET1_TXD0	enet1.TDATA[0]	enet1.TDATA[0]									Keeper
ENET1_TXD1	enet1.TDATA[1]	enet1.TDATA[1]									Keeper
ENET1_TXEN	enet1.TX_EN	enet1.TX_EN									Keeper
ENET1_TXCLK	enet1.TX_CLK	enet1.TX_CLK									Keeper
ENET1_RXER	enet1.RX_ER	enet1.RX_ER									Keeper
ENET2_RXD0	enet2.RDATA[0]	enet2.RDATA[0]									Keeper
ENET2_CRS_DV	enet2.CRS_EN	enet2.CRS_EN									Keeper
ENET2_TXD1	enet2.TDATA[1]	enet2.TDATA[1]									Keeper
ENET2_TXEN	enet2.TX_EN	enet2.TX_EN									Keeper
ENET2_TXCLK	enet2.TX_CLK	enet2.TX_CLK									Keeper
ENET2_RXER	enet2.RX_ER	enet2.RX_ER									Keeper
LCD_CLK	lcdif.CLK	lcdif.CLK									Keeper
LCD_ENABLE	lcdif.ENABLE	lcdif.ENABLE									Keeper
LCD_HSYNCR	lcdif.HSYNCR	lcdif.HSYNCR									Keeper
LCD_VSYNCR	lcdif.VSYNCR	lcdif.VSYNCR									Keeper
LCD_RESET	lcdif.RESET	lcdif.RESET									Keeper
LCD_DATA00	lcdif.DATA[0]	lcdif.DATA[0]									Keeper
LCD_DATA01	lcdif.DATA[1]	lcdif.DATA[1]									Keeper
LCD_DATA02	lcdif.DATA[2]	lcdif.DATA[2]									Keeper
LCD_DATA03	lcdif.DATA[3]	lcdif.DATA[3]									Keeper
LCD_DATA04	lcdif.DATA[4]	lcdif.DATA[4]									Keeper
LCD_DATA05	lcdif.DATA[5]	lcdif.DATA[5]									Keeper
LCD_DATA06	lcdif.DATA[6]	lcdif.DATA[6]									Keeper
LCD_DATA07	lcdif.DATA[7]	lcdif.DATA[7]									Keeper
LCD_DATA08	lcdif.DATA[8]	lcdif.DATA[8]									Keeper
LCD_DATA09	lcdif.DATA[9]	lcdif.DATA[9]									Keeper
LCD_DATA10	lcdif.DATA[10]	lcdif.DATA[10]									Keeper
LCD_DATA11	lcdif.DATA[11]	lcdif.DATA[11]									Keeper
LCD_DATA12	lcdif.DATA[12]	lcdif.DATA[12]									Keeper
LCD_DATA13	lcdif.DATA[13]	lcdif.DATA[13]									Keeper
LCD_DATA14	lcdif.DATA[14]	lcdif.DATA[14]									Keeper
LCD_DATA15	lcdif.DATA[15]	lcdif.DATA[15]									Keeper
LCD_DATA16	lcdif.DATA[16]	lcdif.DATA[16]									Keeper
LCD_DATA17	lcdif.DATA[17]	lcdif.DATA[17]									Keeper
LCD_DATA18	lcdif.DATA[18]	lcdif.DATA[18]									Keeper
LCD_DATA19	lcdif.DATA[19]	lcdif.DATA[19]									Keeper
LCD_DATA20	lcdif.DATA[20]	lcdif.DATA[20]									Keeper
LCD_DATA21	lcdif.DATA[21]	lcdif.DATA[21]									Keeper
LCD_DATA22	lcdif.DATA[22]	lcdif.DATA[22]									Keeper
LCD_DATA23	lcdif.DATA[23]	lcdif.DATA[23]									Keeper
NAND_RE_B	gpio4.IO[0]	gpio4.IO[0]									Keeper
NAND_WE_B	gpio4.IO[1]	gpio4.IO[1]									Keeper
NAND_DATA00	gpio4.IO[2]	gpio4.IO[2]									Keeper
NAND_DATA01	gpio4.IO[3]	gpio4.IO[3]									Keeper
NAND_DATA02	gpio4.IO[4]	gpio4.IO[4]									Keeper
NAND_DATA03	gpio4.IO[5]	gpio4.IO[5]									Keeper
NAND_DATA04	gpio4.IO[6]	gpio4.IO[6]									Keeper
NAND_DATA05	gpio4.IO[7]	gpio4.IO[7]									Keeper
NAND_DATA06	gpio4.IO[8]	gpio4.IO[8]									Keeper
NAND_DATA07	gpio4.IO[9]	gpio4.IO[9]									Keeper
NAND_ALE	gpio4.IO[10]	gpio4.IO[10]									Keeper
NAND_WP_B	gpio4.IO[11]	gpio4.IO[11]									Keeper
NAND_READY_B	gpio4.IO[12]	gpio4.IO[12]									Keeper
NAND_CE0_B	gpio4.IO[13]	gpio4.IO[13]									Keeper
NAND_CE1_B	gpio4.IO[14]	gpio4.IO[14]									Keeper
NAND_CLE	gpio4.IO[15]	gpio4.IO[15]									Keeper
NAND_DQS	gpio4.IO[16]	gpio4.IO[16]									Keeper
SD1_CMD	gpio2.IO[16]	gpio2.IO[16]									Keeper
SD1_CLK	gpio2.IO[17]	gpio2.IO[17]									Keeper
SD1_DATA0	gpio2.IO[18]	gpio2.IO[18]									Keeper
SD1_DATA1	gpio2.IO[19]	gpio2.IO[19]									Keeper
SD1_DATA2	gpio2.IO[20]	gpio2.IO[20]									Keeper
SD1_DATA3	gpio2.IO[21]	gpio2.IO[21]									Keeper
CSI_MCLK	gpio4.IO[17]	gpio4.IO[17]									Keeper
CSI_PIXCLK	gpio4.IO[18]	gpio4.IO[18]									Keeper
CSI_VSYNCR	gpio4.IO[19]	gpio4.IO[19]									Keeper
CSI_HSYNCR	gpio4.IO[20]	gpio4.IO[20]									Keeper
CSI_DATA00	gpio4.IO[21]	gpio4.IO[21]									Keeper
CSI_DATA01	gpio4.IO[22]	gpio4.IO[22]									Keeper
CSI_DATA02	gpio4.IO[23]	gpio4.IO[23]									Keeper
CSI_DATA03	gpio4.IO[24]	gpio4.IO[24]									Keeper
CSI_DATA04	gpio4.IO[25]	gpio4.IO[25]									Keeper
CSI_DATA05	gpio4.IO[26]	gpio4.IO[26]									Keeper
CSI_DATA06	gpio4.IO[27]	gpio4.IO[27]									Keeper
CSI_DATA07	gpio4.IO[28]	gpio4.IO[28]									Keeper

