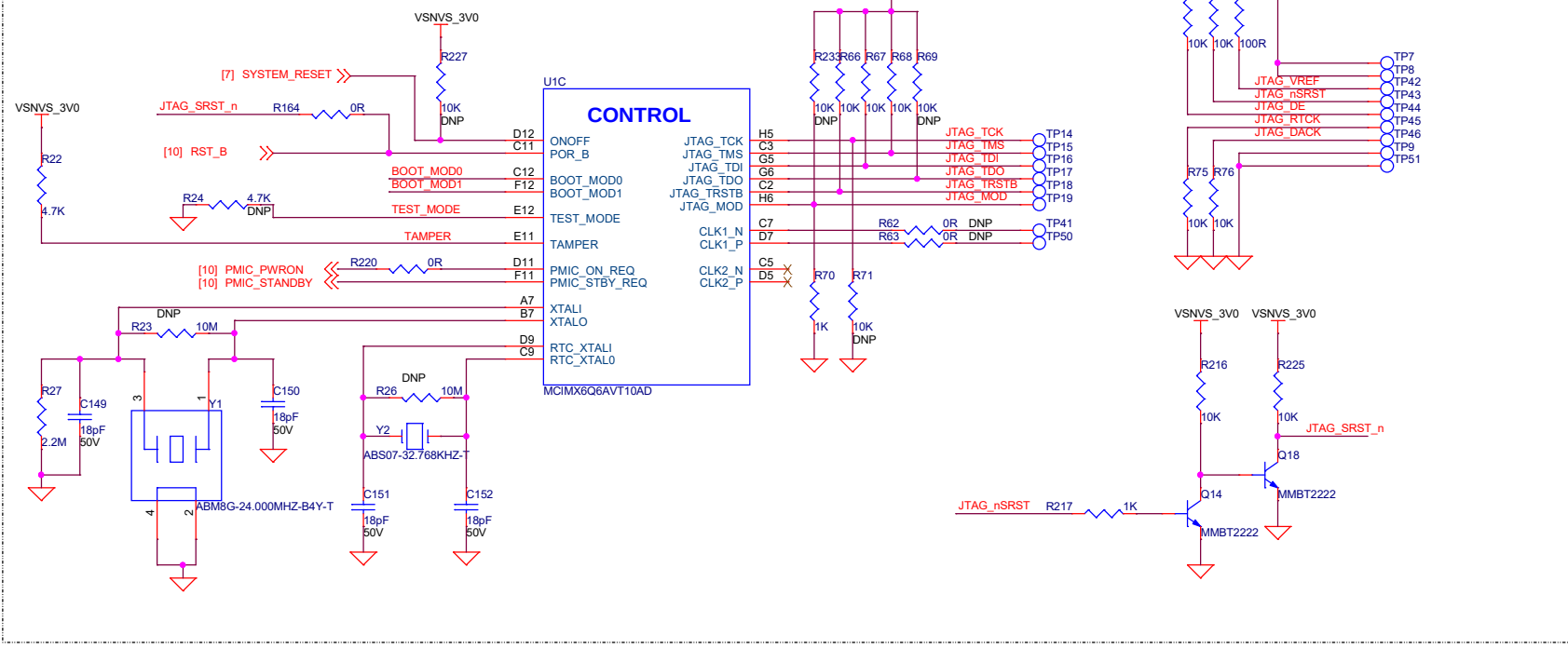
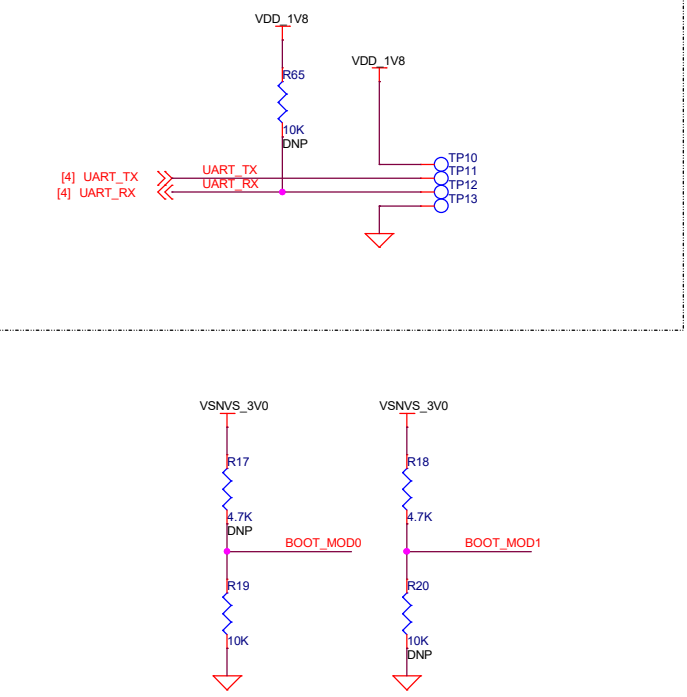


JTAG INTERFACE



UART DEBUG INTERFACE



BOOTMODE INTERFACE

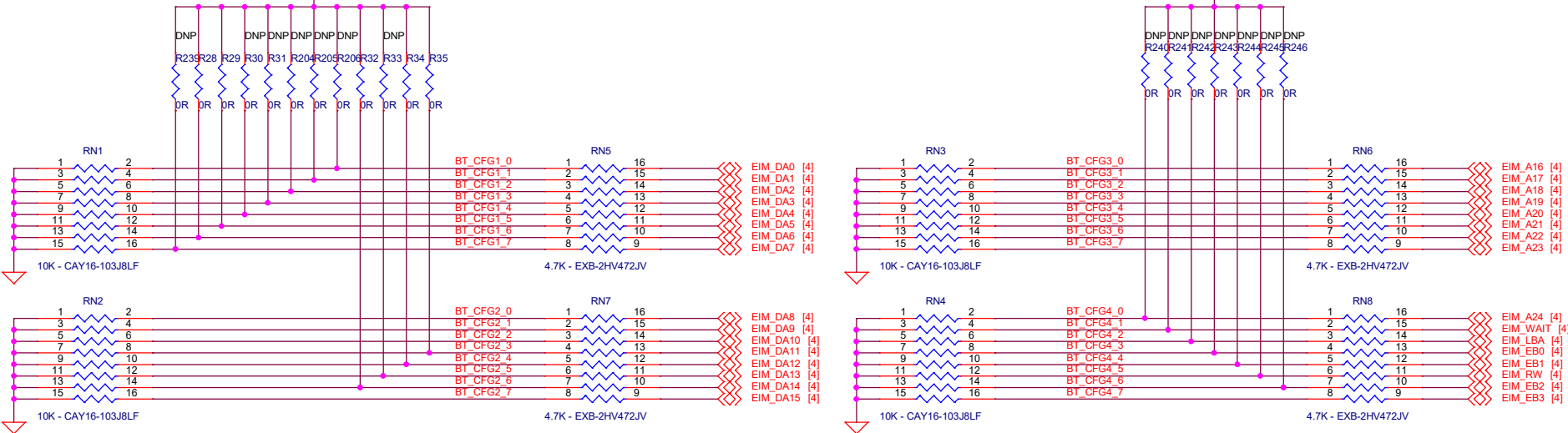


Table 8-21. Serial ROM Boot eFUSE Descriptions

Fuse	Config	Definition	GPIO	Shipped Value	Settings
BOOT_CFG1[7:4]	OEM	Boot Device Selection	Yes	0000	0011 - Boot from Serial ROM
BOOT_CFG4[6]	OEM	EEPROM Recovery Enable	Yes	0	0 - Disabled EEPROM recovery 1 - Enabled EEPROM recovery
BOOT_CFG4[5:4]	OEM	CS select (SPI only)	Yes	00	00 - ECSPiX_SS0 01 - ECSPiX_SS1 10 - ECSPiX_SS2 11 - ECSPiX_SS3
BOOT_CFG4[3]	OEM	SPI Addressing (SPI only)	Yes	0	0 - 2-bytes (16-bit) 1 - 3-bytes (24-bit)
BOOT_CFG4[2:0]	OEM	Port Select	Yes	00	000 - ECSPi-1 001 - ECSPi-2 010 - ECSPi-3 011 - ECSPi-4 100 - ECSPi-5 101- I2C1 110- I2C2 111- I2C3

Boot Select Table

8	7	6	5	4	3	2	1
BT_CFG1_7	BT_CFG1_6	BT_CFG1_5	BT_CFG1_4	BT_CFG2_6	BT_CFG2_5	BT_CFG2_4	BT_CFG2_3
011X = MMC/eMMC Boot				00 = 1-bit 01 = 4-bit 10 = 8-bit		01 = SD2 Boot 10 = SD3 Boot 11 = SD4 Boot	
010X = SD/eSD Boot				X0 = 1-bit X1 = 4-bit		00 = SD1 Boot 01 = SD2 Boot 10 = SD3 Boot 11 = SD4 Boot	
0011 = SPI Boot							

Table 8-1. Boot MODE Pin Settings

BOOT_MODE[1:0]	Boot Type
00	Boot From Fuses
01	Serial Downloader
10	Internal Boot
11	Reserved

Note: Default boot mode will be eMMC after image flash through serial downloader