

**Primary iMX6 on SN04** *This is imx refuses to boot although fuse settings are correct*

Re-Loading shadow registers by software command

eFUSE Shadow registers set by ROM boot loader

Expression	Value	Loca
gp0CDtp	0x021BC000	0x800
ctrl	0x00000005	0x021
ctrl_set	0x00000005	0x021
ctrl_clr	0x00000005	0x021
ctrl_tog	0x00000005	0x021
timing	0x03CC34B0	0x021
Unused_1	<array>	0x021
data	0x00000000	0x021
Unused_2	<array>	0x021
read_ctrl	0x00000000	0x021
Unused_3	<array>	0x021
read_fuse_data	0xAFA484A7	0x021
Unused_4	<array>	0x021
Lock	0xE6F6E677	0x021
Unused_5	<array>	0x021
cfg4	0xAFA686A7	0x021
Unused_6	<array>	0x021
cfg5	0xA6A786A6	0x021
Unused_7	<array>	0x021
cfg6	0xE6E686A6	0x021
Unused_9	<array>	0x021
mac_addr_0	0x82C3C383	0x021
Unused_10	<array>	0x021
mac_addr_1	0xC3C38382	0x021
Unused_11	<array>	0x021
mac_addr	0xC3C28383	0x021
Unused_12	<array>	0x021
gp1	0xC3C3C2C3	0x021
Unused_13	<array>	0x021
gp2	0xC3C3C3CF	0x021

Software read of fuses produces corrupt values

Expression	Value	Lo
gp0CDtp	0x021BC000	0x800
ctrl	0x00000000	0x021
ctrl_set	0x00000000	0x021
ctrl_clr	0x00000000	0x021
ctrl_tog	0x00000000	0x021
timing	0x01461299	0x021
Unused_1	<array>	0x021
data	0x00000000	0x021
Unused_2	<array>	0x021
read_ctrl	0x00000000	0x021
Unused_3	<array>	0x021
read_fuse_data	0x00000000	0x021
Unused_4	<array>	0x021
Lock	0x00328003	0x021
Unused_5	<array>	0x021
cfg4	0x0B000003	0x021
Unused_6	<array>	0x021
cfg5	0x00000010	0x021
Unused_7	<array>	0x021
cfg6	0x00000000	0x021
Unused_9	<array>	0x021
mac_addr_0	0x00000000	0x021
Unused_10	<array>	0x021
mac_addr_1	0x00000000	0x021
Unused_11	<array>	0x021
mac_addr	0x00000000	0x021
Unused_12	<array>	0x021
gp1	0x00000000	0x021
Unused_13	<array>	0x021
gp2	0x0000000C	0x021

Who did this?

Boot fuses are burned identically

**Secondary iMX6 on SN04**

*This is the imx that Elise burned fuses to, and it DOES boot correctly from the fuse settings.*

Re-Loading shadow registers by software command

eFUSE Shadow registers set by ROM boot loader

Expression	Value	Loca
gp0CDtp	0x021BC000	0x800
ctrl	0x00000006	0x021
ctrl_set	0x00000006	0x021
ctrl_clr	0x00000006	0x021
ctrl_tog	0x00000006	0x021
timing	0x03CC34B0	0x021
Unused_1	<array>	0x021
data	0x00000000	0x021
Unused_2	<array>	0x021
read_ctrl	0x00000000	0x021
Unused_3	<array>	0x021
read_fuse_data	0x00000010	0x021
Unused_4	<array>	0x021
Lock	0x0032800B	0x021
Unused_5	<array>	0x021
cfg4	0x0B000030	0x021
Unused_6	<array>	0x021
cfg5	0x00000010	0x021
Unused_7	<array>	0x021
cfg6	0x00000000	0x021
Unused_9	<array>	0x021
mac_addr_0	0x00000000	0x021
Unused_10	<array>	0x021
mac_addr_1	0x00000000	0x021
Unused_11	<array>	0x021
mac_addr	0x00000000	0x021
Unused_12	<array>	0x021
gp1	0x00000000	0x021
Unused_13	<array>	0x021
gp2	0x00000000	0x021

Software read works (except for one Lock bit)

Expression	Value	Locatic
gp0CDtp	0x021BC000	0x800
ctrl	0x00000000	0x021
ctrl_set	0x00000000	0x021
ctrl_clr	0x00000000	0x021
ctrl_tog	0x00000000	0x021
timing	0x01461299	0x021
Unused_1	<array>	0x021
data	0x00000000	0x021
Unused_2	<array>	0x021
read_ctrl	0x00000000	0x021
Unused_3	<array>	0x021
read_fuse_data	0x00000000	0x021
Unused_4	<array>	0x021
Lock	0x00328003	0x021
Unused_5	<array>	0x021
cfg4	0x0B000030	0x021
Unused_6	<array>	0x021
cfg5	0x00000010	0x021
Unused_7	<array>	0x021
cfg6	0x00000000	0x021
Unused_9	<array>	0x021
mac_addr_0	0x00000000	0x021
Unused_10	<array>	0x021
mac_addr_1	0x00000000	0x021
Unused_11	<array>	0x021
mac_addr	0x00000000	0x021
Unused_12	<array>	0x021
gp1	0x00000000	0x021
Unused_13	<array>	0x021
gp2	0x00000000	0x021