

# How to Use an Older Uboot version with 3.1x.xx Kernel Version.

## About this document

This document describe the setup detail to learn how to install a new kernel version which are device tree dependable with an older Uboot version.

## Software versions

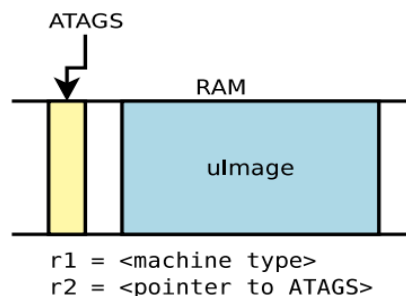
The contents of this document are valid for the latest versions of i.MX6 BSP and older Uboot version than 2014.4v by the time of writing, listed below:

- Uboot 2009
- L3.14.38\_6qp\_ga

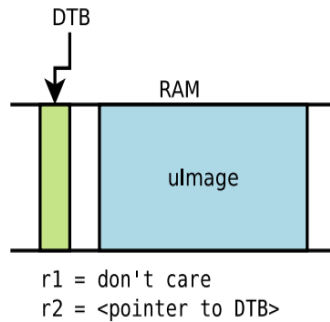
## 1. Introduction

Before The device tree the normal kernel booting process was:

1. Kernel containing the description of the hardware
2. Single Binary file (ulmage)
3. Bootloader prepares additional information ATAGS (KERNEL command line).



5. Since kernel version 3.1x and up, device tree is a must for linux embedded devices, where kernel does not contain any hardware description. The hardware description is contained in a different binary file, the device tree blob. Bootloader load two binaries:



## 2. Bootloader DTB Compatible

Not all the bootloaders support the DTB loading process (old Uboot versions), for these cases Kernel configuration changes to ease transition:

CONFIG\_ARM\_APPENDED\_DTB

- The kernel looks for the dtb after the kernel image.
- Developer must add the dtb to your kernel image.

CONFIG\_ARM\_ATAG\_DTB\_COMPAT

- Kernel reads ATAGS from Bootloader and patches device tree with them.

```
cat arch/arm/boot/zImage arch/arm/boot/dts/myboard.dtb > my-zImage
mkimage ... -d my-zImage my-uImage
```

## 3. Setup Examples

In this example we are going to show how to use Uboot 2009 with latest BSP i.MX6Q supported kernel 3.14.38

1. Setup the toolchain:

```
$ cd ~/
```

## Freescale Semiconductor

```
$ wget -c
https://releases.linaro.org/14.09/components/toolchain/binaries/gcc-
linaro-arm-linux-gnueabi-hf-4.9-2014.09\_linux.tar.xz
$ tar xf gcc-linaro-arm-linux-gnueabi-hf-4.9-2014.09_linux.tar.xz
$ export ARCH=arm
$ export CROSS_COMPILE=gcc-linaro-arm-linux-gnueabi-hf-4.9-
2014.09_linux/bin/arm-linux-gnueabi-hf-
$ unset LDFLAGS
```

For these instructions, we are assuming: DISK=/dev/sdg, cat /proc/partitions is very useful for determining the device id.

```
$ export DISK=/dev/sdg
```

2. Download Uboot 2009 and compile it, and load it to SD:

```
$ git clone http://git.freescale.com/git/cgit.cgi/imx/uboot-imx.git -b
imx_v2009.08_3.0.35_4.1.0
$ export ARCH=arm
$ export CROSS_COMPILE= $TARGET_PREFIX
$ make <board>_config
$ make

Erase uSD/SD card:
$ sudo dd if=/dev/zero of=${DISK} bs=1M count=10

Install the bootloader:
$ sudo dd if=uboot.bin of=${DISK} && sync
```

3. Configure Linux Kernel (In this example, L3.14.38 )

```
$ cd ~/
$ wget -c http://git.freescale.com/git/cgit.cgi/imx/linux-2.6-
imx.git/snapshot/linux-2.6-imx-rel\_imx\_3.14.38\_6qp\_ga.tar.gz
```

HOW TO USE AN OLDER UBOOT VERSION WITH 3.1x.xx KERNEL VERSION

## Freescle Semiconductor

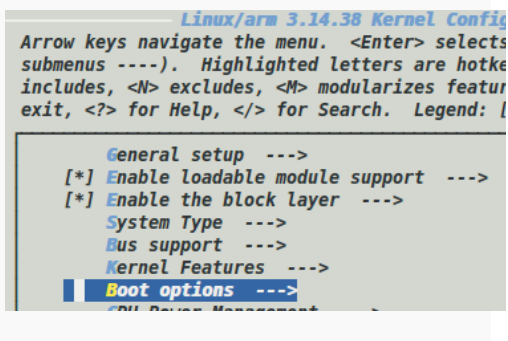
```
$ tar xf linux-2.6-imx-rel_imx_3.14.38_6qp_ga.tar.gz
$ cd linux-2.6-imx-rel_imx_3.14.38_6qp_ga

$ make ARCH=arm CROSS_COMPILE=../gcc-linaro-arm-linux-gnueabi-hf-4.9-
2014.09_linux/bin/arm-linux-gnueabi-hf- imx_v7_defconfig

$ make ARCH=arm CROSS_COMPILE=../gcc-linaro-arm-linux-gnueabi-hf-4.9-
2014.09_linux/bin/arm-linux-gnueabi-hf- menuconfig

For CONFIG_ARM_APPENDED_DTB, Enable:
-> Boot options
    -> Use appended device tree blob to zImage

for CONFIG_ARM_ATAG_DTB_COMPAT
-> Boot options
    -> Use appended device tree blob to zImage (EXPERIMENTAL)
        -> Supplement the appended DTB with traditional ATAG information
```



```
[*] Use appended device tree blob to zImage (EXPERIMENTAL)
[*] Supplement the appended DTB with traditional ATAG information
    Kernel command line type (Use bootloader kernel arguments)
    (noinitrd console=ttyMXC0,115200) Default kernel command string
    Kernel command line type (Use bootloader kernel arguments)
```

#### 4. Create ulmage:

```
$ make ARCH=arm CROSS_COMPILE=../gcc-linaro-arm-linux-gnueabi-hf-4.9-
2014.09_linux/bin/arm-linux-gnueabi-hf- -j4 zImage

$ make ARCH=arm CROSS_COMPILE=../gcc-linaro-arm-linux-gnueabi-hf-4.9-
2014.09_linux/bin/arm-linux-gnueabi-hf- dtbs
```

## Freescle Semiconductor

```
$ cat arch/arm/boot/zImage arch/arm/boot/dts/imx6q-sabresd.dtb > zImage
$ mkimage -A arm -O linux -T kernel -C none -a 0x10800000 -e 0x10800000
-n "Linux Kernel" -d zImage uImage
```

### 5. Create your SD system partitions:

```
sudo fdisk ${DISK}
```

- d ///delete all partitions currently on sd
- n // create new partition
- p // Primary partition
- 1 // partition number 1
- 2048 //default
- +1G //
- N // created 2d parition
- p
- 2
- default
- default
- t //change partition t
- 1 // firts
- B // to be fat32
- W // write partiotions

### 6. Create partition types:

```
$ sudo mkfs.vfat -n KERNEL ${DISK}1
$ sudo mkfs.ext3 ${DISK}2
```

### 7. Mount the sd partitions on your host

```
$ sudo mount ${DISK}1 /"mount directory"
```

### 8. Copy the ulmage to a FAT partition of the SD

## Freescale Semiconductor

```
$ sudo cp uImage /"mount directory"
```

9. Umount the SD form the host

```
$ sudo mount /"mount directory"
```

10. Insert the eSD and boot up the board. Stop in Uboot menu.
11. Check your mmc device (for this case mmcdev=2)
12. Load the ulmage to memory and boot the Linux kernel

```
$ fatload mmc 2:1 0x10008000 uImage
```

```
$ bootm 0x10008000
```

You should get the next output:

```
MX6Q SABRESD U-Boot > fatload mmc 2:1 0x10008000 uImage
reading uImage
6056174 bytes read
MX6Q SABRESD U-Boot > bootm 0x10008000
## Booting kernel from Legacy Image at 10008000 ...
Image Name: Linux Kernel
Image Type: ARM Linux Kernel Image (uncompressed)
Data Size: 6056110 Bytes = 5.8 MB
Load Address: 10800000
Entry Point: 10800000
Verifying Checksum ... OK
Loading Kernel Image ... OK
OK
Starting kernel ...

Booting Linux on physical CPU 0x0
Linux version 3.14.38 (usuario@ubuntu) (gcc version 4.9.2 20140904 (prerelease) <
CPU: ARMv7 Processor [412fc09a] revision 10 (ARMv7), cr=10c53c7d
CPU: PIPT / UIPT nonaliasing data cache, UIPT aliasing instruction cache
Machine model: Freescale i.MX6 Quad SABRE Smart Device Board
cma: CMA: reserved 320 MiB at 3c000000
Memory policy: Data cache writealloc
PERCPU: Embedded 8 pages/cpu @ab723000 s8320 r8192 d16256 u32768
Built 1 zonelists in Zone order, mobility grouping on. Total pages: 260096
Kernel command line: noinitrd console=ttyMXC0,115200
PID hash table entries: 4096 (order: 2, 16384 bytes)
Dentry cache hash table entries: 131072 (order: 7, 524288 bytes)
Inode-cache hash table entries: 65536 (order: 6, 262144 bytes)
Memory: 699648K/1048576K available (7460K kernel code, 456K rodata, 2584K rodata,
Virtual kernel memory layout:
vector : 0xffff0000 - 0xffff1000 ( 4 kB)
fixmap : 0xffff0000 - 0xfffe0000 ( 896 kB)
umalloc : 0xc0000000 - 0xff000000 (1000 MB)
lowmem : 0x80000000 - 0xc0000000 (1024 MB)
pkmap : 0x7fe00000 - 0x80000000 ( 2 MB)
modules : 0x7f000000 - 0x7fe00000 ( 14 MB)
.text : 0x80000000 - 0x809d72c4 (10045 kB)
.init : 0x809d8000 - 0x80a38000 ( 385 kB)
.data : 0x80a3a000 - 0x80aac0c0 ( 457 kB)
.bss : 0x80aac0cc - 0x80b1825c ( 433 kB)
STMP: HwLib=64, Order=0, Msp0=0x00000000, Cpu=0, Node=1
```