

GPIO USB ID

BIYONG SUN
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Introduction

This document mainly introduces how to use gpio usb id.
This can provide more options to avoid pin conflicts.



Case Introduction

iMX93 11x11 evk uses a peripheral circuit built with a typeC chip to perform USB role switch.

However, in many cases, the hardware design does not have the same typeC circuit as the evk, and the USB ID is needed to do switch.

The two USB IDs of the current iMX93 are muxed with the eQOS pins.

The probability of this conflict is very high.

So we need to use an alternative solution “gpio usb id” to avoid this pin allocation conflict.

MX93_PAD_ENET1_MDC__HSIOMIX_OTG_ID1

MX93_PAD_ENET1_MDC__ENET_QOS_MDC

MX93_PAD_ENET1_TD3__HSIOMIX_OTG_ID2

MX93_PAD_ENET1_TD3__ENET_QOS_RGMII_TD3

Linux Drive and Device Tree Bindings

Currently, there is a solutions in Linux that can use USB GPIO extcon as USB ID for role switching.

USB GPIO extcon

Kernel Configuration:

`CONFIG_EXTCON_USB_GPIO`

Binding:

`linux/Documentation/devicetree/bindings/extcon/
extcon-usb-gpio.txt`



Device Tree

Based on lf-6.6.52-2.2.0

imx93-11x11-evk-no-typec-gpio-usb-b-conn.dts --- 2 x gpio-usb-b-connector

Remove ptn5110

```
&ipi2c3 {  
    /delete-node/ tcpc@50;  
    /delete-node/ tcpc@51;  
};
```

/delete-node/ tcpc@50 and /delete-node/ tcpc@51 are used to remove the functions of type-C including role switching. At this time, the two ptn5110 only provide internal power to the vbus of usb1 and usb2. There is no external power supply through vbus as host to outside such as usb memory stick, etc.

imx93-11x11-evk-no-typec-gpio-usbid-extcon.dts

```
// SPDX-License-Identifier: (GPL-2.0+ OR MIT)
/*
 * Copyright 2019-2020 NXP
 */
/dts-v1/;

#include "imx93-11x11-evk.dts"

/*
 * A special USB cable made to power VBUS externally for testing.
 * usb1 id: J1001 pin 3(GPIO_IO02)
 * usb2 id: J1001 pin 5(GPIO_IO03)
 */
/ {
    extcon_usb1: extcon1@1 {
        compatible = "linux,extcon-usb-gpio";
        pinctrl-names = "default";
        pinctrl-0 = <&pinctrl_usb_1_id>;
        id-gpios = <&gpio2 2 GPIO_ACTIVE_HIGH>;
    };

    extcon_usb2: extcon2@2 {
        compatible = "linux,extcon-usb-gpio";
        pinctrl-names = "default";
        pinctrl-0 = <&pinctrl_usb_2_id>;
        id-gpios = <&gpio2 3 GPIO_ACTIVE_HIGH>;
    };
};
```

```
&ipi2c3 {
    /delete-node/ tpc@50;
    /delete-node/ tpc@51;
};

&usbotg1 {
    extcon = <0>, <&extcon_usb1>;
    dr_mode = "otg";
    /delete-property/ usb-role-switch;
    /delete-node/ port;
};

&usbotg2 {
    extcon = <0>, <&extcon_usb2>;
    dr_mode = "otg";
    /delete-property/ usb-role-switch;
    /delete-node/ port;
};

&iomuxc {
    pinctrl_usb_1_id: pinctrl_usb_1_id_grp {
        fsl,pins = <
                                MX93_PAD_GPIO_IO02__GPIO2_IO02 0x31e
                                >;
    };

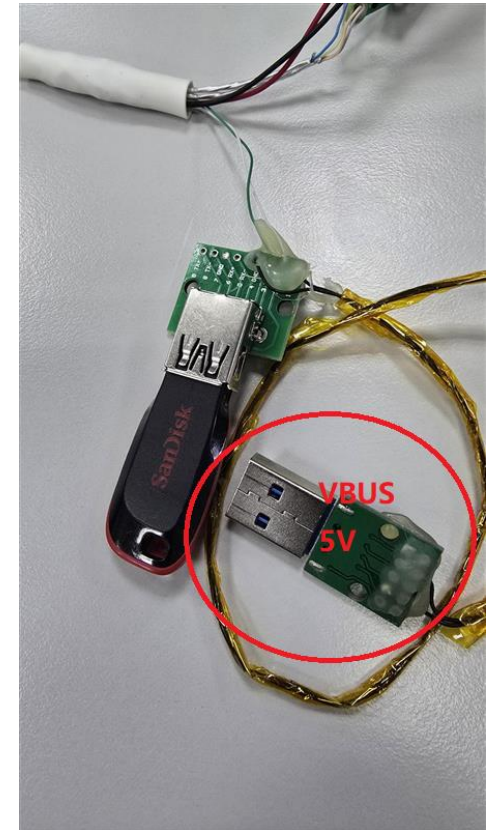
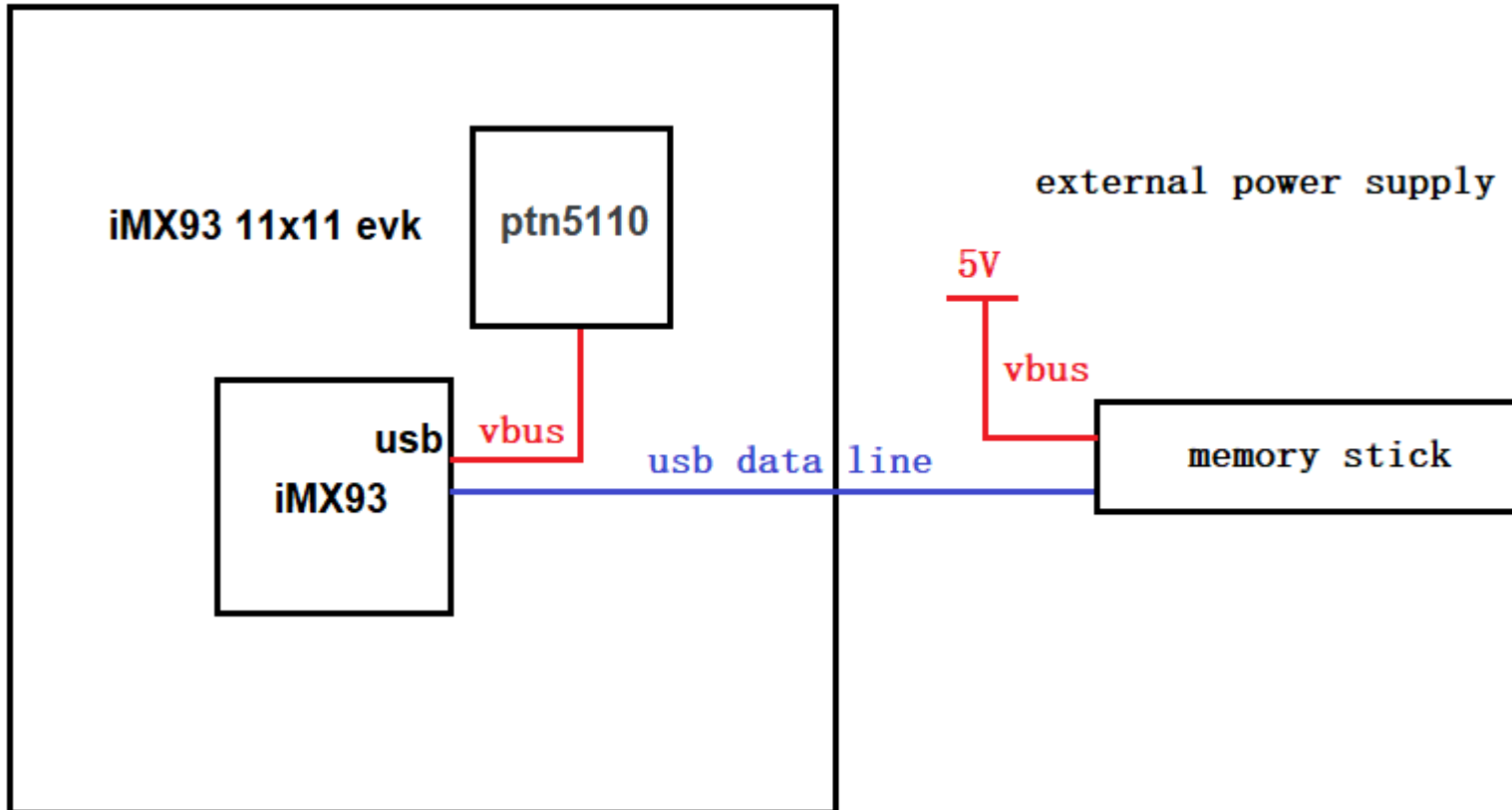
    pinctrl_usb_2_id: pinctrl_usb_2_id_grp {
        fsl,pins = <
                                MX93_PAD_GPIO_IO03__GPIO2_IO03 0x31e
                                >;
    };
};
```

Testing extcon-usb-gpio

Because the ptn5110 node has been removed from the device tree i2c. Linux no longer drives ptn5110. The ptn5110 only has the default function after power-on, which only powers the iMX93 usb vbus.

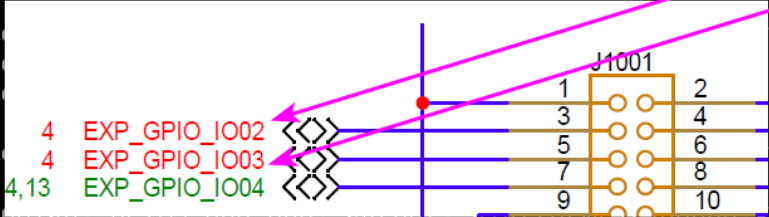
When testing the host function, a special usb cable is required, which can use an external power supply to power the memory stick vbus.

This is also done to avoid reworking the hardware on the iMX93 11x11 evk.



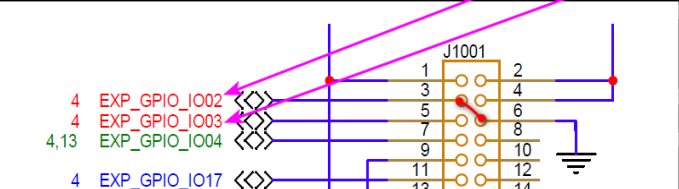
Testing extcon-usb-gpio(cont.)

```
root@imx93evk:~# cat /sys/devices/platform/soc@0/4c100000.usb/ci_hdrc.0/role
gadget usb1 device
root@imx93evk:~# cat /sys/devices/platform/soc@0/4c200000.usb/ci_hdrc.1/role
gadget usb2 device
root@imx93evk:~# cat /sys/kernel/debug/gpio
gpiochip0: GPIOs 512-543, parent: platform/43810000.gpio, 43810000.gpio:
 gpio-514 (          |id          ) in  hi IRQ
 gpio-515 (          |id          ) in  hi IRQ
...
gpiochip4: GPIOs 640-663, parent: i2c/1-0022, 1-0022, can sleep:
 gpio-644 (          |id          ) in  hi IRQ
```



The diagram shows a 14-pin connector J1001. Pin 3 is connected to EXP_GPIO_IO02, pin 5 to EXP_GPIO_IO03, and pin 7 to EXP_GPIO_IO04. Pin 6 is connected to ground. Other pins are also connected to various components.

```
root@imx93evk:~# cat /sys/devices/platform/soc@0/4c100000.usb/ci_hdrc.0/role
host usb1 host
root@imx93evk:~# cat /sys/devices/platform/soc@0/4c200000.usb/ci_hdrc.1/role
gadget usb2 device
root@imx93evk:~# cat /sys/kernel/debug/gpio
gpiochip0: GPIOs 512-543, parent: platform/43810000.gpio, 43810000.gpio:
 gpio-514 (          |id          ) in  lo IRQ
 gpio-515 (          |id          ) in  hi IRQ
...
gpiochip4: GPIOs 640-663, parent: i2c/1-0022, 1-0022, can sleep:
 gpio-644 (          |id          ) in  hi IRQ
 gpio-652 (          |id          ) out hi ACTIVE LOW
 gpio-653 (          |id          ) out hi
 gpio-654 (          |id          ) out lo
 gpio-669 (          |id          ) out hi ACTIVE LOW
```



The diagram shows a 14-pin connector J1001. Pin 3 is connected to EXP_GPIO_IO02, pin 5 to EXP_GPIO_IO03, and pin 7 to EXP_GPIO_IO04. Pin 6 is connected to ground. Other pins are also connected to various components.

```
root@imx93evk:~# ls /run/media/
boot-mmcb1k0p1 sda
root@imx93evk:~# [ 500.406556] usb 1-1: new high-speed USB device number 3 using ci_hdrc
[ 500.570811] usb-storage 1-1:1.0: USB Mass Storage device detected
[ 500.579214] scsi host0: usb-storage 1-1:1.0
[ 501.603695] scsi 0:0:0:0: Direct-Access   SanDisk  Cruzer Blade    1.00 PQ: 0 ANSI: 6
[ 501.626139] sd 0:0:0:0: [sda] 30031872 512-byte logical blocks: (15.4 GB/14.3 GiB)
[ 501.634976] sd 0:0:0:0: [sda] Write Protect is off
[ 501.639801] sd 0:0:0:0: [sda] Mode Sense: 43 00 00 00
[ 501.646327] sd 0:0:0:0: [sda] Write cache: disabled, read cache: enabled, doesn't support DPO or FUA
[ 501.662808] sda:
[ 501.665426] sd 0:0:0:0: [sda] Attached SCSI removable disk
[ 503.010712] sda:
```

When usb1 id (J1001, pin 3, GPIO_IO02) is shorted to ground(J1001, pin 6), usb1 switches to host mode.

You can see that the usb memory stick is mounted by linux.



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