

Bulletin for SABRE Automotive Infotainment Touch Interrupt i.MX 6Quad and i.MX 6DualLite CPU Cards

by: Freescale Applications Engineering

This bulletin applies to orange CPU Cards with these part numbers:

- MCIMX6QAICPU1
- MCIMX6DLAICPU1

The cards have the following identifying stickers, respectively:

- SCH-27142 REV C
- SCH-27767 REV A

Issue addressed is a touch interrupt path error.

Contents

1 Touch Interrupt Path Error	2
------------------------------------	---



1 Touch Interrupt Path Error

1.1 Identification of Boards Requiring Field Rework

Rework is only needed for the few CPU Cards that do NOT have a TDA4451 sticker on the top side. For example, rework was completed on the board pictured below before shipping.



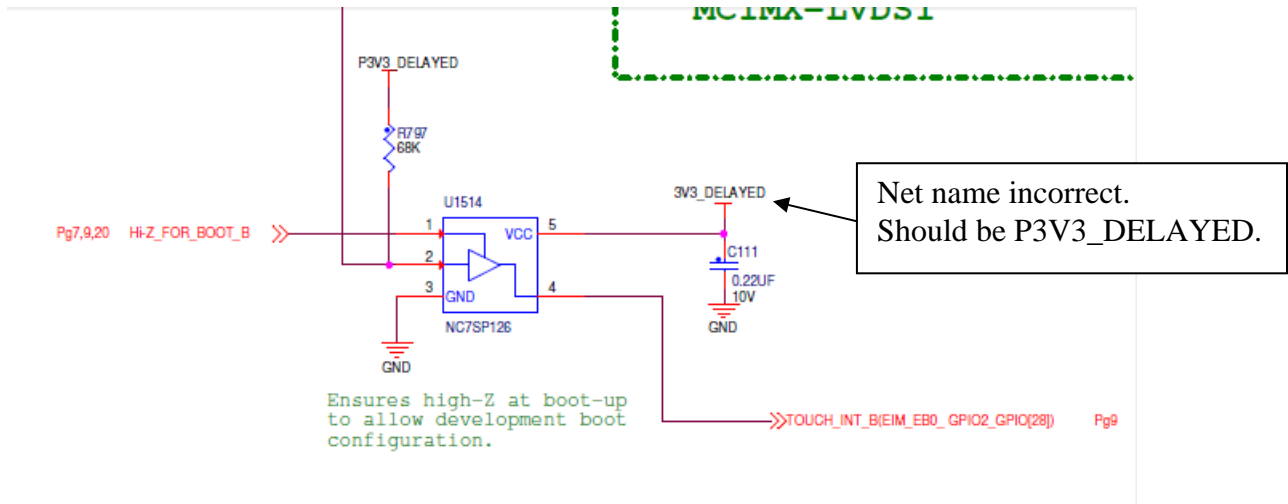
1.2 Defect Description

The processor never receives the LVDS touch panel interrupt. A buffer has its power pin unconnected.

1.3 Resolution

Connect interrupt buffer U1514 pin 5 to power rail P3V3_DELAYED.

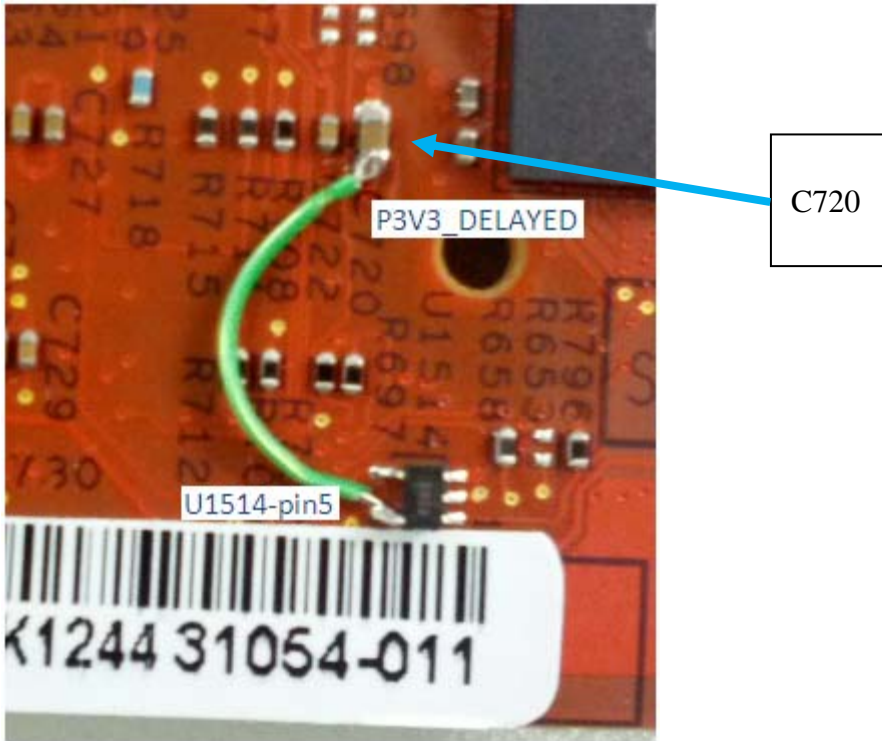
1.4 Schematic Detail



1.5 Board Rework Detail

Rework location is on the bottom of the board near the serial number sticker, as shown. Add the one wire.

Boards with a TDA4451 sticker already have the rework with a very small gauge red or orange wire.



Revision History

0

Revision History	
Revision	Description
1	Removed <i>PMIC Backfeed</i> rework because not implemented on the board. Issued separate document on iMX Community.

NOTES

How to Reach Us:

Home Page:

freescale.com

Web Support:

freescale.com/support

Information in this document is provided solely to enable system and software implementers to use Freescale products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document.

Freescale reserves the right to make changes without further notice to any products herein. Freescale makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. Freescale does not convey any license under its patent rights nor the rights of others. Freescale sells products pursuant to standard terms and conditions of sale, which can be found at the following address: freescale.com/SalesTermsandConditions.

Freescale, the Freescale logo, AltiVec, C-5, CodeTest, CodeWarrior, ColdFire, C-Ware, Energy Efficient Solutions logo, Kinetis, mobileGT, PowerQUICC, Processor Expert, QorIQ, Qorivva, StarCore, Symphony, and VortiQa are trademarks of Freescale Semiconductor, Inc., Reg. U.S. Pat. & Tm. Off. Airfast, BeeKit, BeeStack, ColdFire+, CoreNet, Flexis, MagniV, MXC, Platform in a Package, QorIQ Qonverge, QUICC Engine, Ready Play, SafeAssure, SMARTMOS, TurboLink, Vybrid, and Xtrinsic are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. The Power Architecture and Power.org word marks and the Power and Power.org logos and related marks are trademarks and service marks licensed by Power.org. The ARM Powered Logo is a trademark of ARM Limited.

© Freescale Semiconductor, Inc. 2012. All rights reserved.

Note that this is the short version of the ARM copyright statement. If ARM is mentioned in the document, use the following ARM copyright statement: ARM is the registered trademark of ARM Limited. ARMnnn is the trademark of ARM Limited.