Bulletin for SABRE Automotive Infotainment Touch Interrupt

i.MX 6Quad and i.MX 6DualLite CPU Cards

by: Freescale Applications Engineering

This bulletin applies to orange CPU Cards with these part numbers:

- MCIMX6QAICPU1
- MCIMX6DLAICPU1

The cards have the following identifying stickers, respectively:

- SCH-27142 REV C
- SCH-27767 REV A

Issue addressed is a touch interrupt path error.

Contents

1 Touch Interrupt Path Error

1.1 Identification of Boards Requiring Field Rework

Rework is only needed for the few CPU Cards that do NOT have a TDA4451 sticker on the top side. For example, rework was completed on the board pictured below before shipping.
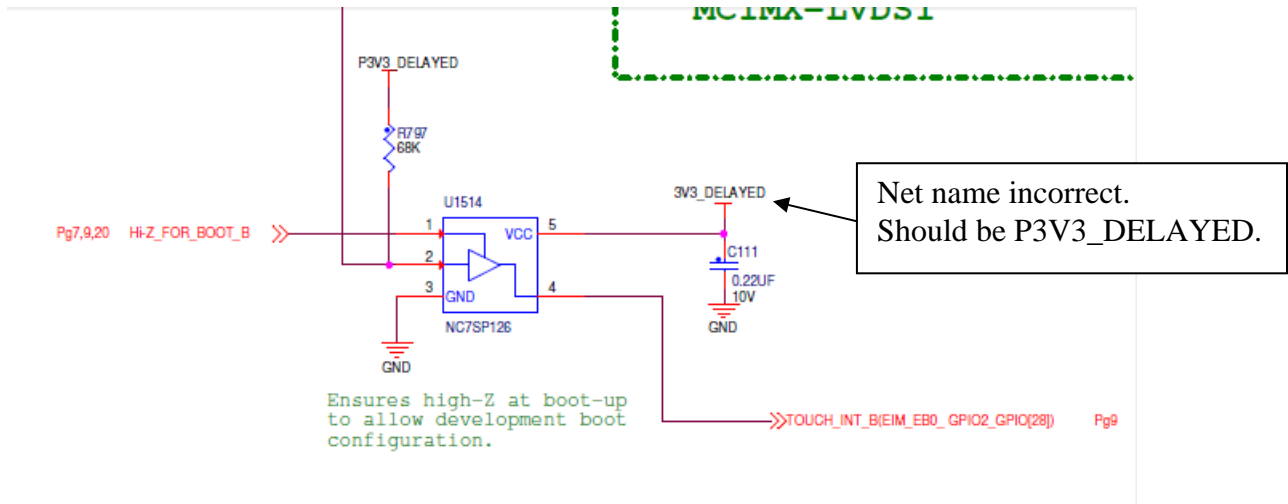

1.2 Defect Description

The processor never receives the LVDS touch panel interrupt. A buffer has its power pin unconnected.

1.3 Resolution

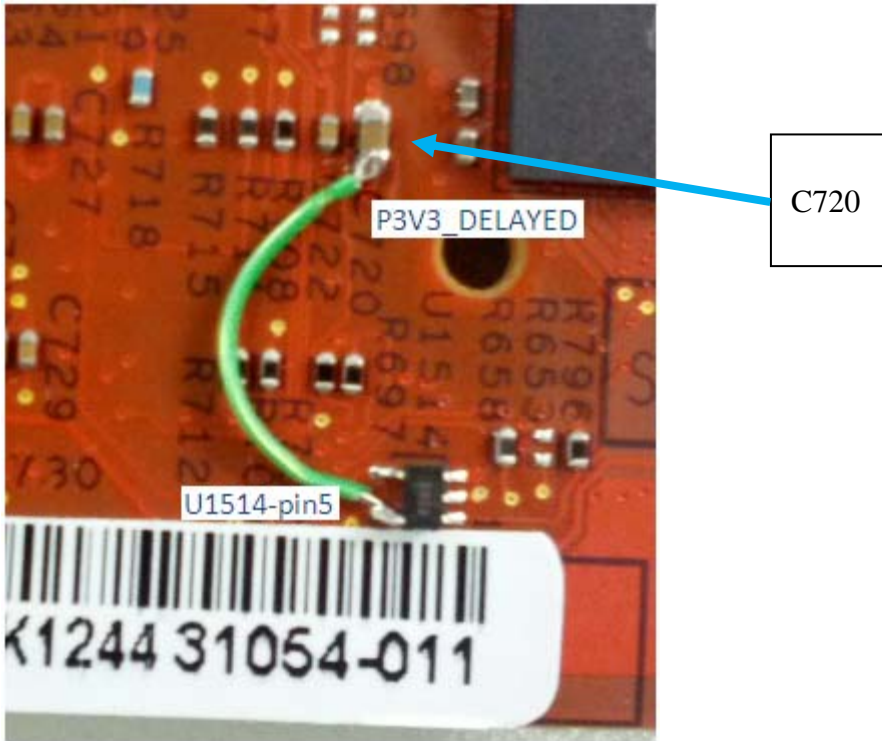Connect interrupt buffer U1514 pin 5 to power rail P3V3_DELAYED.

1.4   Schematic Detail

MC1MX-LVDS1

P3V3_DELAYED

R797
68K

Pg7,9,20   Hi-Z_FOR_BOOT_B

U1514

1		5
VCC		
2		
3		4
GND		

NC7SP126

GND

3V3_DELAYED

C111
0.22UF
10V

GND

Net name incorrect.
Should be P3V3_DELAYED.

Ensures high-Z at boot-up
to allow development boot
configuration.

TOUCH_INT_B(EIM_EB0_ GPIO2_GPIO[28])   Pg9

1.5 Board Rework Detail

Rework location is on the bottom of the board near the serial number sticker, as shown. Add the one wire.

Boards with a TDA4451 sticker already have the rework with a very small gauge red or orange wire.

Revision History

0

Revision History	
Revision	**Description**
1	Removed *PMIC Backfeed* rework because not implemented on the board. Issued separate document on iMX Community.

NOTES

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